**Vishay Semiconductors** 

High Performance Schottky Rectifier, 1 A



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DO-214AC (SMA)

PRODUCT SUMMARY				
Package	DO-214AC (SMA)			
I <sub>F(AV)</sub>	1 A			
V <sub>R</sub>	100 V			
V <sub>F</sub> at I <sub>F</sub>	0.63 V			
I <sub>RM</sub>	1 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Single die			
E <sub>AS</sub>	1.0 mJ			

#### **FEATURES**

Low forward voltage drop



FREE

- Guard ring for enhanced ruggedness and long **RoHS** term reliability COMPLIANT HALOGEN
- · Small foot print, surface mountable
- · High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

The VS-10MQ100-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	1	А		
V <sub>RRM</sub>		100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	120	А		
V <sub>F</sub>	1.5 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.68	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-10MQ100-M3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	100	N/
Maximum working peak reverse voltage	V <sub>RWM</sub>		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at $T_L = 126$ °C On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad are		1.5	
See fig. 4	IF(AV)	$I_{F(AV)}$ 50 % duty cycle at $T_L = 135$ °C, rectangu On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1	A
Maximum peak one cycle non-repetitive surge current, $T_1 = 25 \ ^{\circ}C$		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	120	
See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse		30	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.5 A, L = 8 mH		1.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	$\begin{array}{c} \mbox{Current decaying linearly to zero in 1 } \mu \mbox{s} \\ \mbox{Frequency limited by } T_J \mbox{ maximum } V_A = 1.5 \mbox{ x } V_R \mbox{ typical} \end{array} \begin{array}{c} 0.5 \end{array}$		0.5	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T <sub>.1</sub> = 25 °C	0.78	V
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1.5 A	1j=23 0	0.85	
See fig. 1	V FM (1)	1 A	– T <sub>.1</sub> = 125 °C	0.63	
		1.5 A	$I_{\rm J} = 125  {}^{\circ}{\rm C}$	0.68	
Maximum reverse leakage current		T <sub>J</sub> = 25 °C		0.1	mA
See fig. 2	I <sub>RM</sub>	$V_{\rm R} = \text{Rated } V_{\rm R}$	$v_{\rm R} = Raled v_{\rm R}$	1	mA
Threshold voltage	V <sub>F(TO)</sub>	$T_{\rm J} = T_{\rm J} \text{ maximum} \qquad \qquad$		0.52	V
Forward slope resistance	r <sub>t</sub>			mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}$ , $T_J = 25 \text{ °C}$ , test signal = 1 MHz 38		pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs		V/µs	

#### Note

<sup>(1)</sup> Pulse width = 300  $\mu$ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approvimete weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (similar D-64)	1,	J

#### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink



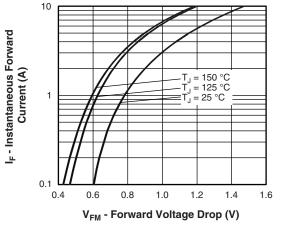
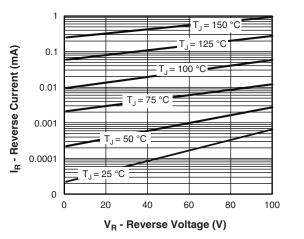
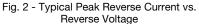


Fig. 1 - Maximum Forward Voltage Drop Characteristics





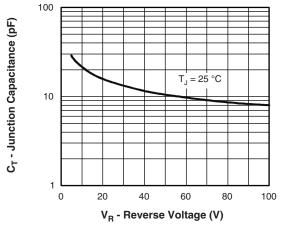


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

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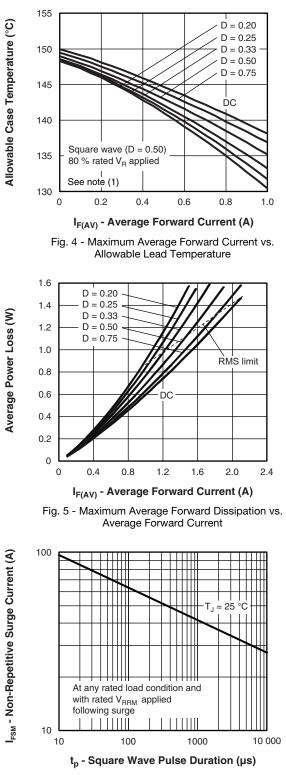


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

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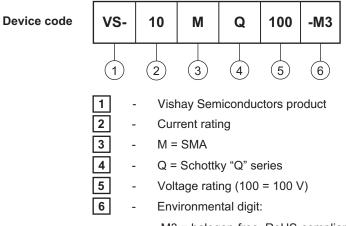
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# VS-10MQ100-M3





### **ORDERING INFORMATION TABLE**



-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-10MQ100-M3/5AT	5AT	7500	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95400			
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		



# **Outline Dimensions**

## **Vishay Semiconductors**

SMA

### **DIMENSIONS** in inches (millimeters)

DO-214AC (SMA)





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