VS-10TQ035-M3, VS-10TQ040-M3, VS-10TQ045-M3

**Vishay Semiconductors** 

## High Performance Schottky Rectifier, 10 A



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PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	10 A				
V <sub>R</sub>	35 V, 40 V, 45 V				
V <sub>F</sub> at I <sub>F</sub>	0.49 V				
I <sub>RM</sub>	15 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
E <sub>AS</sub>	13 mJ				
Package	2L TO-220AC				
Circuit configuration	Single				

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### DESCRIPTION

The VS-10TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL CHARACTERISTICS VALUES UN						
I <sub>F(AV)</sub>	Rectangular waveform	10	А			
V <sub>RRM</sub>		35 to 45	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1050	А			
V <sub>F</sub>	10 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.49	V			
TJ	Range	-55 to +175	°C			

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-10TQ035-M3	VS-10TQ040-M3	VS-10TQ045-M3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	35	40	45	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	55	40	45	v		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS		
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 151 °C	10				
Maximum peak one cycle non-repetitive surge current	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	or 3 µs rect. pulse Following any rated load condition and with rated		А		
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	280			
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 2 A, L = 6.5 mH		13	mJ		
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		2	А		

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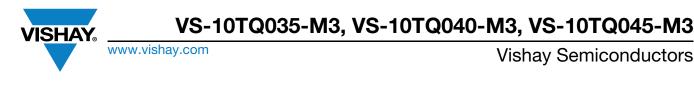
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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		10 A	T 05 %0	0.57	V	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	20 A	T <sub>J</sub> = 25 °C	0.67		
See fig. 1	VFM (")	10 A	T 105 %O	0.49		
		20 A	T <sub>J</sub> = 125 °C	0.61		
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		2	٣٨	
See fig. 2		T <sub>J</sub> = 125 °C	$V_R = Rated V_R$	15	mA	
Maximum junction capacitance	C <sub>T</sub>	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS	
Maximum junction and storage temperature rar	ge	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	
Maximum thermal resist junction to case	Maximum thermal resistance, junction to case		DC operation See fig. 4	2.0	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth, and greased	0.50	0/10	
Approvimate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque maximum				12 (10)	(lbf ∙ in)	
Marking device			Case style 2L TO-220AC	10TQ035		
			Case signe 22 10-220AC	10TC	10TQ045	



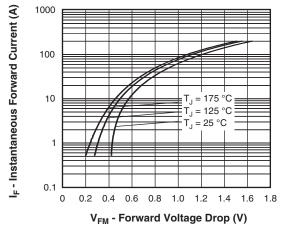


Fig. 1 - Maximum Forward Voltage Drop Characteristics

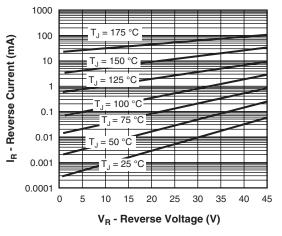


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

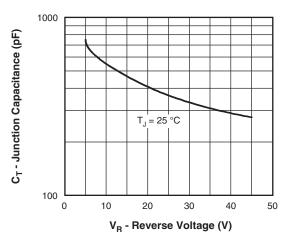


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

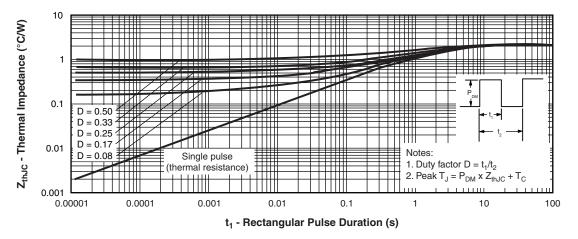
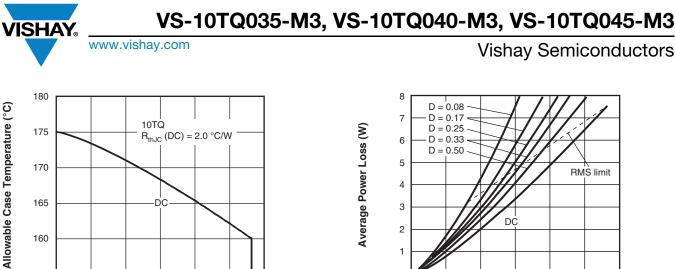


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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10

15

5

DC

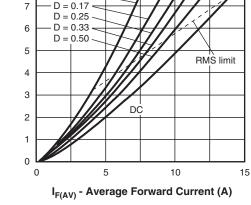
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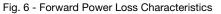
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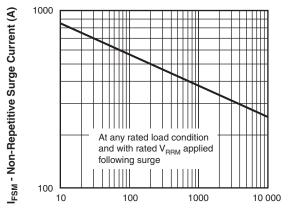
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155

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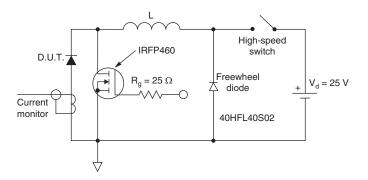






t<sub>p</sub> - Square Wave Pulse Duration (μs)







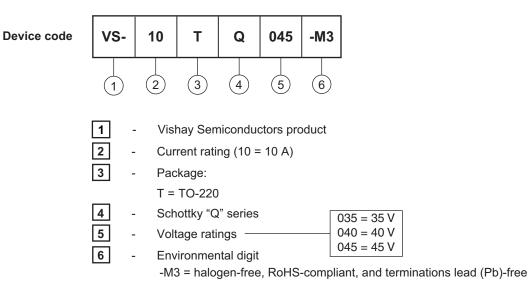
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### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-10TQ035-M3	50	1000	Antistatic plastic tube				
VS-10TQ040-M3	50	1000	Antistatic plastic tube				
VS-10TQ045-M3	50	1000	Antistatic plastic tube				

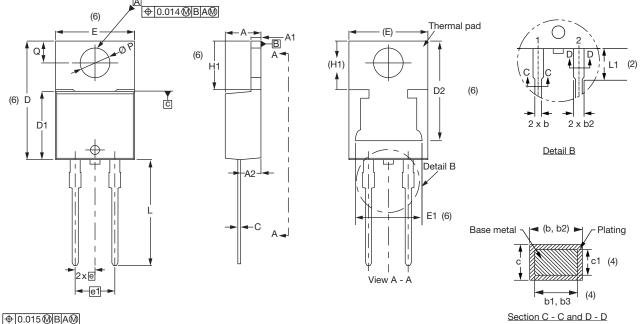
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?96156					
Part marking information	www.vishay.com/doc?95391				

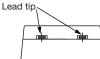


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### 2L TO-220AC

#### **DIMENSIONS** in millimeters and inches





SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

Conforms to JEDEC®	outline	TO-220AC
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SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension and finish uncontrolled in L1

<sup>(3)</sup> Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Dimension b1, b3, and c1 apply to base metal only

(5) Controlling dimensions: inches

<sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2, and E1

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> TO-220, except D2

Revision: 13-Jun-2019

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