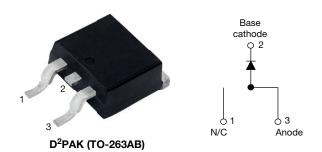


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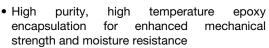
High Performance Schottky Rectifier, 15 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	15 A				
V_{R}	35 V, 40 V, 45 V				
V _F at I _F	0.50 V				
I _{RM} max.	70 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	16 mJ				
Package	D ² PAK (TO-263AB)				
Circuit configuration	Single				

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-12TQ...S-M3 Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES U							
I _{F(AV)}	Rectangular waveform	15	Α				
V _{RRM}	Range	35 to 45	V				
I _{FSM}	t _p = 5 μs sine	990	Α				
V _F	15 A _{pk} , T _J = 125 °C	0.50	V				
TJ	Range	-55 to +150	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-12TQ035S-M3	VS-12TQ040S-M3	VS-12TQ045S-M3	UNITS		
Maximum DC reverse voltage	V_R	35	40	45	W		
Maximum working peak reverse voltage	V_{RWM}	33	40	45	V		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS		
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 120 °C	15	Α			
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	990			
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	250	А		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2.4 A, L = 5.5 mH		16	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		2.4	А		

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		15 A	- T _{.1} = 25 °C	0.56		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	30 A	1j=25 C	0.71	V	
See fig. 1	V FM (1)	15 A	- T _{.I} = 125 °C	0.50	V	
		30 A	- IJ = 125 C	0.64		
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	1.75	mA	
See fig. 2	IRM ("/	T _J = 125 °C	v _R = nateu v _R	70	IIIA	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal ran	900	pF		
Typical series inductance	L _S	Measured lead to lead 5	8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 150	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.0	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	C/VV	
				2	g	
Approximate weight				0.07	OZ.	
minimum				6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device			Case style D ² PAK (TO-263AB)	12TQ 12TQ 12TQ	044S	

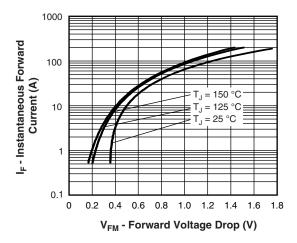


Fig. 1 - Maximum Forward Voltage Drop Characteristics

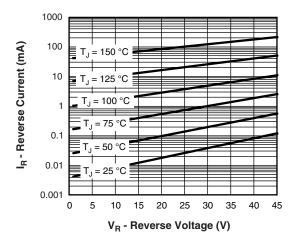


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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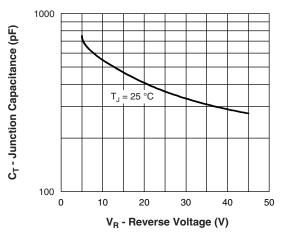


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

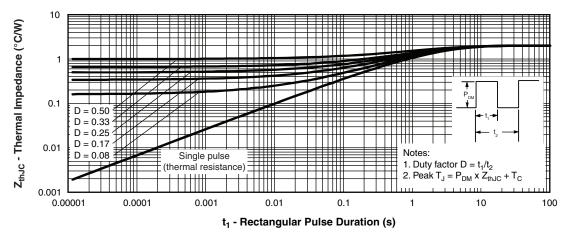


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

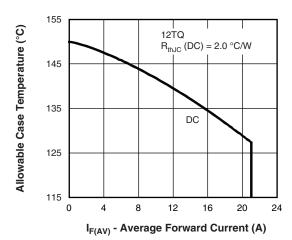


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

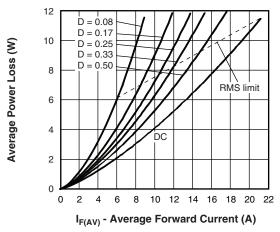
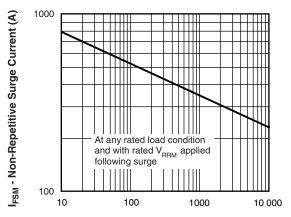


Fig. 6 - Forward Power Loss Characteristics



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

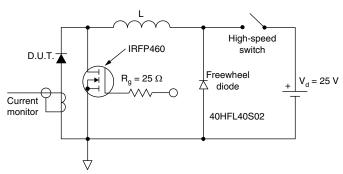
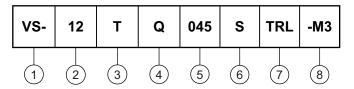


Fig. 8 - Unclamped Inductive Test Circuit

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating
- 3 Package: T = TO-220
- Schottky "Q" series 035 = 35 V 5 - Voltage ratings - 040 = 40 V
- 6 S = D²PAK (TO-263AB)
- 7 • None = tube
 - TRL = tape and reel (left oriented)
 - TRR = tape and reel (right oriented)
- 8 -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free



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ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-12TQ035S-M3	50	1000	Antistatic plastic tubes						
VS-12TQ035STRR-M3	800	800	13" diameter reel						
VS-12TQ035STRL-M3	800	800	13" diameter reel						
VS-12TQ040S-M3	50	1000	Antistatic plastic tubes						
VS-12TQ040STRR-M3	800	800	13" diameter reel						
VS-12TQ040STRL-M3	800	800	13" diameter reel						
VS-12TQ045S-M3	50	1000	Antistatic plastic tubes						
VS-12TQ045STRR-M3	800	800	13" diameter reel						
VS-12TQ045STRL-M3	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96164				
Part marking information	www.vishay.com/doc?95444				
Packaging information	www.vishay.com/doc?96424				



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

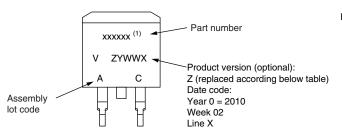
- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



Part Marking Information

Vishay Semiconductors

D²PAK



Example: This is a xxxxxx ⁽¹⁾ with assembly lot code AC, assembled on WW 02, 2010

Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z) PRODUCT DEFINITION					
A Termination lead (Pb)-free					
B Totally lead (Pb)-free					
E RoHS-compliant and termination lead (Pb)-free					
F RoHS-compliant and totally lead (Pb)-free					
M Halogen-free, RoHS-compliant, and termination lead (Pb)-free					
N Halogen-free, RoHS-compliant, and totally lead (Pb)-free					
G	Green				



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