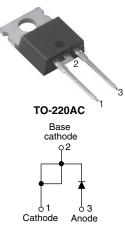
VS-15ETL06HN3



Vishay Semiconductors

Ultralow V_F Hyperfast Rectifier for Discontinuous Mode PFC, 15 A FRED Pt®



VS-15ETL06HN3

PRODUCT SUMMARY									
Package	TO-220AC								
I _{F(AV)}	15 A								
V _R	600 V								
V _F at I _F	0.85 V								
t _{rr} typ.	60 ns								
T _J max.	175 °C								
Diode variation	Single die								

FEATURES

- Hyperfast recovery time
- · Benchmark ultralow forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

State of the art, ultralow V_F, soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Peak repetitive reverse voltage	V _{RRM}		600	V						
Average rectified forward current	I _{F(AV)}	T _C = 153 °C	15							
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	250	А						
Peak repetitive forward current	I _{FM}		30							
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C						

ELECTRICAL SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$ unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-					
Forward voltage	V _F	I _F = 15 A	-	0.99	1.05	V				
		I _F = 15 A, T _J = 150 °C	-	0.85	0.92					
Poverao lookago ourrent		$V_{R} = V_{R}$ rated	-	0.1	10					
Reverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	15	120	μΑ				
Junction capacitance	CT	V _R = 600 V	-	20	-	pF				
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH				

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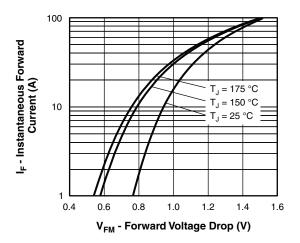


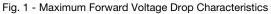
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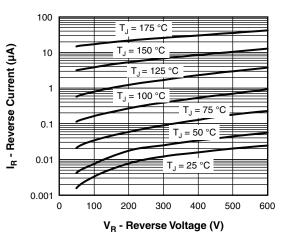
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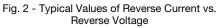
DYNAMIC RECOVERY CHARACTERISTICS ($T_C = 25$ °C unless otherwise specified)											
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS				
Reverse recovery time		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ J}$	Α/μs, V _R = 30 V	-	60	-					
	+	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 100$	-	190	-	20					
	t _{rr}	T _J = 25 °C		-	220	-	ns				
		T _J = 125 °C		-	320	-					
Pools recovery ourrent	I _{RRM}	T _J = 25 °C	I _F = 15 A dl. (dt = 200 A (up	-	19	-	А				
Peak recovery current		IRRM	T _J = 125 °C	dl _F /dt = 200 A/µs V _B = 390 V	-	26	-	A			
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	2.2	-					
		Q _{rr}	T _J = 125 °C		-	4.3	-	μC			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C				
Thermal resistance, junction to case	R _{thJC}		-	1.1	1.4					
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W				
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-					
Wainht			-	2.0	-	g				
Weight			-	0.07	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style TO-220AC		15ET	L06H					













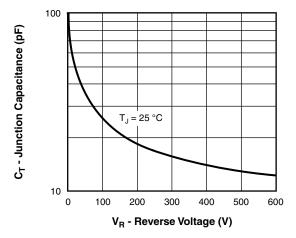


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

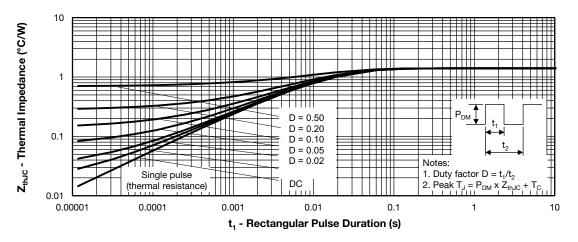
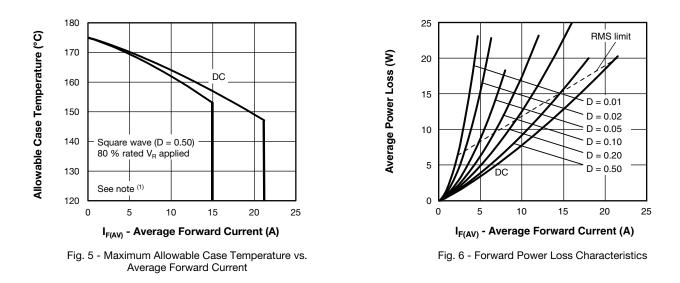


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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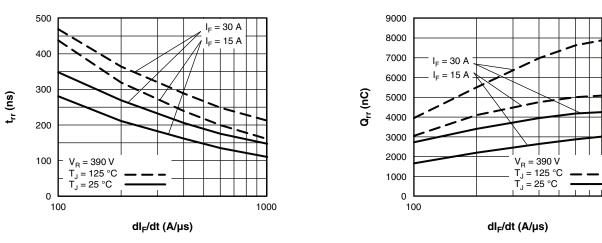


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

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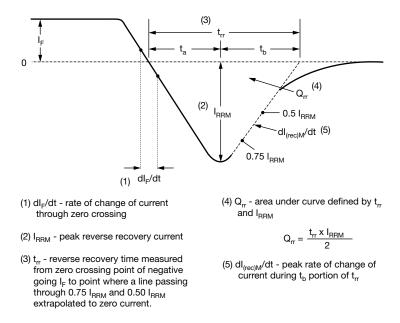


Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code	vs-	15	Е	т	L	06	Н	N3
L	1	2	3	4	5	6	7	8
1 2 3 4 5 6 7 8		Curr E = T = L = Volt H =	rent ratii single d TO-220 ultralow age ratii AEC-Q		15 A) erfast re 600 V) lified	ecovery		

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-15ETL06HN3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS								
Dimensions	www.vishay.com/doc?95221							
Part marking information	www.vishay.com/doc?95068							
SPICE model	www.vishay.com/doc?96051							



TO-220AC

plane

DIMENSIONS in millimeters and inches









Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220AC

⊕ 0.015 **()** BA()

SYMBOL	MILLIN	IETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040			e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4		H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068			L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4		L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024			L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4		L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6		θ	90° t	o 93°	90° t	o 93°	
E	10.11	10.51	0.398	0.414	3, 6							

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Dimension b1, b3 and c1 apply to base metal only
- ⁽⁵⁾ Controlling dimension: inches
- ⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2 and E1
- ⁽⁷⁾ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- ⁽⁸⁾ Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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