

High Performance Schottky Rectifier, 1.5 A



DO-214AC (SMA)

PRODUCT SUMMARY			
Package	DO-214AC (SMA)		
I _{F(AV)}	1.5 A		
V _R	40 V		
V _F at I _F	0.34 V		
I _{RM}	20 mA at 125 °C		
T _J max.	150 °C		
Diode variation	Single die		
E _{AS}	6.0 mJ		

FEATURES

- Extremely low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability



- Surface mountable
- Compact size
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Switching power supplies
- Meter protection
- Reverse protection for power input to PC board circuits
- · Battery isolation and charging
- · Low threshold voltage diode
- Freewheeling or by-pass diode
- Low voltage clamp

DESCRIPTION

The VS-15MQ040-M3 Schottky rectifier is designed to be used for low power applications where a reverse voltage of 40 V is encountered and surface mountable is required.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.5	A		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 µs sine	330	A		
V _F	2 A _{pk} , T _J = 125 °C	0.43	V		
TJ	Range	-40 to +150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-15MQ040-M3	UNITS
Maximum DC reverse voltage	V_R	40	V
Maximum working peak reverse voltage	V_{RWM}	40	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at $T_L = 105$ °C, 1 On PC board 9 mm ² island (0.013 mm thick copper pad area	ŭ	2.1	A
See fig. 4	I _{F(AV)}	50 % duty cycle at $T_L = 113$ °C, I On PC board 9 mm ² island (0.013 mm thick copper pad area	ŭ	1.5	A
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	330	
non-repetitive surge current See fig. 6	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	140	Α
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 1.0		A	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop		1.5 A	- T _{.1} = 25 °C	0.43	V
	V (1)	2 A	- IJ=25 C	0.49	
See fig. 1	V _{FM} ⁽¹⁾	1.5 A	T _J = 125 °C	0.34	
		2 A		0.43	
Maximum reverse leakage current	I _{RM}	T _J = 25 °C	V _R = Rated V _R	0.5	mA
See fig. 2		T _J = 125 °C		20	
Threshold voltage	V _{F(TO)}	$T_{J} = T_{J} \text{ maximum} $ 0.26 64.6		0.26	V
Forward slope resistance	r _t			64.6	mΩ
Typical junction capacitance	C _T	V _R = 10 V _{DC} , T _J = 25 °C, test signal = 1 MHz		134	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width = 300 $\mu s,$ duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	OZ.
Marking device		Case style SMA (similar D-64)	Х	F

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



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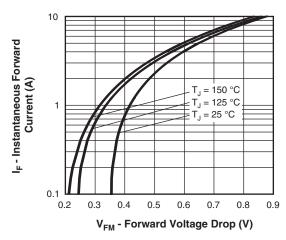


Fig. 1 - Maximum Forward Voltage Drop Characteristics

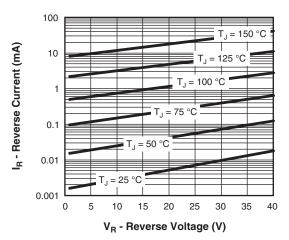


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

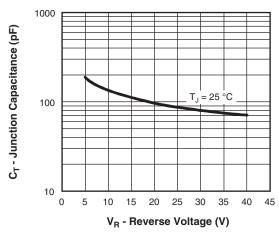
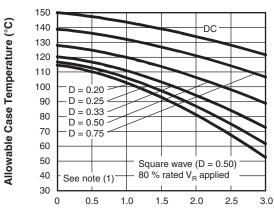
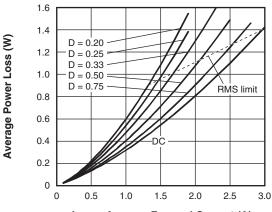


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



I_{F(AV)} - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature



I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

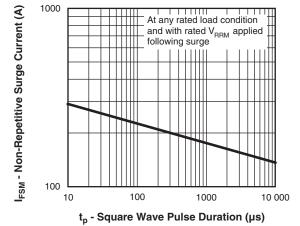


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

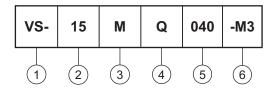
Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 80$ % rated V_R



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating
- 3 M = SMA
- 4 Q = Schottky "Q" series
- Voltage rating (040 = 40 V)
- 6 Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	PREFERRED PACKAGE CODE	PACKAGING DESCRIPTION		
VS-15MQ040-M3/5AT	5AT	7500	13" diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95400</u>				
Part marking information <u>www.vishay.com/doc?95403</u>				
Packaging information	www.vishay.com/doc?95404			



SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)



Mounting Pad Layout





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