Vishay Semiconductors



# Three Phase Bridge, 160 A (Power Modules)



PRIMARY CHARACTERISTICS					
Ι <sub>Ο</sub>	160 A at 118 °C				
V <sub>RRM</sub>	1600 V to 1800 V				
Package	MTC				
Circuit configuration	Three phase bridge				

### FEATURES

- Blocking voltage up to 1800 V
- High surge capability



- High thermal conductivity package, electrically <sup>COMPLIANT</sup> insulated case
- Excellent power volume ratio
- 3600 V<sub>RMS</sub> isolating voltage
- UL approved file E78996
- Designed for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

A range of extremely compact, encapsulated three phase bridge rectifiers offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
Io <sup>(1)</sup>		257	А			
IO (1)	T <sub>C</sub>	85	°C			
1	50 Hz	1540	Α			
IFSM	60 Hz	1610				
l <sup>2</sup> t	50 Hz	11 860	A <sup>2</sup> 0			
1-1	60 Hz	10 825	— A <sup>2</sup> s			
l²√t		118 580	A²√s			
V <sub>RRM</sub>	Range	1600 to 1800	V			
T <sub>Stg</sub>	Range	-40 to +125	°C			
TJ	Range	-40 to +150	°C			

Note

<sup>(1)</sup> Maximum output current must be limited to 220 A to do not exceed the maximum temperature of terminals

## ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS								
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> MAXIMUM AT T <sub>J</sub> = MAXIMUM mA				
VS 160MT C	160	1600 1700		12				
VS-160MTC 180		1800	1900					

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FORWARD CONDUCTION						
PARAMETER	SYMBOL		TEST CONDIT	VALUES	UNITS	
Maximum DC output current	L.	120° root or	onduction angle		160	А
at case temperature	lo	120 1601.00		118	°C	
	I <sub>FSM</sub>	t = 10 ms	No voltage		1540	A
Maximum peak, one-cycle forward, non-repetitive surge current		t = 8.3 ms	reapplied	Initial T <sub>J</sub> = T <sub>J</sub> maximum	1610	
		t = 10 ms	100 % V <sub>RRM</sub>		1295	
		t = 8.3 ms	reapplied		1355	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	t = 10 ms	No voltage		11 860	- A <sup>2</sup> s
		t = 8.3 ms	reapplied		10 825	
Maximum - t for fusing	1-1	t = 10 ms	100 % V <sub>BBM</sub>		8385	
		t = 8.3 ms	reapplied		7620	
Maximum I²√t for fusing	l²√t	t = 0.1 ms to	118 580	A²√s		
Low level value of threshold voltage	V <sub>FT(TO)1</sub>	(16.7 % x π :	$I_{F(AV)} < I < \pi \times I_{F}$	0.81	V	
High level value of threshold voltage	V <sub>FT(TO)2</sub>	$(I > \pi \times I_{F(AV)}), T_J$ maximum 0.98				v
Low level value of forward slope resistance	r <sub>f1</sub>	16.7 % x π x	$I_{F(AV)} < I < \pi \times I_{F(AV)}$	3.89	mΩ	
High level of forward slope resistance	r <sub>f2</sub>	$(I > \pi \times I_{F(AV)}), T_J$ maximum 3.68				11152
Maximum forward voltage drop	V <sub>FM</sub>	I <sub>pk</sub> = 300 A,	T <sub>J</sub> = 25 °C, per ju	nction	1.85	v
RMS isolation voltage	VISOL	T <sub>J</sub> = 25 °C, a	all terminal shorte	d f = 50 Hz, t = 1 s	3600	v

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operati	ng	TJ		-40 to +150	℃	
Maximum storage tempera	ature	T <sub>Stg</sub>		-40 to +125	U	
Maximum thermal resistar	ice,	Б	DC operation per module	0.058		
junction to case		R <sub>thJC</sub>	DC operation per junction	0.35	°C/W	
Typical thermal resistance case to heatsink	3	R <sub>thCS</sub>	Per module Mounting surface smooth, flat, and greased	0.03		
Mounting torque	to heatsink		A mounting compound is recommended and the	5	Nm	
± 15 %	to terminal		torque should be rechecked after a period of 3 h to allow for the spread of the compound. Lubricated	5	INITI	
Approximate weight			threads.	235	g	

	N PER J	JUNCTI	ON								
DEVICES	S	INE HALF	WAVE CO	NDUCTIO	N	REC	TANGULA	R WAVE (	CONDUCT	ION	UNITS
DEVICES	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
VS-160MTC Series	0.054	0.061	0.076	0.107	0.165	0.039	0.064	0.083	0.111	0.167	°C/W

#### Note

Table shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC



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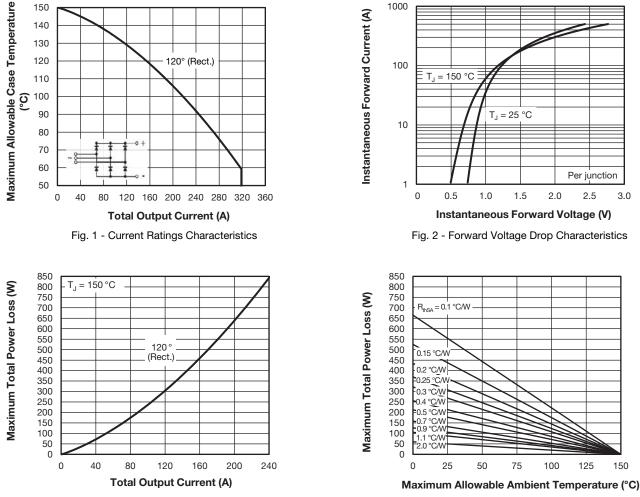
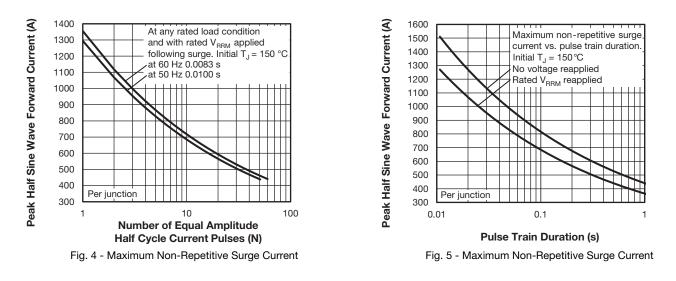
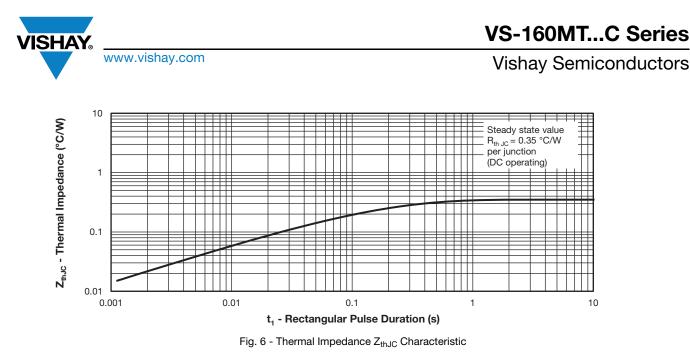


Fig. 3 - Total Power Loss Characteristics

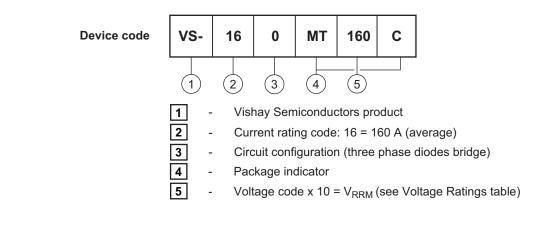


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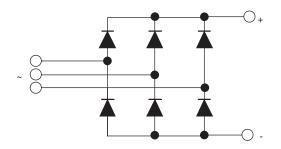
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