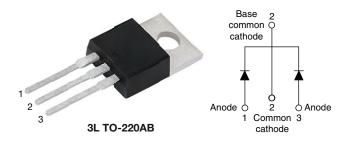


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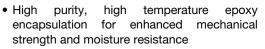
## High Performance Schottky Rectifier, 2 x 8 A



PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub>	2 x 8 A						
$V_{R}$	60 V, 80 V, 100 V						
V <sub>F</sub> at I <sub>F</sub>	0.58 V						
I <sub>RM</sub> max.	7 mA at 125 °C						
T <sub>J</sub> max.	175 °C						
E <sub>AS</sub>	7.5 mJ						
Package	3L TO-220AB						
Circuit configuration	Common cathode						

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- · High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES UN								
I <sub>F(AV)</sub>	Rectangular waveform	16	Α					
V <sub>RRM</sub>		60 to 100	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	Α					
V <sub>F</sub>	8 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.58	V					
TJ	Range	-55 to +175	°C					

VOLTAGE RATINGS								
PARAMETER SYMBOL VS-16CTQ060-M3 VS-16CTQ080-M3 VS-16CTQ100-M3 UNITS								
Maximum DC reverse voltage	$V_R$	60	80	100	W			
Maximum working peak reverse voltage	$V_{RWM}$	00	00	100	V			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS			
Maximum average forward per		I	50.0% districted at T = 440.00 master adaptives		8			
current, see fig. 5	per device	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 148 °C, rectangular waveform		16	A		
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7		_	5 μs sine or 3 μs rect. pulse	Following any rated	850	Α		
		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	275	A		
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.50 A, L = 60 mH		7.50	mJ		
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 µs Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		0.50	Α		



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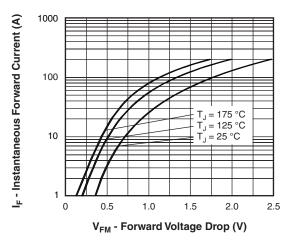
ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS			
		8 A	T <sub>.1</sub> = 25 °C	0.72	V		
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	16 A	1j=25 C	0.88			
See fig. 1	V <sub>FM</sub> (1)	8 A	T 105 °C	0.58			
		16 A	T <sub>J</sub> = 125 °C	0.69			
Maximum reverse leakage current per leg	. (1)	T <sub>J</sub> = 25 °C	V <sub>R</sub> = rated V <sub>R</sub>	0.55	A		
See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	v <sub>R</sub> = rated v <sub>R</sub>	7.0	mA		
Threshold voltage	V <sub>F(TO)</sub>	T T mayimum		0.415	V		
Forward slope resistance	r <sub>t</sub>	ıj = ıj maxımum	$T_J = T_J$ maximum		mΩ		
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range	500	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs			

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C				
Maximum thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation	3.25	°C/W				
Maximum thermal resistance junction to case per package	R <sub>thJC</sub>	DC operation	1.63					
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50					
Approximate weight			2	g				
Approximate weight			0.07	OZ.				
Mounting torque minimum			6 (5)	kgf · cm				
Mounting torque — maximum			12 (10)	(lbf · in)				
			16CT	Q060				
Marking device		Case style 3L TO-220AB	16CTQ080					
			16CTQ100					

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100 T<sub>J</sub> = 175 °C I<sub>R</sub> - Reverse Current (mA) 10 T<sub>J</sub> = 150 °C = 125 °C = 100 °C 0.1 T<sub>1</sub> = 75 °C 0.01 T<sub>J</sub> = 50 °C T<sub>1</sub> = 25 °C 0.001 0.0001 20 0 40 60 80 100 V<sub>R</sub> - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

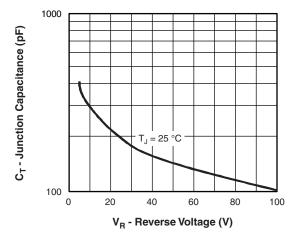


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

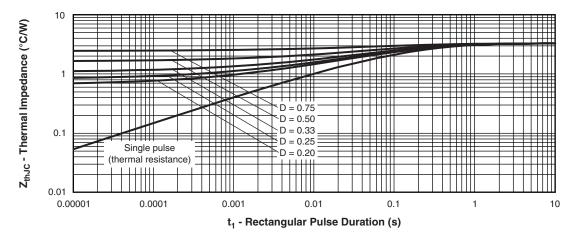


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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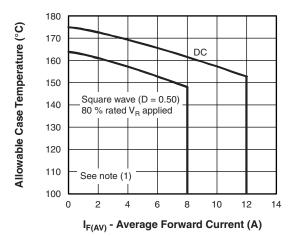


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

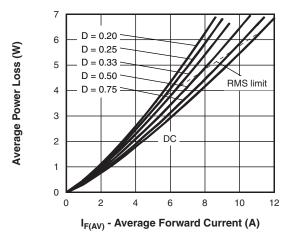


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

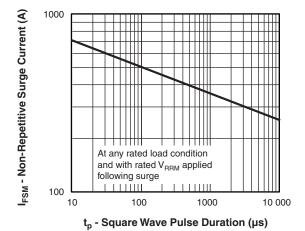


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

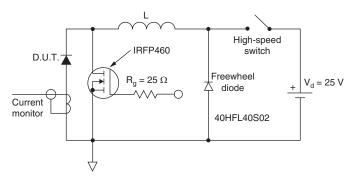


Fig. 8 - Unclamped Inductive Test Circuit

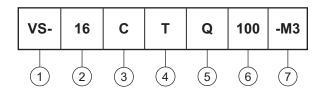
### Note

Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_{R} (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$  applied

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#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (16 = 16 A)

Circuit configuration

C = common cathode

4 - Package

T = TO-220

5 - Schottky "Q" series

060 = 60 V 080 = 80 V

6 - Voltage rating

100 = 80 V 100 = 100 V

7 - Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-16CTQ060-M3	50	1000	Antistatic plastic tube						
VS-16CTQ080-M3	50	1000	Antistatic plastic tube						
VS-16CTQ100-M3	50	1000	Antistatic plastic tube						

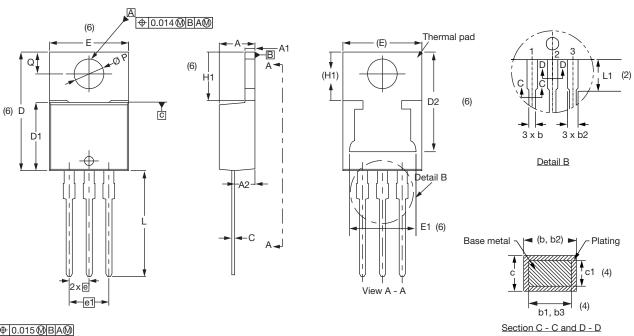
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96154					
Part marking information	www.vishay.com/doc?95028					
SPICE model	www.vishay.com/doc?95279					



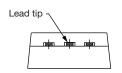
### Vishay Semiconductors

### **3L TO-220AB**

#### **DIMENSIONS** in millimeters and inches



#### **⊕** 0.015 **M** B A **M**



Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIM	IETERS	INCHES		NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355		1		•			•	

### **Notes**

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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