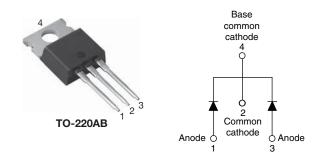
# VS-16CTU04HN3

**Vishay Semiconductors** 

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## Ultrafast Rectifier, 16 A FRED Pt<sup>®</sup>



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 8 A					
V <sub>R</sub>	400 V					
V <sub>F</sub> at I <sub>F</sub>	0.94 V					
t <sub>rr</sub> (typ.)	24 ns					
T <sub>J</sub> max.	175 °C					
Package	TO-220AB					
Circuit configuration	Common cathode					

### **FEATURES**

- · Ultrafast recovery time
- · Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

FRED Pt<sup>®</sup> series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage		V <sub>RRM</sub>		400	V			
Average rectified forward current	per leg			8				
	total device	I <sub>F(AV)</sub>	$T_{C} = 155 \text{ °C}, \text{ rated } V_{R}$	16	А			
Non-repetitive peak surge current		I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	100	A			
Peak repetitive forward current		I <sub>FRM</sub>	$T_{C}$ = 155 °C, rated $V_{R}$ , square wave, 20 kHz	16				
Operating junction and storage temp	peratures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C			

<b>ELECTRICAL SPECIFICATIONS PER LEG</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	400	-	-	
Forward voltage	VF	I <sub>F</sub> = 8 A	-	1.19	1.3	V
	۷F	I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	0.94	1.0	
		V <sub>R</sub> = V <sub>R</sub> rated	-	0.2	10	
Reverse leakage current I <sub>R</sub>		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	20	500	μA
Junction capacitance	CT	V <sub>R</sub> = 400 V		14	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH

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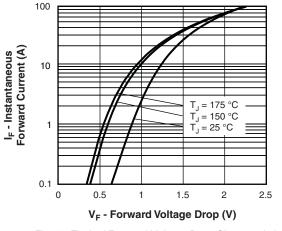
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<b>DYNAMIC RECOVERY CHARACTERISTICS PER LEG</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	50 A/µA, V <sub>R</sub> = 30 V	-	35	-		
Reverse recovery time	+	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	-	24	-	ns		
	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	43	-	115	
		T <sub>J</sub> = 125 °C		-	67	-		
Dook rooovery ourrent		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 8 A dI <sub>F</sub> /dt = 200 A/μs	-	2.8	-	А	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$V_{\rm R} = 200 \text{ V}$	-	6.3	-	~	
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	60	-		
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	210	-	nC	

THERMAL MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and temperature range	storage	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance,	per leg	Р		-	3.6	4	
junction to case	per device	R <sub>thJC</sub>		-	1.8	2	
Thermal resistance, junction to ambient		R <sub>thJA</sub>	Typical socket mount	-	-	50	°C/W
Thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-	-
W/aight				-	2.0	-	g
Weight				-	0.07	-	oz.
Mounting torque				6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device			Case style TO-220AB	16CTU04H			•

## **VS-16CTU04HN3**

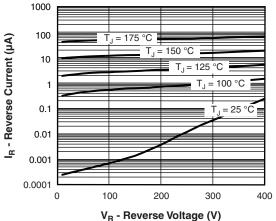
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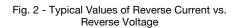


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Fig. 1 - Typical Forward Voltage Drop Characteristics





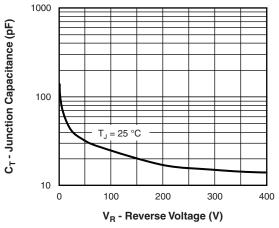


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

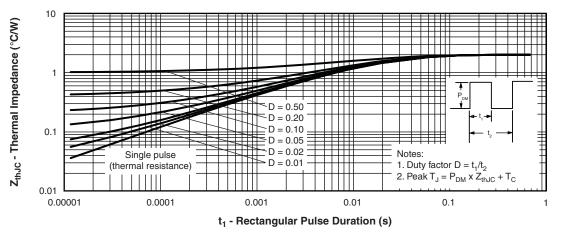
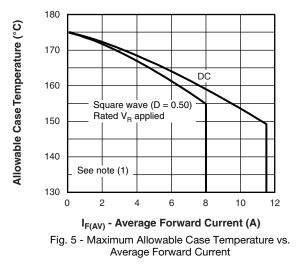
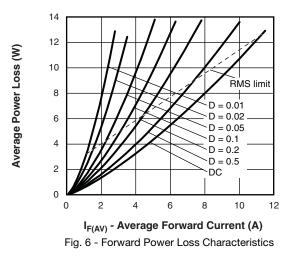


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics







#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \, \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \, \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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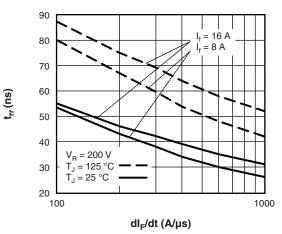
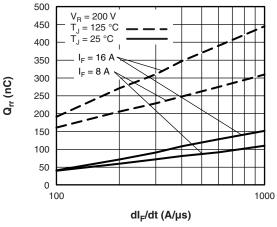


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

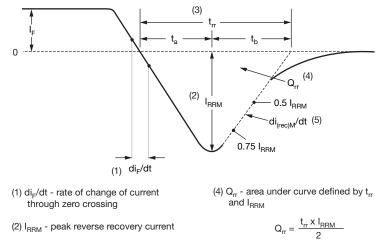




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(3)  $t_{\rm rr}$  - reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current. (5) di w/dt - peak rate of change of

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 1 - Reverse Recovery Waveform and Definitions

### **ORDERING INFORMATION TABLE**

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Device code	VS-	16	С	т	U	04	н	N3
		(2)	3	4	5	6	(7)	(8)
	1 - 2 -			niconduc ng (16 =		oduct		
	3 -	Circ	uit conf	iguratior	ו:			
	4 -		Commo kage:	on catho	de			
	5 -		TO-220 afast red					
	6 -	Volt	age rati	ng (04 =	= 400 V)			
	7 -			101 qua				
	8 -			ntal digit en-free,		complia	nt, and	totally le

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-16CTU04HN3	50	1000	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95222						
Part marking information	www.vishay.com/doc?95028					
SPICE model	www.vishay.com/doc?96565					

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**TO-220AB** 

#### **DIMENSIONS** in millimeters and inches





.ead	assignments

**Diodes** 

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left( 4\right) }$  Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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