Vishay Semiconductors

## High Performance Schottky Rectifier, 180 A



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PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	180 A				
V <sub>R</sub>	100 V				
Package	HALF-PAK (D-67)				
Circuit configuration	Single diode				

### FEATURES

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

The VS-183NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	180	A			
V <sub>RRM</sub>		100	V			
I <sub>FSM</sub>	$t_p = 5 \ \mu s \ sine$	22 000	A			
V <sub>F</sub>	180 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.73	V			
TJ	Range	-55 to +175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-183NQ100PbF	UNITS			
Maximum DC reverse voltage	V <sub>R</sub>	100	M			
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	v			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS		
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 128 °C	240			
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse Following any rated load condition and with		22 000	А	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	2500		
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 5.5 A, L = 1 mH		15	mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1	А	



COMPLIANT



## VS-183NQ100PbF

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS		
		180 A	T <sub>.1</sub> = 25 °C	0.91	V	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	360 A	1j=25 C	1.23		
See fig. 1		180 A	T <sub>J</sub> = 125 °C	0.73		
		360 A	1j = 125 0	0.9		
Maximum reverse leakage current	1 (1)	$T_J = 25 \ ^{\circ}C$	$V_{\rm B} = Rated V_{\rm B}$	4.5	mA	
See fig. 2	I <sub>RM</sub> (1)	T <sub>J</sub> = 125 °C	$v_{\rm R}$ = naleu $v_{\rm R}$	60		
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range	4150	pF		
Typical series inductance	L <sub>S</sub>	From top of terminal hole to r	6.0	nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000			V/µs	

Note

<sup>(1)</sup> Pulse width = 500  $\mu$ s

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	e	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4		°C/W
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05	0/10
				30	g
Approximate weight				1.06	oz.
Mounting torque maximum Terminal torque minimum maximum maximum				3 (26.5)	N · m (lbf · in)
			Non-lubricated threads	4 (35.4)	
			Non-Iubricateu tineaus	3.4 (30)	
				5 (44.2)	
Case style				HALF-PA	K module

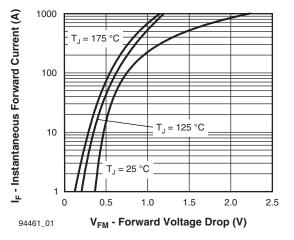


Fig. 1 - Maximum Forward Voltage Drop Characteristics

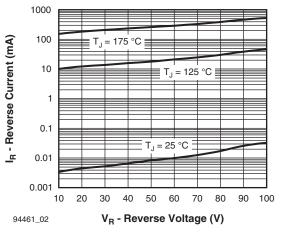


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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## VS-183NQ100PbF

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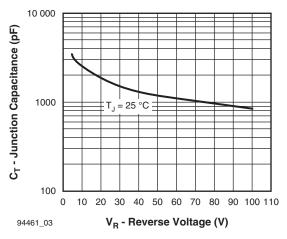


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

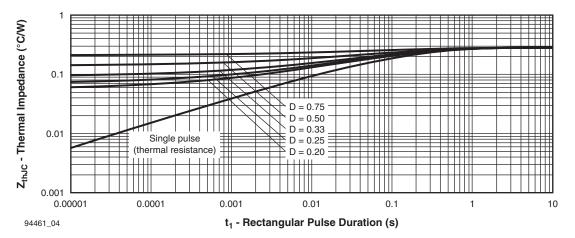


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

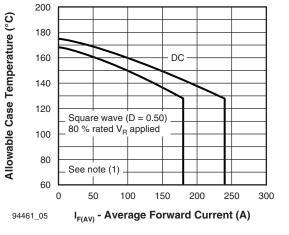


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

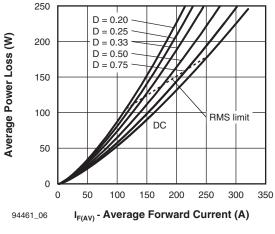


Fig. 6 - Forward Power Loss Characteristics

Revision: 01-Feb-2019

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Document Number: 94461

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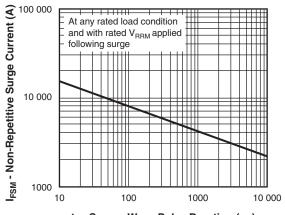




Fig. 7 - Maximum Non-Repetitive Surge Current

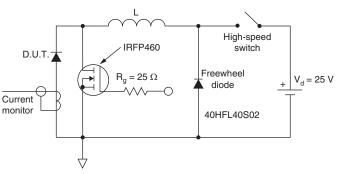


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R at V_{R1} = rated V_R$ 

### **ORDERING INFORMATION TABLE**

Device code	VS-	18	3	Ν	Q	100	PbF
	1	2	3	4	5	6	7
	1 -	Vis	hay Sen	nicondu	ctors pro	oduct	
	2 -	Ave	erage cu	rrent ra	ting (x 1	0)	
	3 - Product silicon identification						
	4 -	N =	not isol	ated			
	5 -	Q =	Schottl	ky rectifi	er diode	;	
	6 -	Vol	tage rati	ing (100	= 100 \	/)	
	7 -	Lea	ad (Pb)-f	ree			

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95020						
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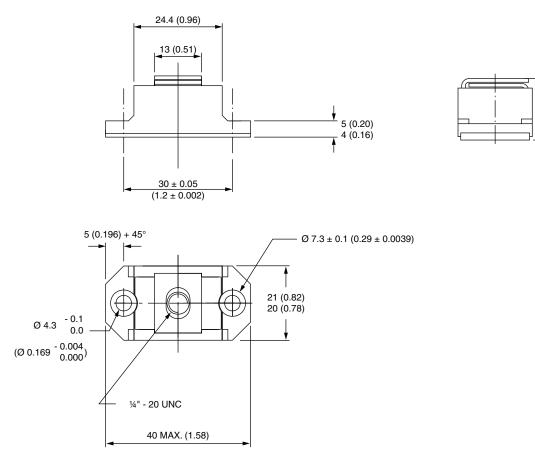
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17.5 (0.69) 16.5 (0.65)



### **DIMENSIONS** in millimeters (inches)

SHAY





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