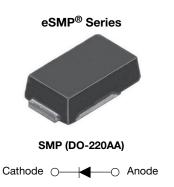
VS-1ENH01-M3, VS-1ENH02-M3

Vishay Semiconductors

COMPLIANT

HALOGEN

Ultrafast Rectifier, 1 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	1 A			
V_{R}	100 V, 200 V			
V _F at I _F	0.69 V			
I _{FSM}	40 A			
t _{rr} (typ.)	23 ns			
T _J max.	175 °C			
Package	SMP (DO-220AA)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.0 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- Low leakage current
- \bullet Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^{\circ}\text{C}$
- For PFC, CRM snubber operation
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATION

For use in high frequency, freewheeling, DC/DC converters, PFC, and in snubber industrial and automotive applications.

MECHANICAL DATA

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per

J-STD-002, meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse	VS-1ENH01-M3	V		100	V
voltage VS-1ENH02-M3	V_{RRM}		200	V	
Average rectified forward	current	I _{F(AV)}	T _C = 168 °C	1	۸
Non-repetitive peak surge current		I _{FSM}	T _J = 25 °C, 10 ms sine pulse	40	А
Operating junction and sto	orage temperatures	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage,	VS-1ENH01-M3	V _{BR} ,	L = 100 uA	100	-	-	
blocking voltage	VS-1ENH02-M3	V _R			-	-	V
Forward voltage		VF	I _F = 1 A	-	0.86	0.92	V
Forward voitage		VF	I _F = 1 A, T _J = 150 °C	-	0.69	0.74	
Reverse leakage current		I _R	V _R = V _R rated	-	-	2	μΑ
			$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	20	
Junction capacitance		C _T	V _R = 200 V	-	8	-	pF



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	23	-	-
Reverse recovery time		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	28	
	t _{rr}	T _J = 25 °C		-	14	-	ns - A
		T _J = 125 °C	$I_F = 1 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_B = 100 \text{ V}$	-	22	-	
Peak recovery current I	I	T _J = 25 °C		-	1.7	-	
	I _{RRM}	T _J = 125 °C		-	2.7	-	
Devenue verseure shaves	0	T _J = 25 °C]	-	10	-	nC
Reverse recovery charge	erse recovery charge Q _{rr}	T _J = 125 °C		-	29	-	110

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage	temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction mount		R _{thJM} (1)	Infinite heatsink	-	7	9	°C/W
Thermal resistance, junction to ambient		R _{thJA}	PCB footprint 4.8 mm x 4.8 mm	-	107	-	C/VV
Marking device VS-1ENH01-M3 VS-1ENH02-M3			Case style SMP (DO-220AA)	1H1			
			Case style SMP (DO-220AA)		1H2		

Note

⁽¹⁾ Thermal resistance junction to mount follows JEDEC® 51-14 transient dual interface test method (TDIM)

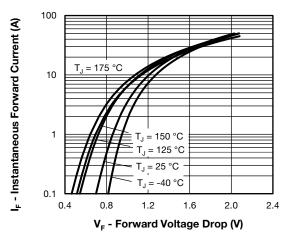


Fig. 1 - Typical Forward Voltage Drop Characteristics

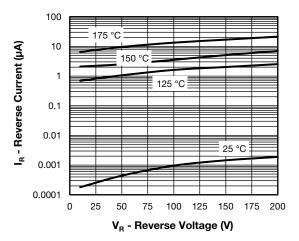


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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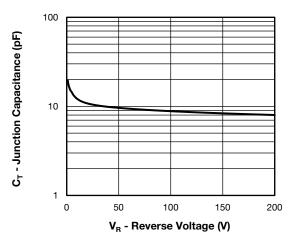


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

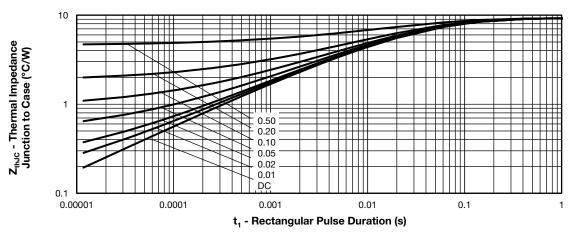


Fig. 4 - Transient Thermal Impedance, Junction to Case

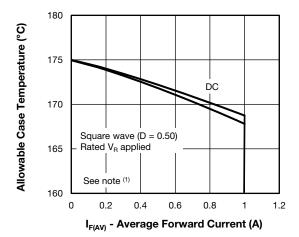


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

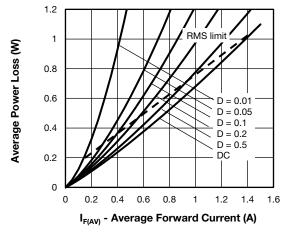


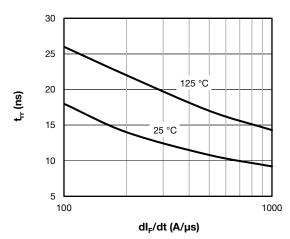
Fig. 6 - Forward Power Loss Characteristics

Note

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}$; $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = rated V_R$

VS-1ENH01-M3, VS-1ENH02-M3

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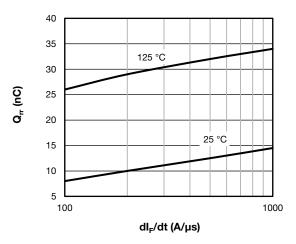
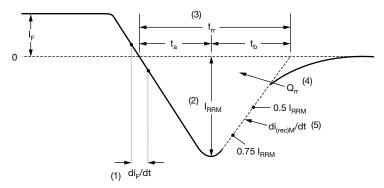


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) ${\rm I}_{\rm RRM}$ peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going $l_{\rm F}$ to point where a line passing through 0.75 $l_{\rm RRM}$ and 0.50 $l_{\rm RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

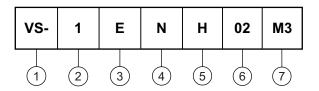
Fig. 9 - Reverse Recovery Waveform and Definitions

VS-1ENH01-M3, VS-1ENH02-M3

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (1 = 1 A)

Circuit configuration:

E = single diode

4 - N = SMP package

5 - Process type,

H = ultrafast recovery

6 - Voltage code (02 = 200 V)

- M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-1ENH01-M3/84A	84A	3000	7" diameter plastic tape and reel			
VS-1ENH01-M3/85A	85A	10 000	13" diameter plastic tape and reel			
VS-1ENH02-M3/84A	84A	3000	7" diameter plastic tape and reel			
VS-1ENH02-M3/85A	85A	10 000	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96547</u>					
Part marking information	www.vishay.com/doc?96574				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96550				

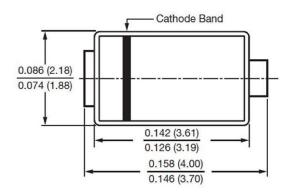


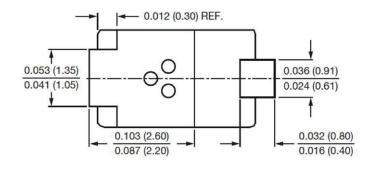


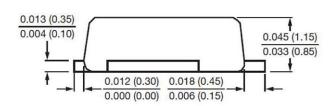
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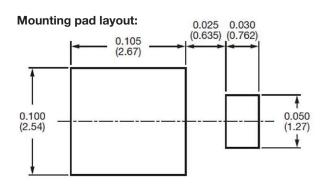
SMP (DO-220AA)

DIMENSIONS in inches (millimeters)











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