

COMPLIANT

High Performance Schottky Rectifier, 200 A

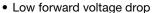


PRIMARY CHARACTERISTICS				
I _{F(AV)}	200 A			
V_{R}	45 V			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

FEATURES







- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-200CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES				
I _{F(AV)}	Rectangular waveform	200	Α			
V_{RRM}		45	V			
I _{FSM}	t _p = 5 μs sine	26 000	А			
V _F	100 A _{pk} , T _J = 125 °C (per leg)	0.52	V			
T _J	Range	-55 to +150	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-200CNQ045PbF	UNITS	
Maximum DC reverse voltage	V_R	45	V	
Maximum working peak reverse voltage	V _{RWM}	45	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per leg		50 % duty cycle at T _C = 116 °C, rectangular waveform		100	^
forward current See fig. 5	per device	I _{F(AV)}			200	A
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	26 000	A
			10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	1550	
Non-repetitive avalanch	ne energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 17 A, L = 1 mH		135	mJ
Repetitive avalanche cu	urrent per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		20	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	100 A	T ₁ = 25 °C	0.55	V
Maximum forward voltage drop per leg		200 A	1j=25 C	0.73	
See fig. 1		100 A	T 105 °C	0.52	
		200 A	T _J = 125 °C	0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	10	- mA
See fig. 2		T _J = 125 °C	VR = nateu VR	800	
Threshold voltage	V _{F(TO)}	- T _J = T _J maximum		0.27	V
Forward slope resistance	r _t			2.0	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5200	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}	- 55	-	150	°C
Thermal resistance, junction to case	per leg	ם	-	-	0.38	°C/W
	per module	R_{thJC}	-	-	0.19	
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-	
Mariah				68		g
Weight			-	2.4	_	OZ.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)
Terminal torque	minal torque		30 (3.4)	-	44.2 (5)	(,
Vertical pull				-	80 35	lbf ⋅ in
2" lever pull				-		

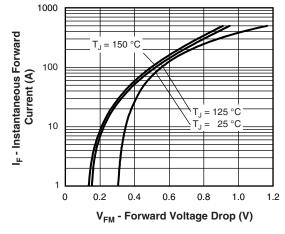


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

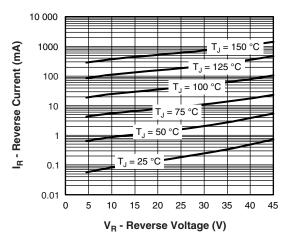


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

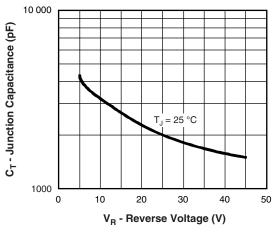


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

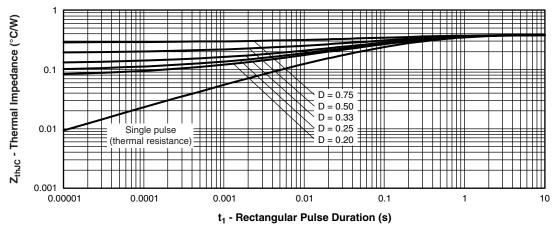


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

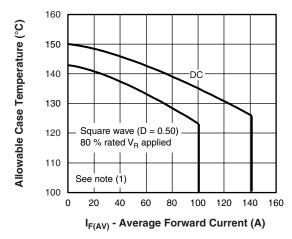


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

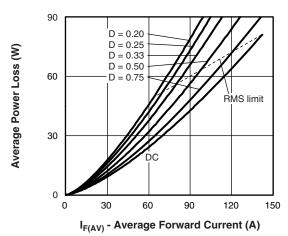


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

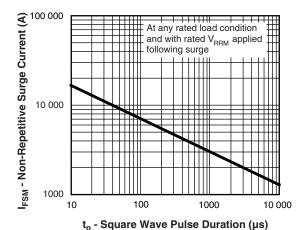


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

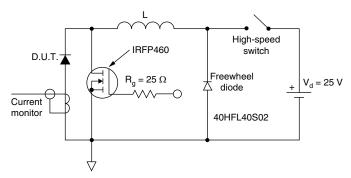


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = & \text{forward power loss} = \text{I}_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at (I}_{\text{F(AV)}}/\text{D) (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = & \text{inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D); I}_{\text{R}} \text{ at V}_{\text{R1}} = 80 \% \text{ rated V}_{\text{R}} \\ \end{array}$

ORDERING INFORMATION TABLE

1 - Vishay Semiconductors product

2 - Average current rating (x 10)

Product silicon identification

- C = circuit configuration

5 - N = not isolated

6 - Q = Schottky rectifier diode

7 - Voltage rating (045 = 45 V)

8 - Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			



TO-244

DIMENSIONS in millimeters (inches)









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Vishay

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