

TO-244


| PRIMARY CHARACTERISTICS |  |
| :---: | :---: |
| $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | 200 A |
| $\mathrm{~V}_{\mathrm{R}}$ | 45 V |
| Package | TO-244 |
| Circuit configuration | Two diodes common cathode |

## FEATURES

- $150{ }^{\circ} \mathrm{C} T_{J}$ operation
- Center tap module

RoHS

- Low forward voltage drop

COMPLIANT

- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165 FI
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


## DESCRIPTION / APPLICATIONS

The VS-200CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to $150{ }^{\circ} \mathrm{C}$ junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

## MAJOR RATINGS AND CHARACTERISTICS

| SYMBOL | CHARACTERISTICS | VALUES | UNITS |
| :--- | :--- | :---: | :---: |
| $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | Rectangular waveform | 200 | A |
| $\mathrm{~V}_{\text {RRM }}$ |  | 45 | V |
| $\mathrm{I}_{\text {FSM }}$ | $\mathrm{t}_{\mathrm{p}}=5 \mu \mathrm{~s}$ sine | 26000 | A |
| $\mathrm{~V}_{\mathrm{F}}$ | $100 \mathrm{~A}_{\mathrm{pk}}, \mathrm{T}_{J}=125^{\circ} \mathrm{C}$ (per leg) | 0.52 | V |
| $\mathrm{~T}_{J}$ | Range | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |


| VOLTAGE RATINGS |  |  |  |
| :--- | :---: | :---: | :---: |
| PARAMETER | SYMBOL | VS-200CNQO45PbF | UNITS |
| Maximum DC reverse voltage | $\mathrm{V}_{\mathrm{R}}$ | 45 | V |
| Maximum working peak reverse voltage | $\mathrm{V}_{\mathrm{RWM}}$ | 45 |  |


| ABSOLUTE MAXIMUM RATINGS |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER | SYMBOL | TEST CONDITIONS |  | VALUES | UNITS |
| Maximum average  <br> Morward current <br> See fig. 5 per leg <br>  per device | $I_{\text {F }}^{\text {(AV) }}$ | $50 \%$ duty cycle at $\mathrm{T}_{\mathrm{C}}=116{ }^{\circ} \mathrm{C}$, rectangular waveform |  | 100 | A |
|  |  |  |  | 200 |  |
| Maximum peak one cycle non-repetitive surge current per leg See fig. 7 | $\mathrm{I}_{\text {FSM }}$ | $5 \mu \mathrm{~s}$ sine or $3 \mu \mathrm{~s}$ rect. pulse | Following any rated load condition and with rated $\mathrm{V}_{\text {RRM }}$ applied | 26000 | A |
|  |  | 10 ms sine or 6 ms rect. pulse |  | 1550 |  |
| Non-repetitive avalanche energy per leg | $\mathrm{E}_{\text {AS }}$ | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}, \mathrm{I}_{\text {AS }}=17 \mathrm{~A}, \mathrm{~L}=1 \mathrm{mH}$ |  | 135 | mJ |
| Repetitive avalanche current per leg | $\mathrm{I}_{\text {AR }}$ | Current decaying linearly to zero in $1 \mu \mathrm{~s}$ Frequency limited by $\mathrm{T}_{\mathrm{J}}$ maximum $\mathrm{V}_{\mathrm{A}}=1.5 \times \mathrm{V}_{\mathrm{R}}$ typical |  | 20 | A |


| PARAMETER | SYMBOL | TEST CONDITIONS |  | VALUES | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Maximum forward voltage drop per leg See fig. 1 | $\mathrm{V}_{\mathrm{FM}}{ }^{(1)}$ | 100 A | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | 0.55 | V |
|  |  | 200 A |  | 0.73 |  |
|  |  | 100 A | $\mathrm{T}_{J}=125^{\circ} \mathrm{C}$ | 0.52 |  |
|  |  | 200 A |  | 0.69 |  |
| Maximum reverse leakage current per leg See fig. 2 | $\mathrm{IRM}^{(1)}$ | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | $\mathrm{V}_{\mathrm{R}}=$ Rated $\mathrm{V}_{\mathrm{R}}$ | 10 | mA |
|  |  | $\mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C}$ |  | 800 |  |
| Threshold voltage | $\mathrm{V}_{\mathrm{F} \text { (TO) }}$ | $\mathrm{T}_{J}=\mathrm{T}_{J}$ maximum |  | 0.27 | V |
| Forward slope resistance | $r_{\text {t }}$ |  |  | 2.0 | $\mathrm{m} \Omega$ |
| Maximum junction capacitance per leg | $\mathrm{C}_{\text {T }}$ | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}_{\mathrm{DC}}$ (t | ge 100 kHz to 1 MHz$) 25^{\circ} \mathrm{C}$ | 5200 | pF |
| Typical series inductance per leg | Ls | From top of | to mounting plane | 7.0 | nH |
| Maximum voltage rate of change | dV/dt | Rated VR |  | 10000 | V/us |

## Note

${ }^{(1)}$ Pulse width $<300 \mu \mathrm{~s}$, duty cycle $<2 \%$

| PARAMETER | SYMBOL | MIN. | TYP. | MAX. | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Maximum junction and storage temperature range | $\mathrm{T}_{\mathrm{J},} \mathrm{T}_{\mathrm{Stg}}$ | -55 | - | 150 | ${ }^{\circ} \mathrm{C}$ |
| Thermal resistance, junction to case | $\mathrm{R}_{\text {thJc }}$ | - | - | 0.38 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
|  |  | - | - | 0.19 |  |
| Thermal resistance, case to heatsink | $\mathrm{R}_{\text {thCs }}$ | - | 0.10 | - |  |
| Weight |  | - | 68 | - | g |
|  |  |  | 2.4 |  | oz. |
| Mounting torque |  | 35.4 (4) | - | 53.1 (6) | $\begin{aligned} & \text { Ibf } \cdot \text { in } \\ & (\mathrm{N} \cdot \mathrm{~m}) \end{aligned}$ |
| Mounting torque center hole |  | 30 (3.4) | - | 40 (4.6) |  |
| Terminal torque |  | 30 (3.4) | - | 44.2 (5) |  |
| Vertical pull |  | - | - | 80 |  |
| 2" lever pull |  | - | - | 35 | - |



Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)


Fig. 4 - Maximum Thermal Impedance $Z_{\text {thJc }}$ Characteristics (Per Leg)


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)


Fig. 6 - Forward Power Loss Characteristics (Per Leg)


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)


Fig. 8 - Unclamped Inductive Test Circuit

## Note

${ }^{(1)}$ Formula used: $T_{C}=T_{J}-\left(P d+P_{R E V}\right) \times R_{\text {thJC }}$;
$\mathrm{Pd}=$ forward power loss $=\mathrm{I}_{F(A V} \times \mathrm{V}_{\mathrm{FM}}$ at ( $\mathrm{I}_{\mathrm{F}}(\mathrm{AV} / \mathrm{D})$ (see fig. 6);
$\mathrm{Pd}_{\mathrm{REV}}=$ inverse power loss $=\mathrm{V}_{\mathrm{R} 1} \times \mathrm{I}_{\mathrm{R}}(1-\mathrm{D}) ; \mathrm{I}_{\mathrm{R}}$ at $\mathrm{V}_{\mathrm{R} 1}=80 \%$ rated $\mathrm{V}_{\mathrm{R}}$

## ORDERING INFORMATION TABLE



| 1 | Vishay Semiconductors product |
| :---: | :---: |
| 2 | Average current rating (x 10) |
| 3 | Product silicon identification |
| 4 | $\mathrm{C}=$ circuit configuration |
| 5 | $N=$ not isolated |
| 6 | Q = Schottky rectifier diode |
| 7 | - Voltage rating ( $045=45 \mathrm{~V}$ ) |
| 8 | - Lead (Pb)-free |


| LINKS TO RELATED DOCUMENTS |  |
| :--- | :--- |
| Dimensions | www.vishay.com/doc?95021 |

## TO-244

DIMENSIONS in millimeters (inches)


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