

COMPLIANT

# **High Performance Schottky Rectifier, 200 A**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	200 A			
$V_{R}$	135 V, 150 V			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

#### **FEATURES**

- 175 °C T<sub>.1</sub> operation
- · Center tap module
- · Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION / APPLICATIONS**

The VS-209CNQ center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I <sub>F(AV)</sub>	Rectangular waveform	Rectangular waveform 200			
$V_{RRM}$	Range	135/150	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	10 000	Α		
V <sub>F</sub>	100 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.71	V		
$T_J$	Range	Range -55 to +175			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-209CNQ135PbF	VS-209CNQ150PbF	UNITS
Maximum DC reverse voltage	$V_{R}$	135	150	V
Maximum working peak reverse voltage	$V_{RWM}$	133	130	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current per leg	J=	50 % duty cycle at T <sub>C</sub> = 131 °C, rectangular waveform		100	
See fig. 5 per devie				200	
Maximum peak one cycle non-repetitive surge		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	10 000	A
current per leg See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse		1200	
Non-repetitive avalanche energy per leg E		$T_J = 25 ^{\circ}\text{C},  I_{AS} = 5.5  \text{A},  L = 1  \text{mH}$		15	mJ
Repetitive avalanche current per leg I <sub>AR</sub>		Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	100 A	T <sub>.1</sub> = 25 °C	1.06	V
Maximum forward voltage drop per leg		200 A	1j=25 C	1.33	
See fig. 1		100 A	T 105 %C	0.74	
		200 A	T <sub>J</sub> = 125 °C	0.88	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	3	mA
See fig. 2		T <sub>J</sub> = 125 °C	V <sub>R</sub> = nateu V <sub>R</sub>	45	IIIA
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		3000	pF
Typical series inductance per leg	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>	-55	-	175	°C	
Thermal resistance, junction to case	per leg	D	-	-	0.38	°C/W	
	per module	$R_{thJC}$	-	-	0.19		
Thermal resistance, case to heatsink		R <sub>thCS</sub>	-	0.10	0 -		
Weight			-	68	_	g	
Weight				2.4		OZ.	
Mounting torque			35.4 (4)	-	53.1 (6)		
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)	
Terminal torque			30 (3.4)	-	44.2 (5)	(14 111)	
Vertical pull			-	-	80	llef in	
2" lever pull			-	-	35	—— lbf ⋅ in	

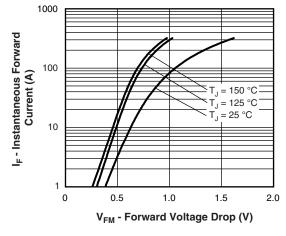


Fig. 1 - Maximum Forward Voltage Drop Characteristics

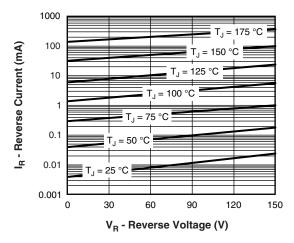


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

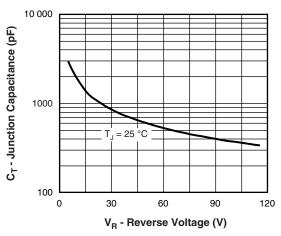


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

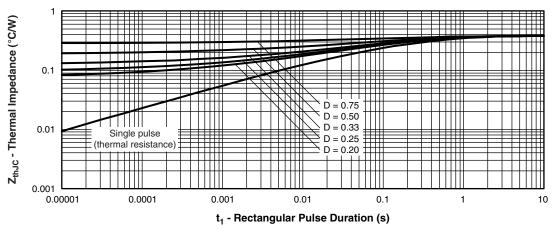


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics

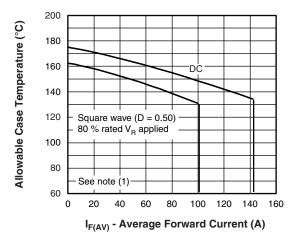


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

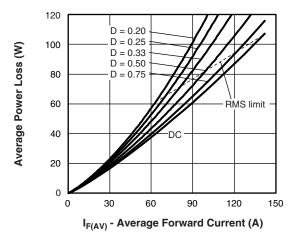
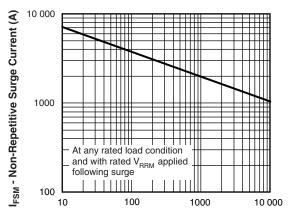


Fig. 6 - Forward Power Loss Characteristics



t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

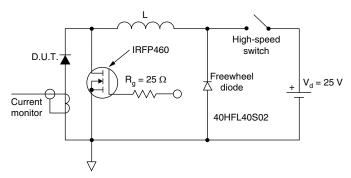


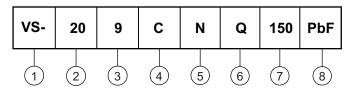
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

Average current rating (x 10)

3 - Product silicon identification

4 - C = circuit configuration

5 - N = not isolated

6 - Q = Schottky rectifier diode

- Voltage ratings — 135 = 135 V 150 = 150 V

Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			



## **TO-244**

### **DIMENSIONS** in millimeters (inches)









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Vishay

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