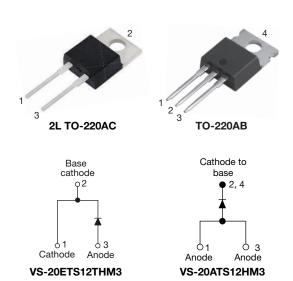
VS-20ETS12THM3, VS-20ATS12HM3

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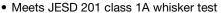
High Voltage, Input Rectifier Diode, 20 A

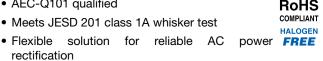


PRIMARY CHARACTERISTICS						
I _{F(AV)} 20 A						
V_{R}	1200 V					
V _F at I _F	1.1 V					
I _{FSM}	300 A					
T _J max.	150 °C					
Package	2L TO-220AC, 3L TO-220AB					
Circuit configuration	Single, common anode					

FEATURES

- Glass passivated pellet chip junction
- AEC-Q101 qualified





- High surge, low V_F rugged blocking diode for DC charging stations
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- On-board and off-board EV/HEV battery chargers
- Input rectification

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

OUTPUT CURRENT IN TYPICAL APPLICATIONS						
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS			
Capacitive input filter T _A = 55 °C, T _J = 125 °C common heatsink of 1 °C/W	16.3	21	А			

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES UNITS							
I _{F(AV)}	Sinusoidal waveform	20	Α				
V _{RRM}		1200	V				
I _{FSM}		300	Α				
V _F	10 A, T _J = 25 °C	1.0	V				
TJ		-40 to +150	°C				

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA				
VS-20ETS12THM3, VS-20ATS12HM3	1200	1300	1				

VS-20ETS12THM3, VS-20ATS12HM3

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	T _C = 105 °C, 180° conduction half sine wave	20			
Maximum peak one cycle	1	10 ms sine pulse, rated V _{RRM} applied	250	Α		
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	300			
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	316	A ² s		
Maximum i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	442	A-5		
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	4420	A²√s		

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS VALUES			UNITS	
Maximum forward voltage drop	V_{FM}	20 A, T _J = 25 °C		1.1	V	
Forward slope resistance	r _t	T 150 °C		10.4	mΩ	
Threshold voltage	V _{F(TO)}	T _J = 150 °C		0.85	V	
Maximum reverse leakage current	la	T _J = 25 °C V _B = Rated V _{BBM}		0.1	m ^	
waxiinum reverse leakage current	I _{RM}	T _J = 150 °C	VR = nated VRRM	1.0	mA	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-40 to +150	°C	
Maximum thermal resistance, junction to case		R_{thJC}	DC operation 1.3		- °C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased 0.5			
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque maximum				12 (10)	(lbf ⋅ in)	
Marking device	Marking device		Case style 2L TO-220AC	20ETS	S12TH	
			Case style 3L TO-220AB	20AT	S12H	

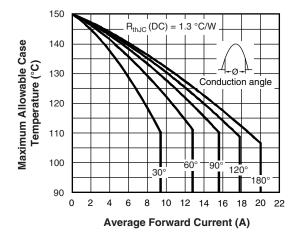


Fig. 1 - Current Rating Characteristics

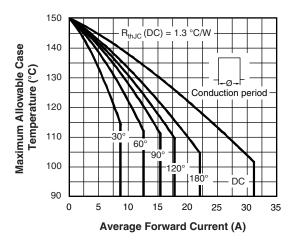


Fig. 2 - Current Rating Characteristics

300



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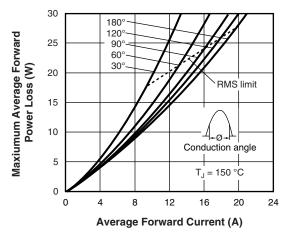
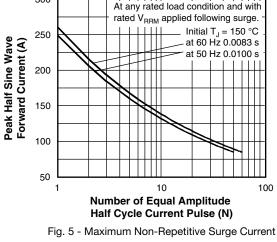


Fig. 3 - Forward Power Loss Characteristics



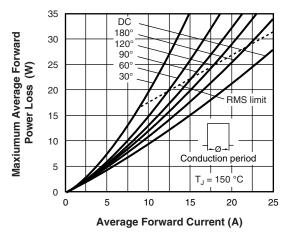


Fig. 4 - Forward Power Loss Characteristics

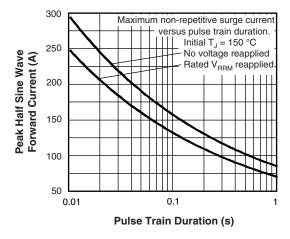


Fig. 6 - Maximum Non-Repetitive Surge Current

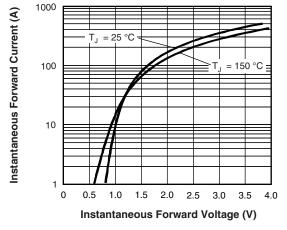


Fig. 7 - Forward Voltage Drop Characteristics

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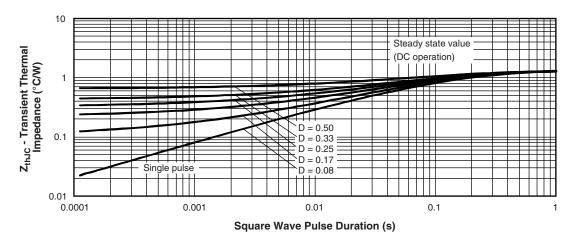
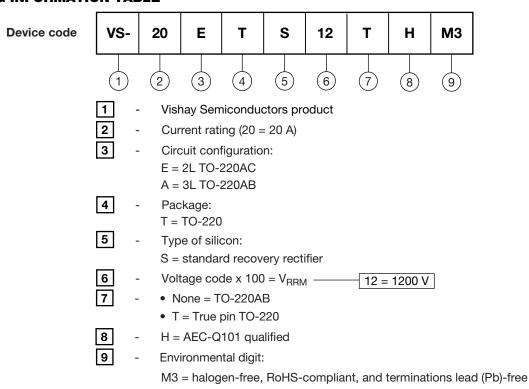


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-20ETS12THM3	50	1000	Antistatic plastic tubes			
VS-20ATS12HM3	50	1000	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS					
Dimensions	2L TO-220AC	www.vishay.com/doc?96069			
Dimensions	3L TO-220AB	www.vishay.com/doc?95222			
Part marking information	2L TO-220AC	www.vishay.com/doc?95391			
Part marking information	3L TO-220AB	www.vishay.com/doc?95028			



VS-20ETS12THM3, VS-20ATS12HM3

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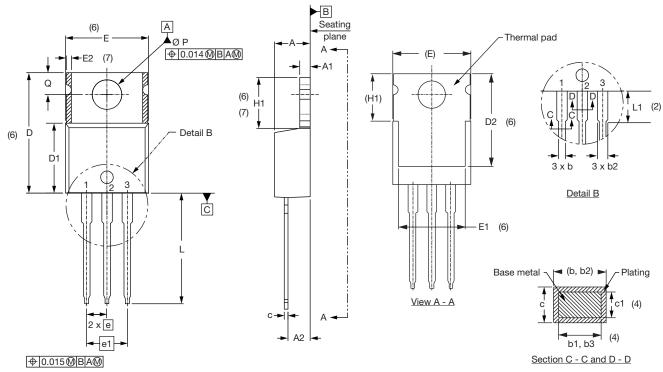
SPICE model	www.vishav.com/doc?96046
of for model	www.visitay.com/doc:30040



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TO-220AB

DIMENSIONS in millimeters and inches



Lead tip

Lead assignments

<u>Diodes</u>

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220AB

	IETERS	INC	HES	NOTES
MIN.	MAX.	MIN.	MAX.	NOTES
4.25	4.65	0.167	0.183	
1.14	1.40	0.045	0.055	
2.56	2.92	0.101	0.115	
0.69	1.01	0.027	0.040	
0.38	0.97	0.015	0.038	4
1.20	1.73	0.047	0.068	
1.14	1.73	0.045	0.068	4
0.36	0.61	0.014	0.024	
0.36	0.56	0.014	0.022	4
14.85	15.25	0.585	0.600	3
8.38	9.02	0.330	0.355	
11.68	12.88	0.460	0.507	6
	4.25 1.14 2.56 0.69 0.38 1.20 1.14 0.36 0.36 14.85 8.38	4.25 4.65 1.14 1.40 2.56 2.92 0.69 1.01 0.38 0.97 1.20 1.73 1.14 1.73 0.36 0.61 0.36 0.56 14.85 15.25 8.38 9.02	4.25 4.65 0.167 1.14 1.40 0.045 2.56 2.92 0.101 0.69 1.01 0.027 0.38 0.97 0.015 1.20 1.73 0.047 1.14 1.73 0.045 0.36 0.61 0.014 0.36 0.56 0.014 14.85 15.25 0.585 8.38 9.02 0.330	4.25 4.65 0.167 0.183 1.14 1.40 0.045 0.055 2.56 2.92 0.101 0.115 0.69 1.01 0.027 0.040 0.38 0.97 0.015 0.038 1.20 1.73 0.047 0.068 1.14 1.73 0.045 0.068 0.36 0.61 0.014 0.024 0.36 0.56 0.014 0.022 14.85 15.25 0.585 0.600 8.38 9.02 0.330 0.355

SYMBOL	MILLIN	METERS INCHES		NOTES	
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Е	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2		0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	

Notes

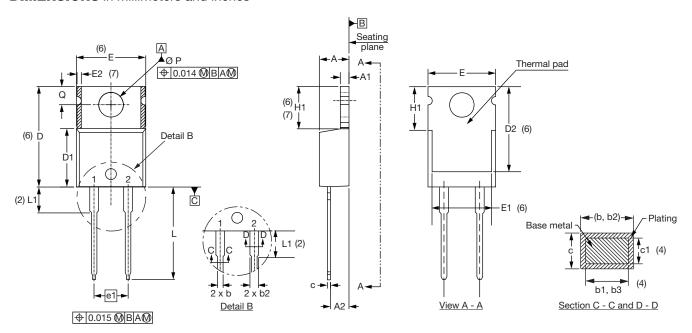
- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



Vishay Semiconductors

2L TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	

Notes

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- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC® TO-220, except D2, where JEDEC® minimum is 0.480".



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