VS-2ENH01-M3, VS-2ENH02-M3

Vishay Semiconductors





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Cathode O Anode

LINKS TO ADDITIONAL RESOURCES



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PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 A				
V _R	100 V, 200 V				
V _F at I _F	0.79 V				
I _{FSM}	40 A				
t _{rr} (typ.)	23 ns				
T _J max.	175 °C				
Package	SMP (DO-220AA)				
Circuit configuration	Single				

FEATURES

- Very low profile typical height of 1.0 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- For PFC, CRM snubber operation
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATION

For use in high frequency, freewheeling, DC/DC converters, PFC, and in snubber industrial and automotive applications.

MECHANICAL DATA

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002, meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse	VS-2ENH01-M3	V		100	V	
voltage	VS-2ENH02-M3	V _{RRM}		200	v	
Average rectified forward	current	I _{F(AV)}	T _C = 158 °C	2	٨	
Non-repetitive peak surge current		I _{FSM}	T _J = 25 °C, 10 ms sine pulse	40	A	
Operating junction and storage temperatures		T _J , T _{Stg}		-55 to +175	°C	

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage,	VS-2ENH01-M3	V _{BR} ,	I _B = 100 μA	100	-	-		
blocking voltage	VS-2ENH02-M3	V _R	$I_R = 100 \mu A$	200	-	-	v	
Forward voltage		VF	I _F = 2 A	-	0.94	1.00	v	
Forward voltage		۷F	I _F = 2 A, T _J = 150 °C	-	0.79	0.84		
Reverse leakage current		I _R	V _R = V _R rated	-	-	2		
			$T_J = 150 \ ^{\circ}C, V_R = V_R \text{ rated}$	-	-	20	μΑ	
Junction capacitance		CT	V _R = 200 V	-	8	-	pF	

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CON	IDITIONS	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 1$	-	23	-			
Reverse recovery time	+	I _F = 0.5 A, I _R = 1 A, I	-	-	28			
	t _{rr}	T _J = 25 °C		-	16	-	A nC	
		T _J = 125 °C	I _F = 2 A dI _F /dt = 200 A/μs V _B = 100 V	-	25	-		
Deale receiver a current		T _J = 25 °C		-	2.0	-		
Peak recovery current	IRRM	T _J = 125 °C		-	3.1	-		
Reverse recovery charge	0	T _J = 25 °C		-	15	-		
	Q _{rr}	T _J = 125 °C		-	37	-		

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction temperature ran	0	T _J , T _{Stg}		-55	-	175	°C	
Thermal resistar junction to mour	,	R _{thJM} ⁽¹⁾	R _{thJM} ⁽¹⁾ Infinite heatsink		7	9	°C/W	
Thermal resistar junction to ambi	,	R _{thJA}	PCB footprint 4.8 mm x 4.8 mm	-	107	-	0/10	
Marking device	VS-2ENH01-M3	Case style SMR (DO 22044) 2H1				-11	•	
IVIAI KII IY UEVICE	VS-2ENH02-M3		Case style SMP (DO-220AA)	2H2				

Note

⁽¹⁾ Thermal resistance junction to mount follows JEDEC[®] 51-14 transient dual interface test method (TDIM)

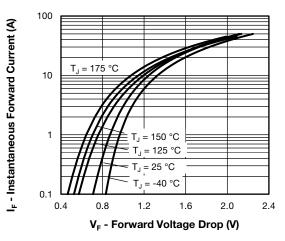


Fig. 1 - Typical Forward Voltage Drop Characteristics

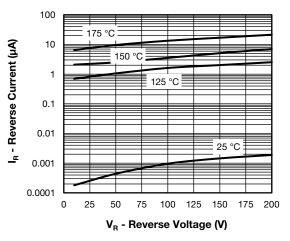


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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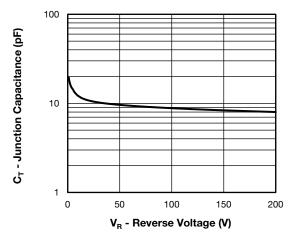


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

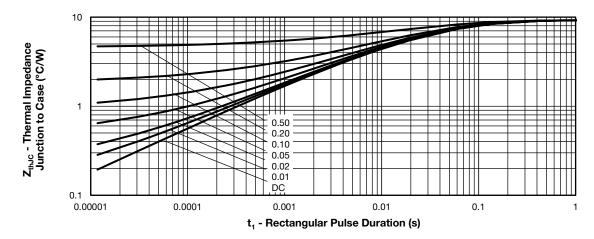
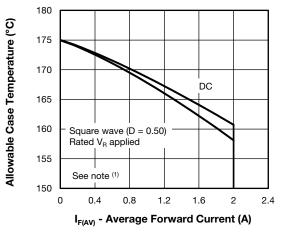
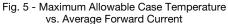


Fig. 4 - Transient Thermal Impedance, Junction to Case





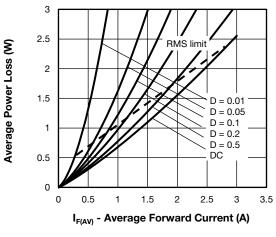


Fig. 6 - Forward Power Loss Characteristics

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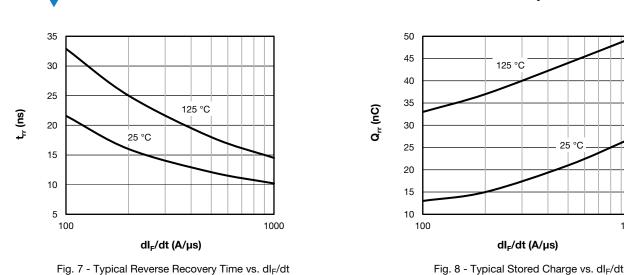
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25 °C

1000



Note

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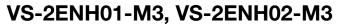
⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

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- Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 5); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R
 - (3) 0 ~ Q_{rr} ⁽⁴⁾ (2) 0.5 I_{RRM} IRRM i_{(rec)M}/dt (5) 0.75 I_{RRM} (1) di_F/dt (1) di_F/dt - rate of change of current (4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM} through zero crossing $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$ (2) I_{RRM} - peak reverse recovery current 2 (3) $\rm t_{\rm rr}$ - reverse recovery time measured (5) di_{(rec)M}/dt - peak rate of change of from zero crossing point of negative going I_F to point where a line passing current during t_b portion of t_{rr} through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.

Fig. 9 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

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Device code	VS-	2	E	N	н	02	М3
		2	3	4	5	6	7
	1	- Visl	nay Sen	niconduo	ctors pro	oduct	
	2	- Cur	rent rati	ng (2 = 2	2 A)		
	3	- Circ	cuit conf	iguratior	า:		
		E =	single c	liode			
	4	- N =	SMP pa	ackage			
	5	- Pro	cess typ	e,			
	_	H =	ultrafas	t recove	ery		
	6	- Voli	tage coo	de (02 =	200 V)		
	7	- M3	= halog	en-free,	RoHS-0	complia	nt, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-2ENH01-M3/84A	84A	3000	7" diameter plastic tape and reel				
VS-2ENH01-M3/85A	85A	10 000	13" diameter plastic tape and reel				
VS-2ENH02-M3/84A	84A	3000	7" diameter plastic tape and reel				
VS-2ENH02-M3/85A	85A	10 000	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96547				
Part marking information	www.vishay.com/doc?96574				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96551				

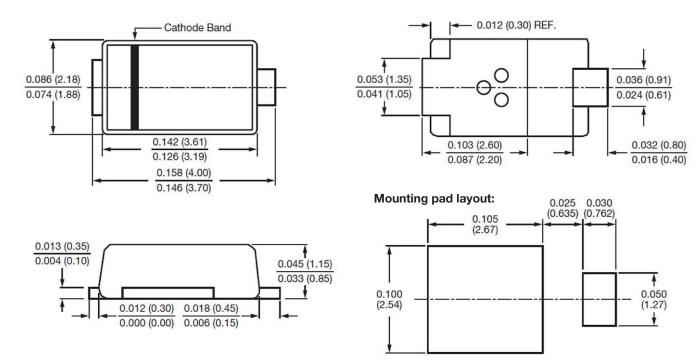


Outline Dimensions

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SMP (DO-220AA)

DIMENSIONS in inches (millimeters)





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