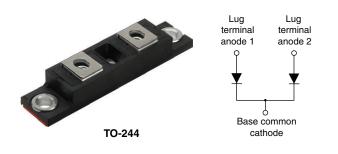
Vishay Semiconductors

High Performance Schottky Rectifier, 300 A



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PRIMARY CHARACTERISTICS				
I _{F(AV)} 300 A				
V _R	100 V			
Package	TO-244			
Circuit configuration Two diodes common cathode				

FEATURES

- 175 °C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-303CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS VALUES UN				
I _{F(AV)}	Rectangular waveform	300	А		
V _{RRM}		100	V		
IFSM	t _p = 5 μs sine	22 000	А		
V _F	150 A_{pk} , T_J = 125 °C (per leg)	0.72	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-303CNQ100PbF	UNITS	
Maximum DC reverse voltage	VR	100	V	
Maximum working peak reverse voltage	V _{RWM}	100	v	

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS			
Maximum average	per leg		50 % duty cycle at T _C = 138 °C, rectangular waveform –		150 % duty such at T 120 %C restance/law waveform		L 50 % duty avalant T 120 °C m	150	
See fig. 5	per device	IF(AV)			300	А			
Maximum peak one cycle surge current per leg	non-repetitive		5 μs sine or 3 μs rect. pulseFollowing any rated load condition and with rated10 ms sine or 6 ms rect. pulseVRRM applied		22 000	A			
See fig. 7		IFSM			2500				
Non-repetitive avalanche	energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 13 A, L = 0.2 mH		15	mJ			
Repetitive avalanche curre	ent per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А			

RoHS

COMPLIANT

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VS-303CNQ100PbF

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ELECTRICAL SPECIFICATIONS

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONE	VALUES	UNITS	
		150 A	T.I = 25 °C	0.91	V
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	300 A	1j=25 C	1.09	
See fig. 1		150 A	T 105 %C	0.72	
		300 A	T _J = 125 °C	0.85	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	4.5	mA
See fig. 2		T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	80	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		4150	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		6.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage tempe	erature range	T _J , T _{Stg}	-55	-	175	°C
per leg		Р	-	-	0.28	
Thermal resistance, junction to case	per module	R _{thJC}	-	-	0.14	°C/W
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-	
Weight			-	68	-	g
			-	2.4	-	oz.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)
Terminal torque			30 (3.4)	-	44.2 (5)	()
Vertical pull			-	-	80	llaf in
2" lever pull			-	-	35	lbf · in

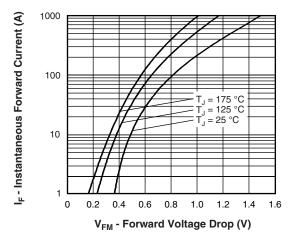
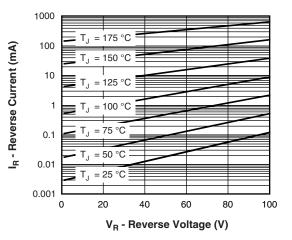
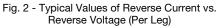


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





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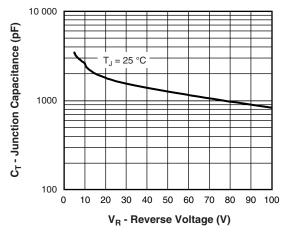


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

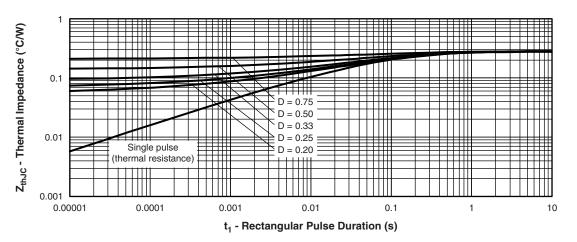


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

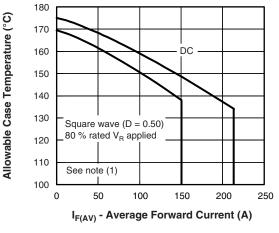


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

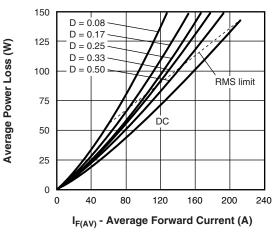


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

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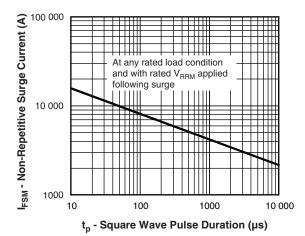


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

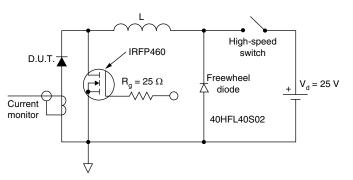


Fig. 8 - Unclamped Inductive Test Circuit

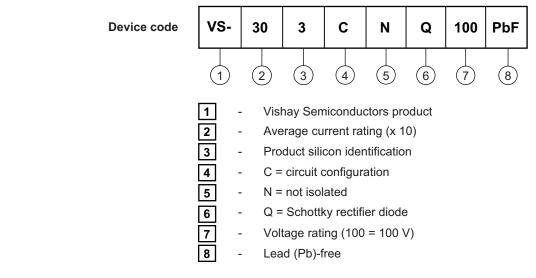
Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6);

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 Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

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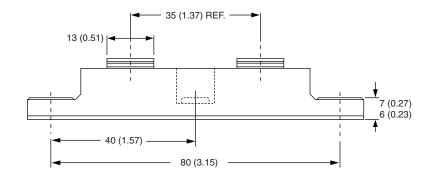


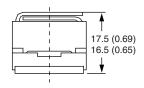


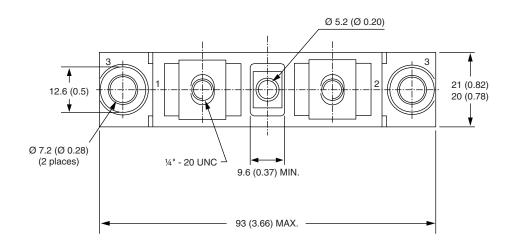
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TO-244

DIMENSIONS in millimeters (inches)









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