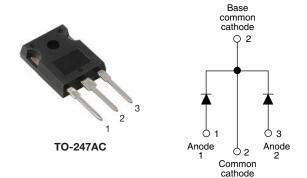


FRED Pt® Ultrafast Rectifier, 2 x 15 A



PRODUCT SUMMARY								
Package	TO-247AC							
I _{F(AV)}	2 x 15 A							
V_{R}	400 V							
V _F at I _F	0.93 V							
t _{rr} typ.	See Recovery table							
T _J max.	175 °C							
Diode variation	Common cathode							

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS COMPLIANT HALOGEN FREE

DESCRIPTION / APPLICATION

FRED Pt® series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		ETER SYMBOL TEST CONDITIONS		VALUES	UNITS				
Peak repetitive reverse voltage		V _{RRM}		400	V				
Average restified forward correct	per leg	- I _{F(AV)}		15					
Average rectified forward current	total device		Rated V _R , T _C = 149 °C	30	^				
Non-repetitive peak surge current per leg		I _{FSM}	T _C = 25 °C	200	А				
Peak repetitive forward current per leg		I _{FRM}	Rated V _R , T _C = 149 °C, square wave, 20 kHz	30					
Operating junction and storage temperatures		T _J , T _{Stg}		-65 to +175	°C				

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	400	-	-					
Forward voltage	V _F	I _F = 15 A -		1.17	1.25	V				
		I _F = 15 A, T _J = 150 °C	-	0.93	1.12					
Davis and Indiana account	I _R	$V_R = V_R$ rated	-	0.3	10					
Reverse leakage current		T _J = 150 °C, V _R = V _R rated	-	30	500	μΑ				
Junction capacitance	C _T	V _R = 400 V - 28 -		pF						
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	12	-	nH				





DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS				
Reverse recovery time	t _{rr}	$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	36	-				
		T _J = 25 °C		-	46	-	ns			
		T _J = 125 °C		-	80	-				
Peak recovery current	I _{RRM}	T _J = 25 °C	$I_F = 15 \text{ A}$ $dI_F/dt = 200 \text{ A/µs}$ $V_R = 200 \text{ V}$	-	3.6	-	A			
		T _J = 125 °C		-	8.7	-				
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	84	-	nC			
		T _J = 125 °C		-	345	-				

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C				
Thermal resistance, junction to case per leg	R _{thJC}		-	0.8	1.5					
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	40	°C/W				
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-					
Weight			-	6.0	-	g				
vveignt			-	0.21	-	OZ.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style TO-247AC	30CPU04H							

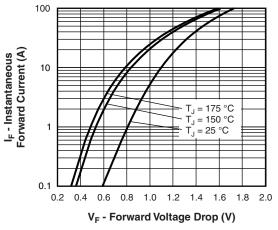


Fig. 1 - Typical Forward Voltage Drop Characteristics

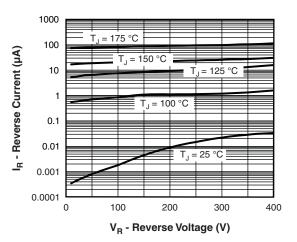


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

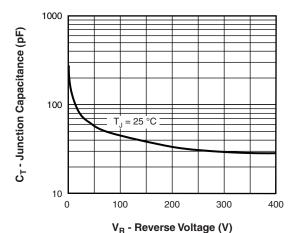


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

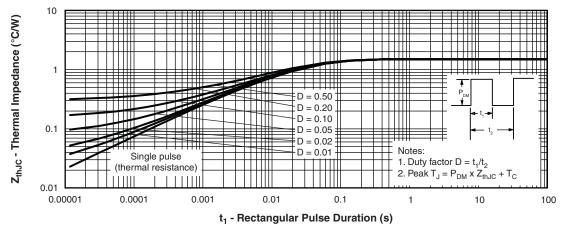


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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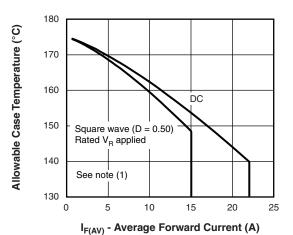


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

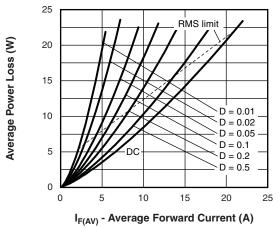


Fig. 6 - Forward Power Loss Characteristics

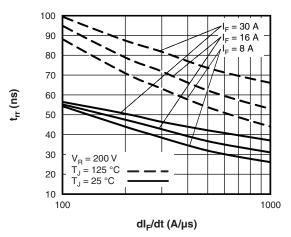


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

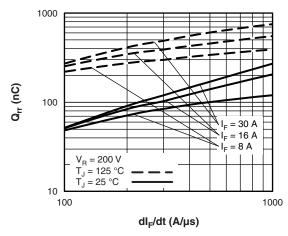
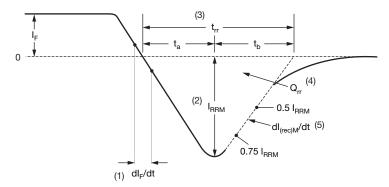


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = Rated \ V_R \end{array}$



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) ${\rm Q_{rr}}$ area under curve defined by ${\rm t_{rr}}$ and ${\rm I_{RRM}}$

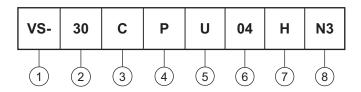
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (30 = 30 A)
- 3 Circuit configuration: C = common cathode
- **4** TO-247AC
- 5 Ultrafast recovery
- 6 Voltage rating (04 = 400 V)
- 7 H = AEC-Q101 qualified
- 8 Environmental digit:

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

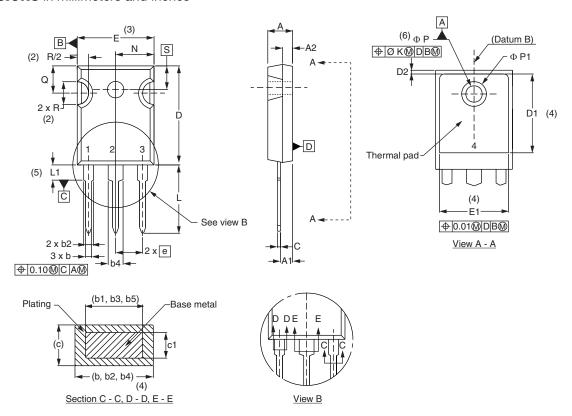
ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-30CPU04HN3	25	500	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95542</u>						
Part marking information	TO-247AC-N3	www.vishay.com/doc?95007				



TO-247 - 50 mils L/F

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		ILLIMETERS INCHES NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES		
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215 BSC		
b1	0.99	1.35	0.039	0.053		Ø K 0.254 0.0)10				
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0.3		
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	7.39	-	0.291	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	'BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}$ Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



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Revision: 02-Oct-12 Document Number: 91000

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