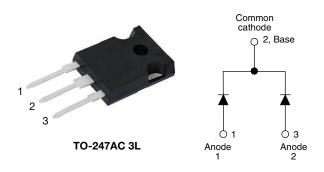
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Ultrafast Rectifier, 2 x 15 A FRED Pt®



PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 15 A							
V _R	400 V							
V _F at I _F	0.93 V							
t _{rr} typ.	See Recovery table							
T _J max.	175 °C							
Package	TO-247AC 3L							
Circuit configuration	Common cathode							

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

FRED Pt[®] series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Peak repetitive reverse voltage	V _{RRM}		400	V					
Average rectified forward current			15						
total devic	IF(AV)	Rated V _R , T _C = 149 °C	30	А					
Non-repetitive peak surge current per leg	I _{FSM}	$T_{C} = 25 \text{ °C}, t_{p} = 10 \text{ ms}$	200	A					
Peak repetitive forward current per leg	I _{FRM}	Rated V_R , T_C = 149 °C, square wave, 20 kHz	30						
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C					

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	400	-	-				
Forward voltage	V _F	I _F = 15 A	-	1.17	1.25	V			
		I _F = 15 A, T _J = 150 °C	-	0.93	1.12				
Reverse leakage current	I _R	$V_{R} = V_{R}$ rated	-	0.3	10				
neverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	30	500	μA			
Junction capacitance	CT	C _T V _R = 400 V		28	-	pF			
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	12	-	nH			

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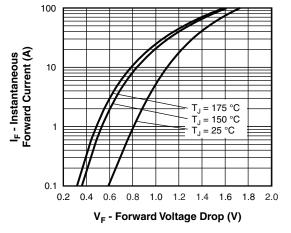
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
	t _{rr}	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	36	60			
Reverse recovery time		T _J = 25 °C		-	46	-	ns • A		
		T _J = 125 °C		-	80	-			
Peak recovery current	I _{RRM}	T _J = 25 °C	$I_{\rm F} = 15 {\rm A}$	-	3.6	-			
		T _J = 125 °C	dl _F /dt = 200 A/µs V _B = 200 V	-	8.7	-			
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	84	-	nC		
		T _J = 125 °C		-	345	-			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C				
Thermal resistance, junction to case per leg	R _{thJC}		-	0.8	1.5					
Thermal resistance, junction to ambient per leg		Typical socket mount	-	-	40	°C/W				
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-					
Weight			-	6.0	-	g				
weight			-	0.21	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style TO-247AC 3L	30CPU04							

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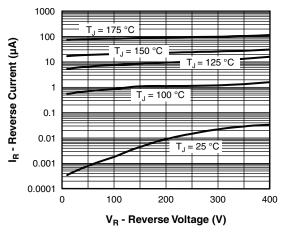
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Fig. 1 - Typical Forward Voltage Drop Characteristics





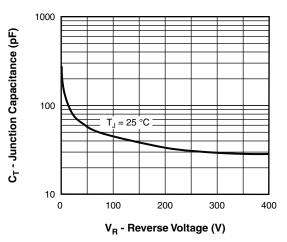


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

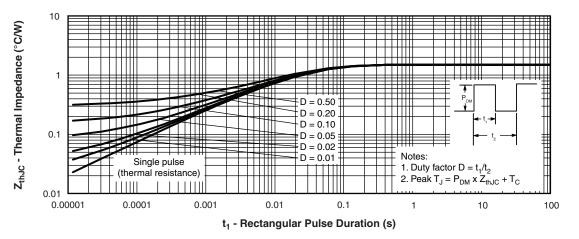
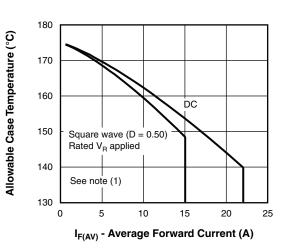


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

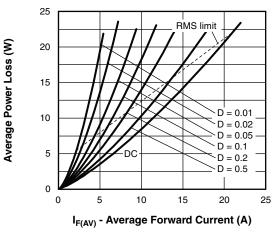


Fig. 6 - Forward Power Loss Characteristics

Note

 $^{(1)} \mbox{ Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \mbox{ forward power loss = } I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = \ inverse \ power \ loss = V_{R1} \ x \ I_R \ (1 \ - D); \ I_R \ at \ V_{R1} = \ rated \ V_R$

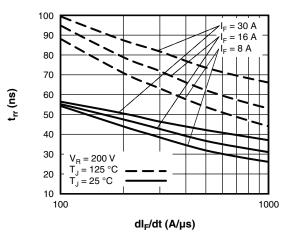
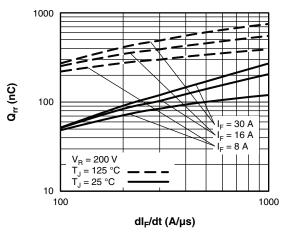


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt





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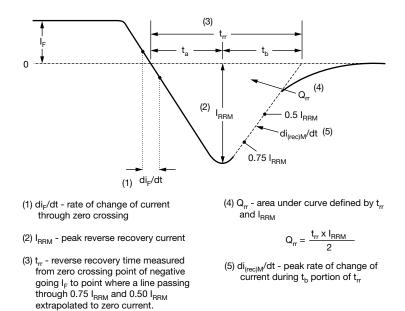
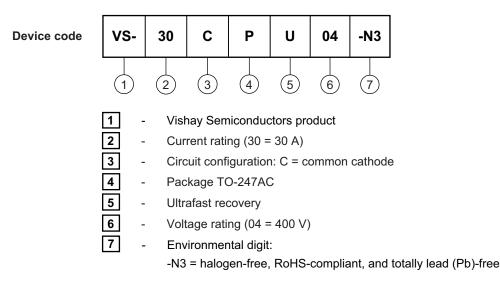


Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

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ORDERING INFORMATION (Example)									
PREFERRED P/N	PACKAGING DESCRIPTION								
VS-30CPU04-N3	25	500	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?96138						
Part marking information	www.vishay.com/doc?95007						

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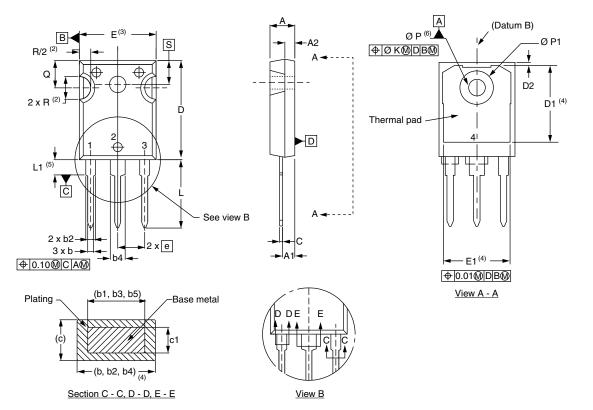


Outline Dimensions

Vishay Semiconductors

TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES		SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBOL	MIN.	MAX.	MIN.	MAX.	
А	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0	010	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØΡ	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	7.39	-	0.291	
с	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3]	S	5.51 BSC 0.2"		0.217	' BSC	
D1	13.08	-	0.515	-	4]						

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

(6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension Q

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