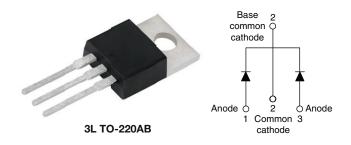
VS-32CTQ025-M3, VS-32CTQ030-M3

**Vishay Semiconductors** 

# High Performance Schottky Rectifier, 2 x 15 A



www.vishay.com

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 15 A			
V <sub>R</sub>	25 V, 30 V			
V <sub>F</sub> at I <sub>F</sub>	0.40 V			
I <sub>RM</sub> max.	97 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
E <sub>AS</sub>	13 mJ			
Package	3L TO-220AB			
Circuit configuration	Common cathode			

### **FEATURES**

- 150 °C T<sub>.</sub> operation
- Low forward voltage drop
- High frequency operation



COMPLIANT

- High nequency operation
  High purity, high temperature epoxy FREE encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## DESCRIPTION

The VS-32CTQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS VALUES UN				
I <sub>F(AV)</sub>	Rectangular waveform	30	А		
V <sub>RRM</sub>		25/30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	900	А		
V <sub>F</sub>	15 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.40	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-32CTQ025-M3	VS-32CTQ030-M3	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	25	30	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	20	30	v	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS		
Maximum average forward current, see fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 115 °C	30			
Maximum peak one cycle non-repetitive		5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with rated	900	А	
surge current, see fig. 7		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	250		
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.20 A, L = 11.10 mH		13	mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3	А	

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### ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST	VALUES	UNITS	
Maximum forward voltage drop See fig. 1		15 A	T <sub>.1</sub> = 25 °C	0.49	V
		30 A	1j=25 C	0.58	
	V <sub>FM</sub> <sup>(1)</sup>	15 A	T <sub>J</sub> = 125 °C	0.40	
		30 A		0.53	
Maximum reverse leakage current	I <sub>BM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated V <sub>B</sub>	1.75	mA
See fig. 2	'RM \''	T <sub>J</sub> = 125 °C	V <sub>R</sub> = naleu V <sub>R</sub>	97	
Threshold voltage	V <sub>F(TO)</sub>	T <sub>J</sub> = T <sub>J</sub> maximum		0.233	V
Forward slope resistance	r <sub>t</sub>			9.09	mΩ
Maximum junction capacitance per leg	CT	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1300	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>B</sub>		10 000	V/µs

### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stora temperature range	age	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C	
Maximum thermal resistanc junction to case per leg	e,	R <sub>thJC</sub>	DC operation See fig. 4	3.25	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased 0		0/11	
Approvimento uscient			2	g		
Approximate weight				0.07	OZ.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf ⋅ in)	
			32CT	Q025		
Marking device			Case style 3L TO-220AB	32CT	Q030	



## VS-32CTQ025-M3, VS-32CTQ030-M3

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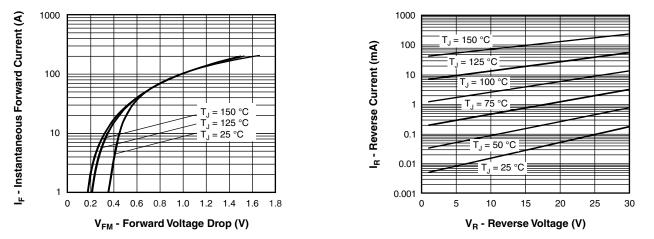


Fig. 1 - Maximum Forward Voltage Drop Characteristics



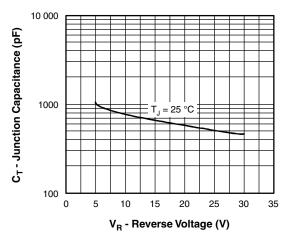
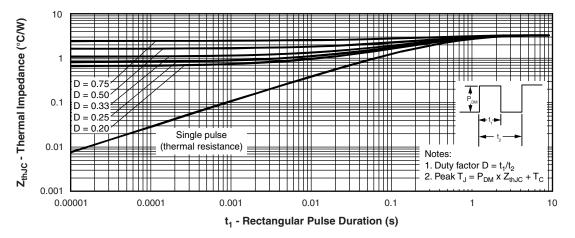
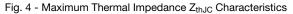
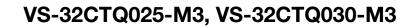


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



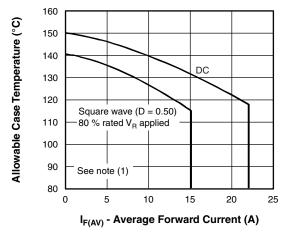


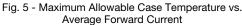
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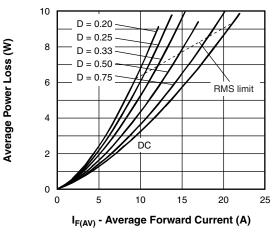




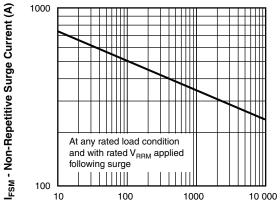
## **Vishay Semiconductors**











t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

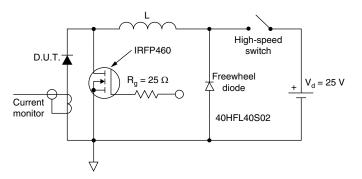


Fig. 8 - Unclamped Inductive Test Circuit

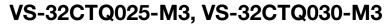
#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R at V_{R1} = 80 \%$  rated  $V_R$ 

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## Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code VS-	32 C	т	Q	030	-M3
	2 3	4	5	6	7
1    -      2    -      3    -      4    -	Vishay Sem Current ratir Circuit confi C = common Package: T = TO-220	ng (30 A guration n cathoo	)	oduct	
5 - 6 - 7 -	Schottky "Q Voltage ratir Environmen -M3 = halog	ngs — tal digit		[	025 = 2 030 = 3

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-32CTQ025-M3	50	1000	Antistatic plastic tube			
VS-32CTQ030-M3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?96154					
Part marking information	www.vishay.com/doc?95028				



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