VS-3EYH01-M3, VS-3EYH02-M3

Vishay Semiconductors

COMPLIANT

HALOGEN

FREE

Hyperfast Rectifier, 3 A FRED Pt®

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SlimSMAW (DO-221AD)

Bottom View

LINKS TO ADDITIONAL RESOURCES

Top View



PRIMARY CHARACTERISTICS					
I _{F(AV)}	3 A				
V_R	100 V, 200 V				
V _F at I _F	0.71 V				
I _{FSM}	70 A				
t _{rr} (typ.)	16 ns				
T _J max.	175 °C				
Package	SlimSMAW (DO-221AD)				
Circuit configuration	Single				

FEATURES

- · Low profile package
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Class 2 whisker test
- Compatible to SOD-128 package case outline
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

For use in high frequency, freewheeling, DC/DC converters, PFC, and in snubber industrial, and automotive applications.

MECHANICAL DATA

Case: SlimSMAW

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage VS-3EYH01-M3			100	V	
VS-3EYH02-M3	V _{RRM}		200	V	
Average rectified forward current	I _{F(AV)} (1)	T _C = 137 °C	3	Α	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C, 10 ms sine pulse wave	70		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

Note

(1) Mounted on infinite heatsink

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking VS-3EYH01-M		V_{BR}, V_{R}	1. 100	100	-	-	
voltage	VS-3EYH02-M3		I _R = 100 μA	200	-	-	v
Forward voltage, per diode		V_{F}	I _F = 3 A	-	0.86	0.95	
		٧F	I _F = 3 A, T _J = 150 °C	-	0.71	0.79	
Reverse leakage current, per diode		1	V _R = V _R rated	-	-	2	μΑ
		ЧR	I_R $T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{ rated}$	-	-	20	
Junction capacitance		C _T	V _R = 200 V	-	16	-	pF

VS-3EYH01-M3, VS-3EYH02-M3

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time		$I_F = 1.0 \text{ A, } dI_F/dt =$	= 50 A/µs, V _R = 30 V	1	22	-	
		$I_F = 1.0 \text{ A}, dI_F/dt =$	= 100 A/µs, V _R = 30 V	-	16	-	
	t _{rr}	$I_F = 0.5 \text{ A}, I_R = 1 \text{A}, I_{rr} = 0.25 \text{ A}$		-	-	30	ns
		T _J = 25 °C	I _F = 3 A,	-	18	-	
		T _J = 125 °C		-	30	-	
Peak recovery current		T _J = 25 °C		-	2.5	-	
	IRRM	I_{RRM} $T_{J} = 125 ^{\circ}C$ $V_{R} = 100 ^{\circ}V$	-	4	-	Α	
Reverse recovery charge	0	T _J = 25 °C] ''	-	23	-	0
	Q_{rr}	T 405.00	1		00		nC

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		-55	=.	175	°C
Thermal resistance, junction to mount		R _{thJM} ⁽¹⁾	Infinite heatsink	-	12	15	
Thermal resistance, junction to ambient		R _{thJA}	Device mounted on FR4 PCB, 2 oz. standard footprint	-	120	150	°C/W
Marking device VS-3EYH01-M3 VS-3EYH02-M3			Case atula SlimSMAW (DO 221AD)	SlimSMAW (DO-221AD) 3H1 3H2		•	
			Case style SilitiSiviAW (DO-22TAD)			1 2	

Note

⁽¹⁾ Thermal resistance junction to mount follows JEDEC® 51-14 transient dual interface test method (TDIM)

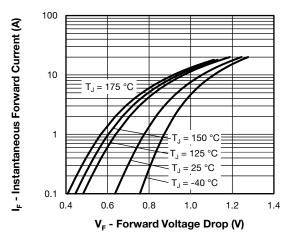


Fig. 1 - Typical Forward Voltage Drop Characteristics

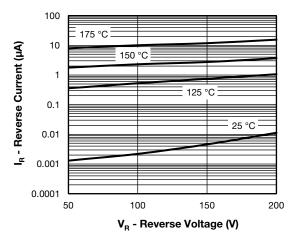


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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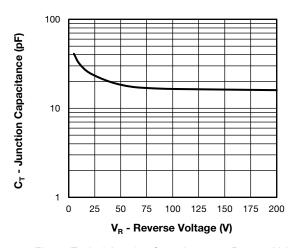


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

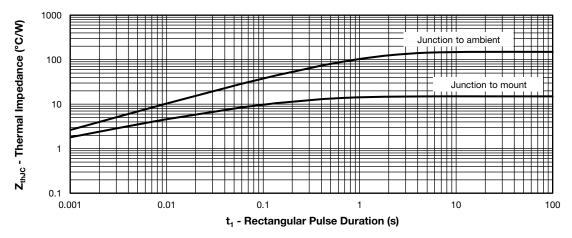


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

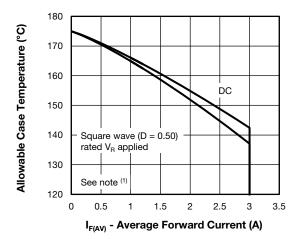


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

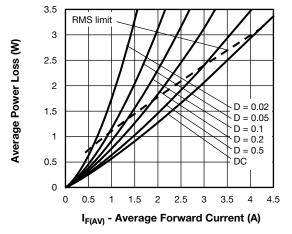


Fig. 6 - Forward Power Loss Characteristics

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = \text{forward power loss} = I_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/\text{D}) \text{ (see fig. 5)}; \\ \text{Pd}_{\text{REV}} = \text{inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } \text{V}_{\text{R1}} = \text{rated V}_{\text{R}} \\ \end{array}$

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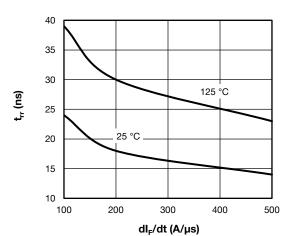


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

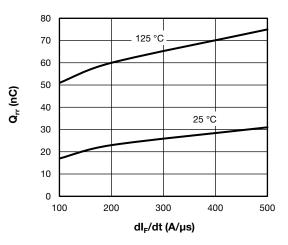
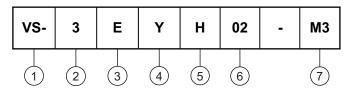


Fig. 8 - Typical Stored Charge vs. dl_F/dt

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (3 = 3 A)

3 - Circuit configuration:

E = single diode

- Y = SlimSMAW (DO-221AD)

5 - Process type,

H = hyperfast recovery

6 - Voltage code (02 = 200 V)

- M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	PACKAGING DESCRIPTION			
VS-3EYH01-M3/H	0.033	Н	3500	7"diameter plastic tape and reel			
VS-3EYH01-M3/I	0.033	I	14 000	13"diameter plastic tape and reel			
VS-3EYH02-M3/H	0.033	Н	3500	7"diameter plastic tape and reel			
VS-3EYH02-M3/I	0.033	I	14 000	13"diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96582</u>					
Part marking information	www.vishay.com/doc?95562				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96586				



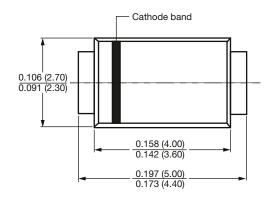


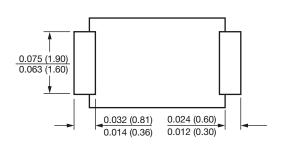
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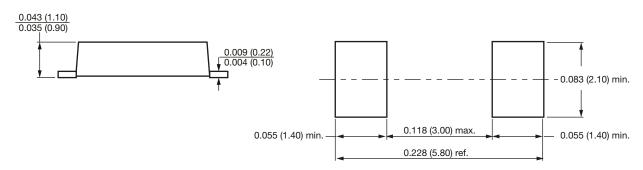
SlimSMAW (DO-221AD)

DIMENSIONS in inches (millimeters)

SlimSMAW (DO-221AD)







Mounting pad layout



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