# VS-400U(R) Series

**Vishay Semiconductors** 



## **Standard Recovery Diodes** (Stud Version), 400 A



PRODUCT SUMMARY				
I <sub>F(AV)</sub> 400 A				
Package	DO-205AB (DO-9)			
Circuit configuration Single diode				

## **FEATURES**

- Wide current range
- · High surge current capabilities
- · Stud cathode and stud anode version
- Standard JEDEC<sup>®</sup> types
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **TYPICAL APPLICATIONS**

- Converters
- Power supplies
- Machine tool controls
- High power drives

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
		400	A		
I <sub>F(AV)</sub>	T <sub>C</sub>	120	°C		
I <sub>F(RMS)</sub>		630	A		
I <sub>FSM</sub>	50 Hz	8250	٨		
	60 Hz	8640	— A		
l <sup>2</sup> t	50 Hz	340	kA <sup>2</sup> s		
1-1	60 Hz	311	— ка²s		
V <sub>RRM</sub>	Range	800 to 1600	V		
TJ		-40 to 200	°C		

### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS							
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> MAXIMUM AT T <sub>J</sub> = T <sub>J</sub> MAXIMUM mA			
	80	800	900				
VS-400U(R) 120		1200	1300	15			
	160	1600	1700				

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# VS-400U(R) Series



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FORWARD CONDUCTION								
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS		
Maximum average forward current				180° conduction, half sine wave			400	A
at case temperature	I <sub>F(AV)</sub>		ion, nan sine wa	ve	120	°C		
Maximum RMS forward current	I <sub>F(RMS)</sub>	DC at 110 °C	case temperatui	e	630	A		
		t = 10 ms	No voltage		8250	A		
Maximum peak, one cycle forward,		t = 8.3 ms	reapplied	Sinusoidal half wave, initial T <sub>J</sub> = T <sub>J</sub> maximum	8640			
non-repetitive surge current	I <sub>FSM</sub>	t = 10 ms	100 % V <sub>BBM</sub>		6940			
		t = 8.3 ms	reapplied		7270			
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	t = 10 ms	No voltage reapplied		340	- kA <sup>2</sup> s		
		t = 8.3 ms			311			
		t = 10 ms	100 % V <sub>RRM</sub> reapplied		241			
		t = 8.3 ms			220			
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied			3400	kA²√s		
Low level value of threshold voltage	V <sub>F(TO)1</sub>	(16.7 % x $\pi$ x I <sub>F(AV)</sub> < I < $\pi$ x I <sub>F(AV)</sub> ), T <sub>J</sub> = T <sub>J</sub> maximum			0.77	v		
High level value of threshold voltage	V <sub>F(TO)2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J maximum$			0.85	v		
Low level value of forward slope resistance	r <sub>f1</sub>	(16.7 % x $\pi$ x $I_{F(AV)} < I < \pi$ x $I_{F(AV)}),$ $T_{J}$ = $T_{J}$ maximum			0.49	mΩ		
High level value of forward slope resistance	r <sub>f2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J maximum$			0.49	11152		
Maximum forward voltage drop	V <sub>FM</sub>	I <sub>pk</sub> = 1500 A, T	J = TJ maximum	, t <sub>p</sub> = 10 ms sinusoidal wave	1.62	V		

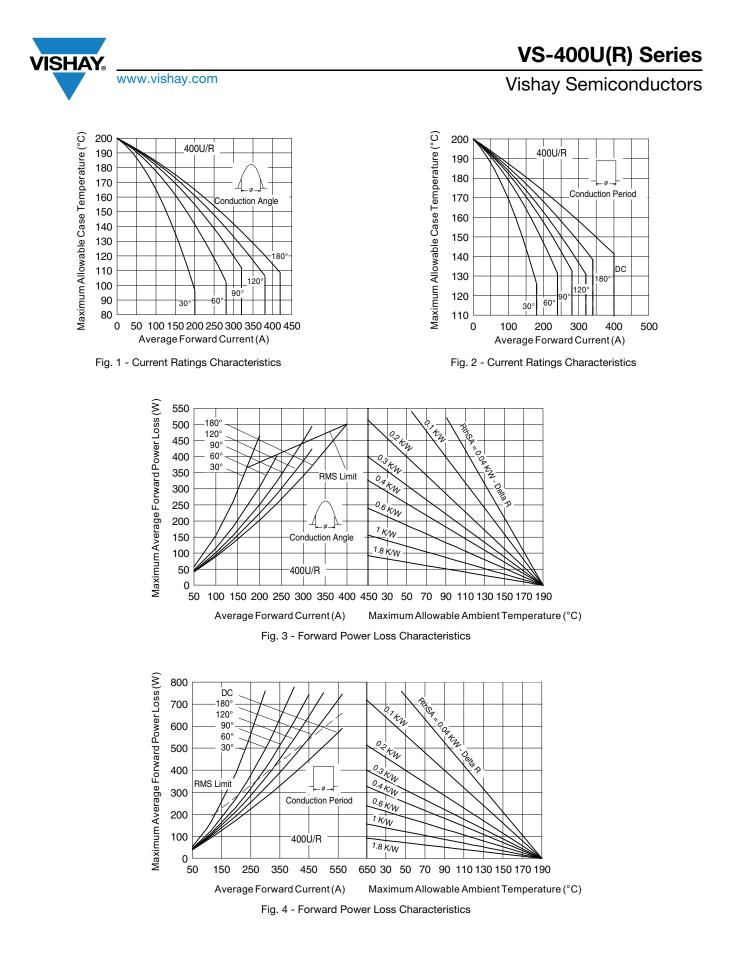
THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	BOL TEST CONDITIONS		UNITS
Maximum junction operating and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-40 to 200	°C
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	R <sub>thJC</sub> DC operation		K/W
Maximum thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth, flat and greased		r./ vv
Maximum allowed mounting torque ± 10 %		Not lubricated threads 27		N·m
Approximate weight			250	g
Case style		See dimensions - link at the end of datasheet DO-205AB (DO-9)		3 (DO-9)

CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS		
180°	0.020	0.013				
120°	0.023	0.023				
90°	0.029	0.031	$T_J = T_J maximum$	K/W		
60°	0.042	0.044				
30°	0.073	0.074				

Note

The table above shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC

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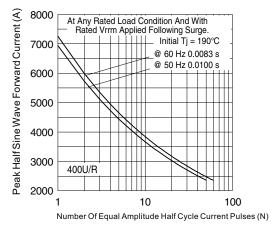


Fig. 5 - Maximum Non-Repetitive Surge Current

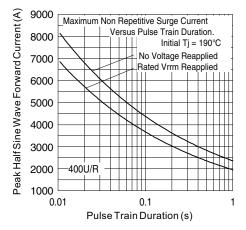


Fig. 6 - Maximum Non-Repetitive Surge Current

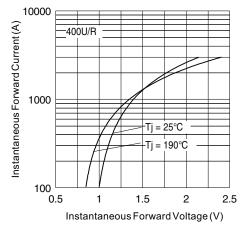
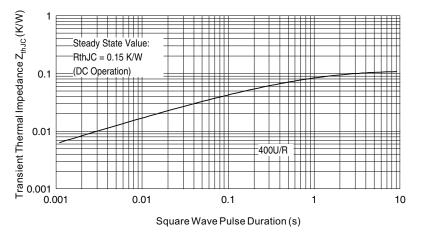
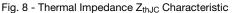


Fig. 7 - Forward Voltage Drop Characteristics





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#### **ORDERING INFORMATION TABLE**

Device code	vs-	40	0	U	R	160	D	
	1	2	3	4	5	6	7	
	1 -	· Vish	iay Sem	iconduc	tors pro	duct		
	2 - 40 = Essential part number							
	3 -	0 = 3	Standar	d recove	ery devi	ce		
	4 - U = Stud normal polarity (cathode to stud)							
	5 -	• No	one = St	ud norm	nal pola	rity (cath	node to	stud)
		• R	= Stud r	everse	polarity	(anode	to stud)	
	6 -	Volt	age cod	e x 10 =	· V <sub>RRM</sub> (	see Vol	tage Ra	atings table)
	7 -	Diffu	used dio	de				

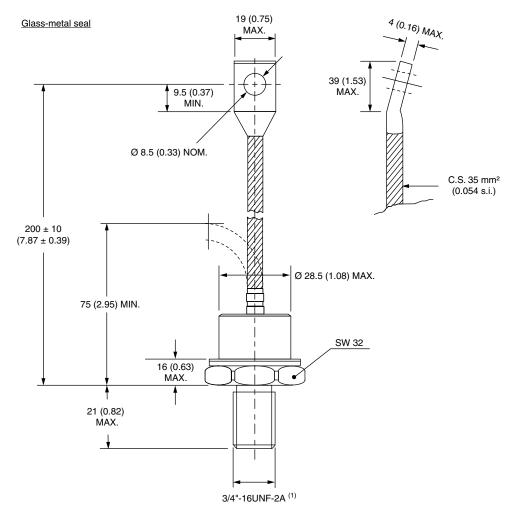
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95339			

**Vishay Semiconductors** 



DO-205AB (DO-9) for 400U(R) Series

#### **DIMENSIONS** in millimeters (inches)



#### Note

• For metric device: M16 x 1.5 contact factory



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