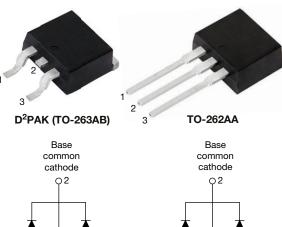
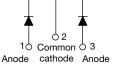
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**Vishay Semiconductors** 

# High Performance Schottky Rectifier, 2 x 20 A





ሪዖ 10 Common 0 3 Anode cathode Anode

VS-42CTQ030S-M3

VS-42CTQ030-1-M3

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 20 A				
V <sub>R</sub>	30 V				
V <sub>F</sub> at I <sub>F</sub>	0.38 V				
I <sub>RM</sub>	183 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
E <sub>AS</sub>	13 mJ				
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA				
Circuit configuration	Common cathode				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Center tap configuration
- Very low forward voltage drop
- High frequency operation



HALOGEN

FREE

- · Guard ring for enhanced ruggedness and long term reliability
- · High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### DESCRIPTION

This center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS					
I <sub>F(AV)</sub>	Rectangular waveform	40	А				
V <sub>RRM</sub>		30	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1100	А				
V <sub>F</sub>	20 $A_{pk}$ , $T_J$ = 125 °C (per leg)	0.38	V				
TJ	Range	-55 to +150	°C				

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-42CTQ030S-M3 VS-42CTQ030-1-M3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub> 30		V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	30	V		



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ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST COND	TEST CONDITIONS		UNITS		
Maximum average	per leg		$I_{F(AV)}$ 50 % duty cycle at T <sub>C</sub> = 121 °C, rectangular waveform 40		20			
forward current See fig. 5	per device	I <sub>F(AV)</sub>			40	А		
Maximum peak one cycle non-repetitive			5 µs sine or 3 µs rect. pulse	Following any rated load	1100	A		
surge current per leg See fig. 7		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	360			
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 3 A, L = 2.90 mH		13	mJ		
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3	А		

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
Maximum forward voltage drop per leg See fig. 1		20 A	T. = 25 °C	0.48	V		
	V <sub>FM</sub> <sup>(1)</sup>	40 A	1J=25 C	0.57			
	V FM	20 A	T <sub>1</sub> = 125 °C	0.38			
		40 A	1j = 125 C	0.51			
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	3	mA		
See fig. 2	IRM ("	T <sub>J</sub> = 125 °C	V <sub>R</sub> = naleu V <sub>R</sub>	183			
Threshold Voltage	V <sub>F(TO)</sub>	T T movimum		0.22	V		
Forward slope resistance	r <sub>t</sub>	$-T_J = T_J maximum$		6.76	mΩ		
Maximum junction capacitance per leg	CT	$V_{R}$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		2840	pF		
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 8.0			nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V					

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

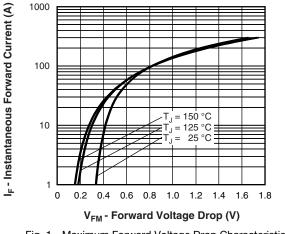
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	OL TEST CONDITIONS		UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C	
Maximum thermal resistance, junction to case per leg		P	DC operation	2.0		
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	De operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50		
Approvimato woight				2	g	
Approximate weight				0.07	oz.	
minimum				6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking daviaa			Case style D <sup>2</sup> PAK (TO-263AB)	42CT0	2030S	
Marking device			Case style TO-262AA	42CTC	2030-1	

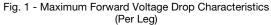
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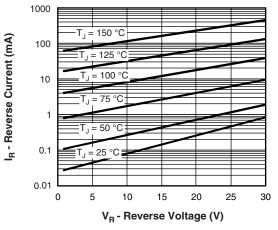
### VS-42CTQ030S-M3, VS-42CTQ030-1-M3

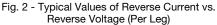


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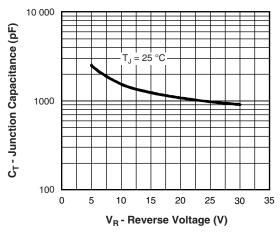


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

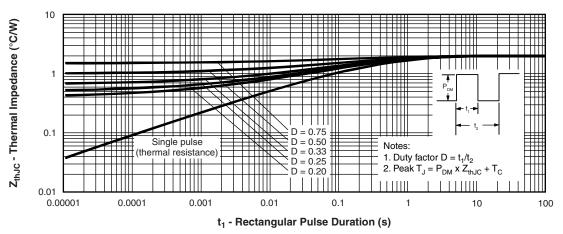
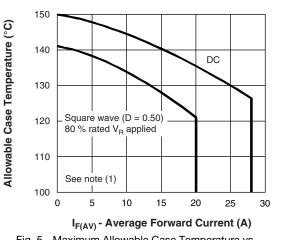


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

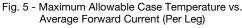
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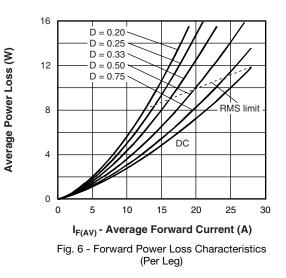
# VS-42CTQ030S-M3, VS-42CTQ030-1-M3

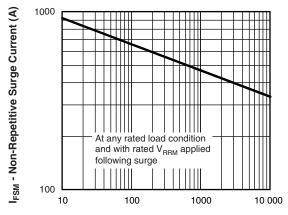




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t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

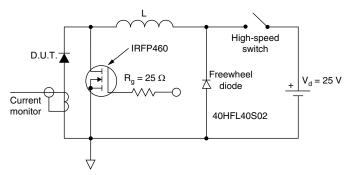


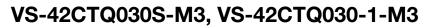
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

Revision: 27-Oct-17

4

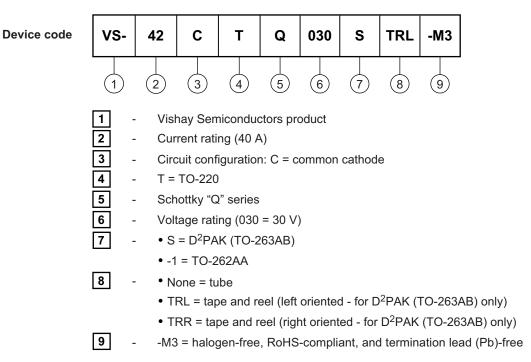
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### **Vishay Semiconductors**

#### **ORDERING INFORMATION TABLE**

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ORDERING INFORMATION						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-42CTQ030S-M3	50	1000	Antistatic plastic tubes			
VS-42CTQ030STRR-M3	800	800	13" diameter reel			
VS-42CTQ030STRL-M3	800	800	13" diameter reel			
VS-42CTQ030-1-M3	50	1000	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS					
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164			
Dimensions	TO-262AA	www.vishay.com/doc?96165			
	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444			
Part marking information	TO-262AA	www.vishay.com/doc?95443			
Packaging information		www.vishay.com/doc?96424			

# **Outline Dimensions**



D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches

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SHA



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

Revision: 08-Jul-15

1

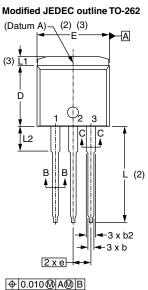


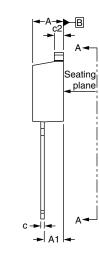
### **Outline Dimensions**

**Vishay Semiconductors** 

**TO-262** 

#### **DIMENSIONS** in millimeters and inches

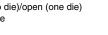


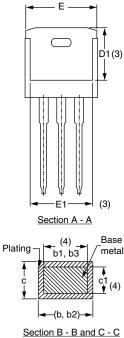


Lead assignments



**Diodes** 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode





Scale: None

CVMDOI	MILLIM	ETERS	INC	NOTES	
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.10	0 BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

#### Notes

Revision: 04-Oct-10

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

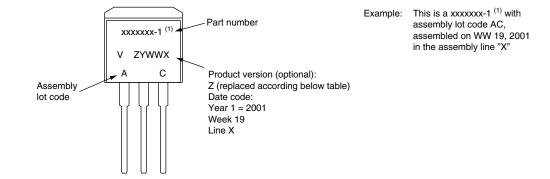
<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

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### **Vishay Semiconductors**

**TO-262** 



#### Note

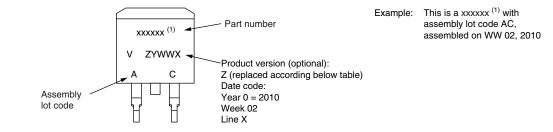
<sup>(1)</sup> If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
A	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS-compliant and termination lead (Pb)-free
F	RoHS-compliant and totally lead (Pb)-free
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free
Ν	Halogen-free, RoHS-compliant and totally lead (Pb)-free
G	Green



### **Vishay Semiconductors**

D<sup>2</sup>PAK



#### Note

<sup>(1)</sup> If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
A	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS-compliant and termination lead (Pb)-free
F	RoHS-compliant and totally lead (Pb)-free
М	Halogen-free, RoHS-compliant, and termination lead (Pb)-free
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free
G	Green



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