Vishay Semiconductors

"Half Bridge" IGBT MTP (Warp Speed IGBT), 114 A



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PRIMARY CHARACTERISTICS						
V _{CES} 600 V						
$V_{CE(on)}$ typical at $V_{GE} = 15 \text{ V}$	2.3 V					
I _C at T _C = 25 °C	114 A					
Speed	30 kHz to 100 kHz					
Package	MTP					
Circuit configuration	Half bridge					

FEATURES

- Gen 4 warp speed IGBT technology
- HEXFRED[®] antiparallel diodes with ultrasoft reverse recovery
- Very low conduction and switching losses
- Optional SMD thermistor (NTC)
- Very low junction to case thermal resistance
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Optimized for welding, UPS and SMPS applications
- Low EMI, requires less snubbing
- Direct mounting to heatsink
- PCB solderable terminals
- Very low stray inductance design for high speed operation

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
		T _C = 25 °C	114		
Continuous collector current	IC	T _C = 109 °C	50]	
Pulsed collector current	I _{CM}		350	A	
Peak switching current	I _{LM}		350	A	
Diode continuous forward current	I _F	T _C = 109 °C	34]	
Peak diode forward current	I _{FM}		200]	
Gate to emitter voltage	V _{GE}		± 20	- V	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500		
.	D	T _C = 25 °C	658	w	
Maximum power dissipation	P _D	T _C = 100 °C	263		

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	$V_{GE} = 0 V, I_{C} = 500 \ \mu A$	600	-	-	V
		V _{GE} = 15 V, I _C = 50 A	-	2.3	3.15	
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 100 A	-	2.5	3.2	v
		V _{GE} = 15 V, I _C = 50 A, T _J = 150 °C	-	1.72	2.17	v
Gate threshold voltage	V _{GE(th)}	I _C = 0.5 mA	3	-	6	
Collector to emitter leaking current	1	$V_{GE} = 0 \text{ V}, \text{ I}_{C} = 600 \text{ A}$	-	-	0.4	mA
Collector to emitter leaking current I _{CES}		V_{GE} = 0 V, I _C = 600 A, T _J = 150 °C	-	-	10	ШA
Diode forward voltage drop	V _{FM}	$I_F = 50 \text{ A}, V_{GE} = 0 \text{ V}$	-	1.58	1.80	
		$I_F = 50 \text{ A}, V_{GE} = 0 \text{ V}, T_J = 150 ^\circ\text{C}$	-	1.49	1.68	V
		I_F = 100 A, V_{GE} = 0 V, T_J = 25 °C	-	1.9	2.17	
Gate to emitter leakage current	I _{GES}	$V_{GE} = \pm 20 \text{ V}$	-	-	± 250	nA



ROHS COMPLIANT

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SWITCHING CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I _C = 52 A	-	331	385	
Gate to emitter charge (turn-on)	Q _{ge}	$V_{CC} = 400 \text{ V}$	-	44	52	nC
Gate to collector charge (turn-on)	Q _{gc}	V _{GE} = 15 V	-	133	176	
Turn-on switching loss	Eon	Internal gate resistors (see electrical diagram)	-	0.26	-	
Turn-off switching loss	E _{off}	$I_{C} = 50 \text{ A}$, $V_{CC} = 480 \text{ V}$, $V_{GE} = 15 \text{ V}$, $L = 200 \mu\text{H}$ energy losses include tail and diode reverse	-	1.2	-	mJ
Total switching loss	E _{ts}	recovery, $T_J = 25 \text{ °C}$	-	1.46	-	
Turn-on switching loss	E _{on}	Internal gate resistors (see electrical diagram)	-	0.73	-	
Turn-off switching loss	E _{off}	$I_{C} = 50 \text{ A}, V_{CC} = 480 \text{ V}, V_{GE} = 15 \text{ V}, L = 200 \mu\text{H}$ energy losses include tail and diode reverse	-	1.66	-	mJ
Total switching loss	E _{ts}	recovery, $T_J = 150 \text{ °C}$	-	2.39	-	
Input capacitance	Cies	V _{GE} = 0 V	-	7100	-	
Output capacitance	Coes	$V_{CC} = 30 V$	-	510	-	pF
Reverse transfer capacitance	C _{res}	f = 1.0 MHz	-	140	-	
Diode reverse recovery time	t _{rr}	V _{CC} = 200 V, I _C = 50 A dl/dt = 200 A/µs	-	82	97	ns
Diode peak reverse current	Irr		-	8.3	10.6	А
Diode recovery charge	Q _{rr}	avat – 2007 vµ3	-	340	514	nC
Diode reverse recovery time	t _{rr}	$V_{CC} = 200 \text{ V}, I_{C} = 50 \text{ A}$	-	137	153	ns
Diode peak reverse current	Irr	dl/dt = 200 A/µs	-	12.7	14.8	А
Diode recovery charge	Q _{rr}	T _J = 125 °C	-	870	1132	nC

THERMISTOR SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Resistance	R ₀ ⁽¹⁾	T ₀ = 25 °C	-	30	-	kΩ
Sensitivity index of the thermistor material	β (1)(2)	T ₀ = 25 °C T ₁ = 85 °C	-	4000	-	к

Notes

⁽¹⁾ T_0 , T_1 are thermistor's temperatures

⁽²⁾
$$\frac{R_0}{R_1} = \exp\left[\beta\left(\frac{1}{T_0} - \frac{1}{T_1}\right)\right]$$
, temperature in Kelvin

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction IGB	BT, diode	TJ		-40	-	150	
temperature range Th	nermistor	IJ		-40	-	125	°C
Storage temperature range		T _{Stg}		-40	-	125	
Junction to case	IGBT	Р		-	-	0.38	
Junction to case	Diode	R _{thJC}		-	-	0.8	°C/W
Case to sink per module		R _{thCS}	Heatsink compound thermal conductivity = 1 W/mK	-	0.06	-	
Clearance ⁽¹⁾			External shortest distance in air between 2 terminals	5.5	-	-	
Creepage ⁽¹⁾			Shortest distance along the external surface of the	8 -	_	_	mm
			insulating material between 2 terminals	0			
			A mounting compound is recommended and the				
Mounting torque to heatsink			torque should be checked after 3 hours to allow for	3 ± 10 %		Nm	
			the spread of the compound. Lubricated threads.				
Weight					66		g

Note

⁽¹⁾ Standard version only i.e. without optional thermistor

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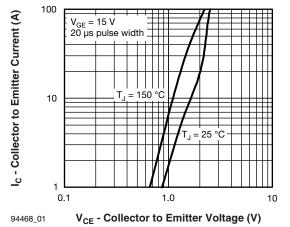


Fig. 1 - Typical Output Characteristics

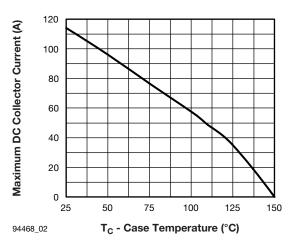


Fig. 2 - Maximum Collector Current vs. Case Temperature

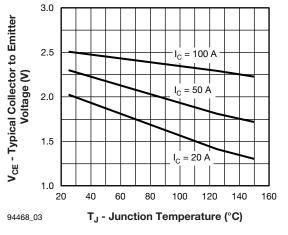


Fig. 3 - Typical Collector to Emitter Voltage vs. Junction Temperature

20 $V_{\rm CC} = 400$ V V_{GE} - Gate to Emitter Voltage (V) $I_{\rm C} = 52 \, {\rm A}$ 16 12 8 4 0 100 200 300 0 400 94468 04 O_G - Typical Gate Charge (nC) Fig. 4 - Typical Gate Charge vs. Gate to Emitter Votlage 100 I_F - Instantaneous Forward Current (A) 10 T_J = 150 °C T₁ = 125 °C T₁ = 25 °C 0.4 2.4 0.8 1.2 1.6 2.0 V_{FM} - Forward Voltage Drop (V) 94468_05 Fig. 5 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current 160 $V_{R} = 200 V$

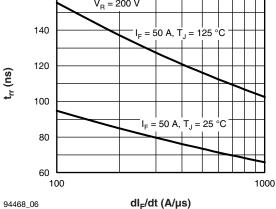


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

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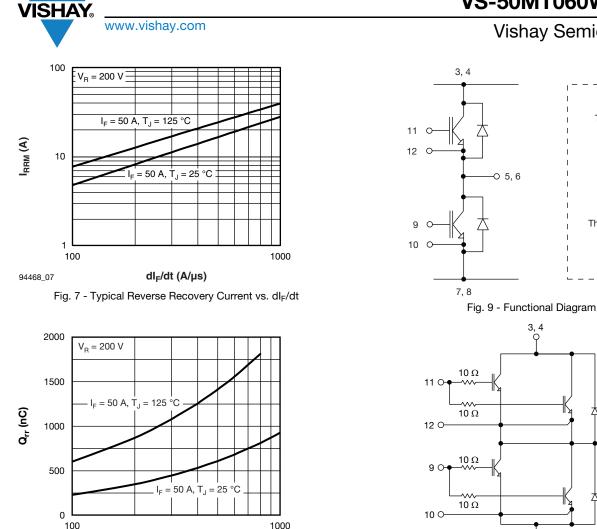
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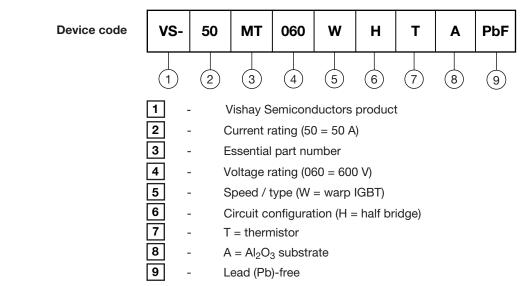
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dl_r/dt (A/µs) Fig. 8 - Typical Stored Charge vs. dl_F/dt

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Thermistor

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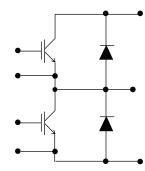
Fig. 10 - Electrical Diagram



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CIRCUIT CONFIGURATION



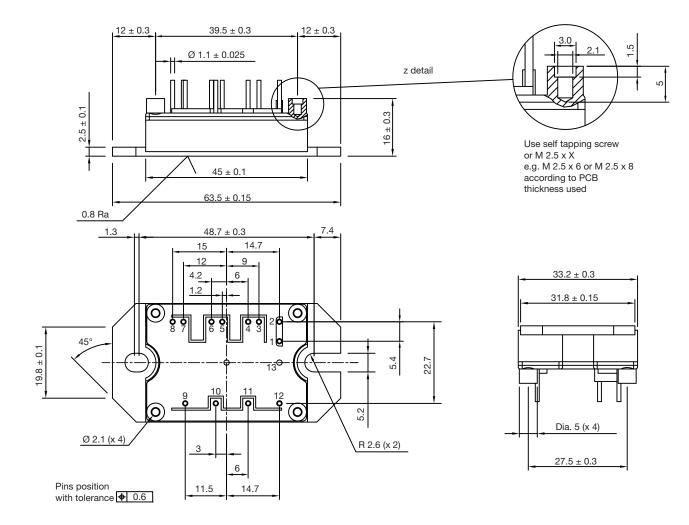
Dimensions www.vishay.com/doc?95175	LINKS TO RELATED DOCUMENTS					
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MTP

DIMENSIONS in millimeters



Note

· Unused terminals are not assembled in the package



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