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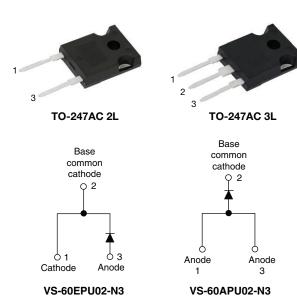
Vishay Semiconductors

RoHS

COMPLIANT

HALOGEN FREE

Ultrafast Soft Recovery Diode, 60 A FRED Pt®



PRIMARY CHARACTERISTICS					
I _{F(AV)}	60 A				
V_{R}	200 V				
V _F at I _F	0.81 V				
t _{rr} typ.	See Recovery table				
T _J max.	175 °C				
Package	TO-247AC 2L, TO-247AC 3L				
Circuit configuration	Single				

FEATURES

- · Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Output rectification
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

DESCRIPTION / APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V_{R}		200	V	
Continuous forward current	I _{F(AV)}	T _C = 127 °C	60		
Single pulse forward current	I _{FSM}	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	800	Α	
Maximum repetitive forward current	I _{FRM}	Square wave, 20 kHz	120		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	TEST CONDITIONS MIN. TYP. M		MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	200	-	-	
Forward voltage V _F	I _F = 60 A	-	0.98	1.08	V	
1 of ward voltage		I _F = 60 A, T _J = 175 °C	-	0.81	0.88	
Reverse leakage current	ı	$V_R = V_R$ rated	-	-	50	μΑ
neverse leakage current	I _R	$T_J = 150 ^{\circ}\text{C}$, $V_R = V_R$ rated	-	-	2	mA
Junction capacitance	C _T	V _R = 200 V - 87		-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		nΗ		



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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 60 A dI _F /dt = 200 A/μs V _R = 160 V	-	28	-	ns - A
		T _J = 125 °C		-	50	-	
Peak recovery current I _{RRN}		T _J = 25 °C		-	4	-	
	IRRM	T _J = 125 °C		-	8	-	
Reverse recovery charge	0	T _J = 25 °C		=	59	-	nC
	Q _{rr}	T _J = 125 °C		-	220	-] 110

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R _{thJC}		-	-	0.70	K/W
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	40	°C/W
Thermal resistance, case to heatsink	R _{thCS}	R _{thCS} Mounting surface, flat, smooth, and greased		0.2	-	K/W
Weight			-	5.5	-	g
weignt			-	0.2	-	oz.
Mounting torque			-	-	1.2	N · m
Marking device		Case style TO-247AC 2L	60EPU02			
iviarking device		Case style TO-247AC 3L		60APU02		

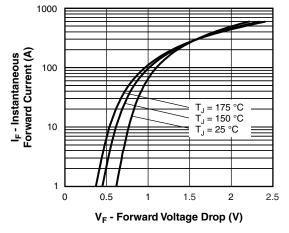


Fig. 1 - Typical Forward Voltage Drop Characteristics

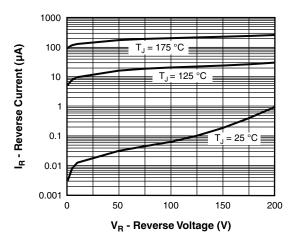


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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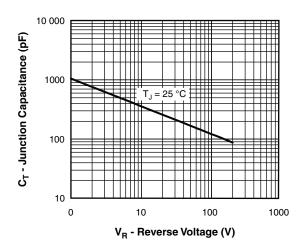


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

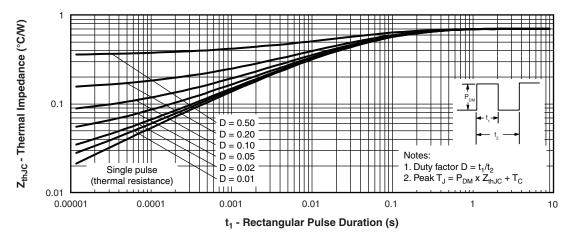


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

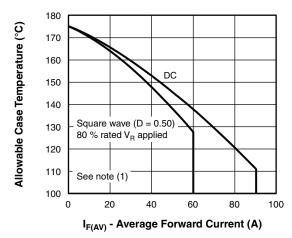


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

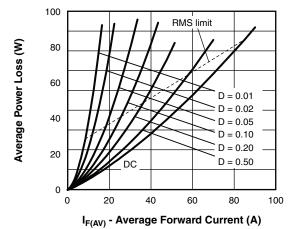


Fig. 6 - Forward Power Loss Characteristics

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$



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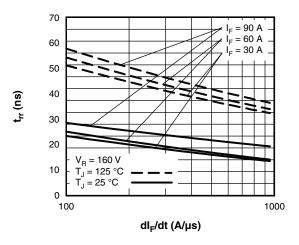


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

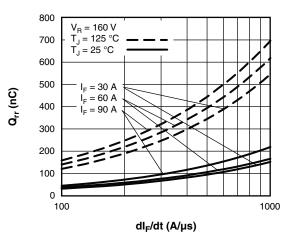
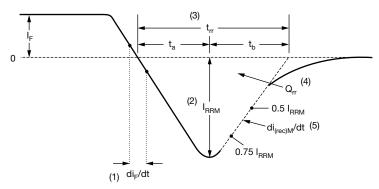


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_{rr}$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during $t_{\rm b}$ portion of $t_{\rm rr}$

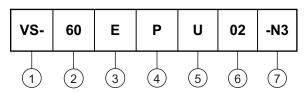
Fig. 9 - Reverse Recovery Waveform and Definitions

VS-60EPU02-N3, VS-60APU02-N3

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Current rating (60 = 60 A)

3 - Circuit configuration:

• E = single diode, 2 pins

• A = single diode, 3 pins

4 - Package:

P = TO-247AC

5 - Type of silicon:

U = ultrafast recovery

6 - Voltage rating (02 = 200 V)

7 - Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-60EPU02-N3	25	500	Antistatic plastic tube	
VS-60APU02-N3	25	500	Antistatic plastic tube	

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AC 2L	www.vishay.com/doc?96144			
Dimensions	TO-247AC 3L	www.vishay.com/doc?96138			
Dort marking information	TO-247AC 2L -N3	www.vishay.com/doc?95648			
Part marking information	TO-247AC 3L-N3	www.vishay.com/doc?95007			
SPICE model		www.vishay.com/doc?96049			



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