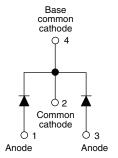


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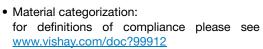
Ultrafast Rectifier, 2 x 3 A FRED Pt®





FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C







ROHS COMPLIANT HALOGEN FREE

PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 3 A							
V_{R}	200 V							
V _F at I _F	0.9 V							
t _{rr} typ.	See Recovery table							
T _J max.	175 °C							
Package	DPAK (TO-252AA)							
Circuit configuration	Common cathode							

DESCRIPTION / APPLICATIONS

Vishay Semiconductors' 200 V series are the state of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS						
Peak repetitive reverse voltage	V_{RRM}		200	V						
Average rectified forward current per device	I _{F(AV)}	Total device, rated V _R , T _C = 159 °C	6							
Non-repetitive peak surge current	I _{FSM}		50	Α						
Peak repetitive forward current per diode	I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 159 °C	6							
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C						

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	ı					
Forward voltage		I _F = 3 A	-	0.9	1	V				
	V _F	I _F = 3 A, T _J = 125 °C	-	0.78	0.9					
		I _F = 6 A	-	1	1.2					
		I _F = 6 A, T _J = 125 °C	-	0.89	1.08	1				
Deverse legisers accoment	I _R	V _R = V _R rated	-	-	5					
Reverse leakage current		T _J = 125 °C, V _R = V _R rated	-	-	100	μA				
Junction capacitance	C _T	V _R = 200 V	-	12	-	pF				
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH				



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		I _F = 1.0 A, dI _F /dt =	-	-	35					
	t _{rr}	T _J = 25 °C		-	19	-	ns A			
		T _J = 125 °C	I _F = 3 A V _R = 160 V dI _F /dt = 200 A/μs	-	26	-				
Peak recovery current		T _J = 25 °C		-	3.1	-				
	I _{RRM}	T _J = 125 °C		-	4.6	-				
Reverse recovery charge		T _J = 25 °C		-	30	-	nC			
	Q _{rr}	T _J = 125 °C]	-	60	-				

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS					
Maximum junction and storage temperature range	T _J , T _{Stg}	-65	-	+175	°C					
Thermal resistance, junction to case per leg	R _{thJC}	-	-	5	°C/W					
Weight		-	0.3	-	g					
Weight		-	0.01	-	oz.					
Mounting torque		6.0	_	12	kgf · cm					
Woulding torque		(5.0)	_	(10)	(lbf·in)					
Marking device		Case style DPAK (TO-252AA) 6CW		H02FN						

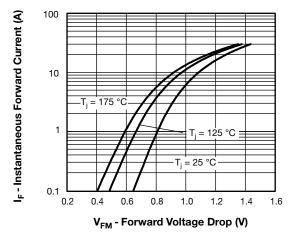


Fig. 1 - Maximum Forward Voltage Drop Characteristics

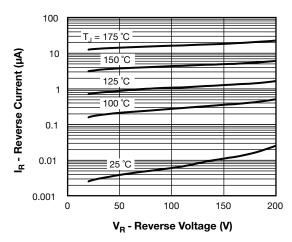


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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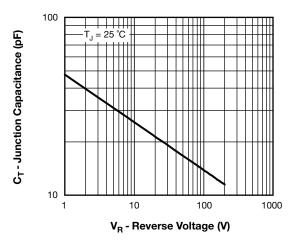


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

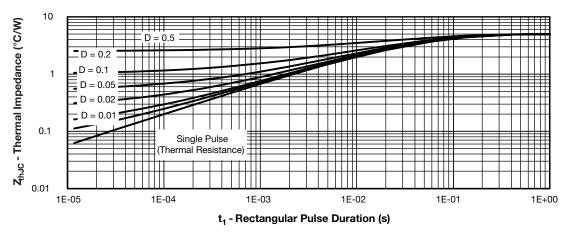


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

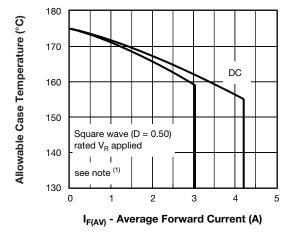


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

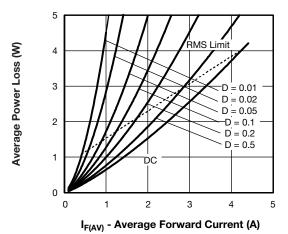


Fig. 6 - Forward Power Loss Characteristics

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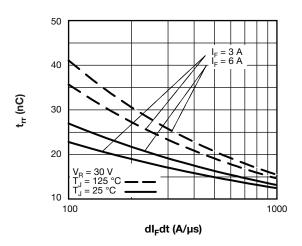


Fig. 7 - Typical Reverse Recovery vs. dl_F/dt

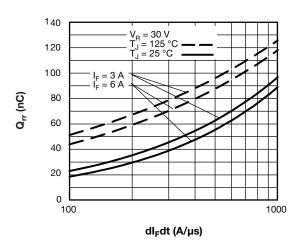


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R

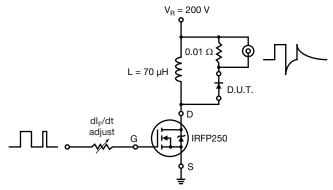
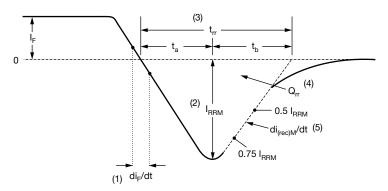


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_F$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

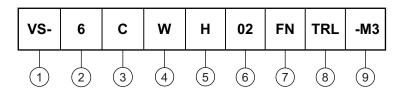
Fig. 10 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (6 = 6 A)

Center tap configuration

Package identifier:

W = DPAK

5 - H = hyperfast recovery

6 - Voltage rating (02 = 200 V)

7 - FN = TO-252AA

- • None = tube (50 pieces)

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

9 - Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-6CWH02FN-M3	75	3000	Antistatic plastic tube						
VS-6CWH02FNTR-M3	2000	2000	13" diameter reel						
VS-6CWH02FNTRL-M3	3000	3000	13" diameter reel						
VS-6CWH02FNTRR-M3	3000	3000	13" diameter reel						

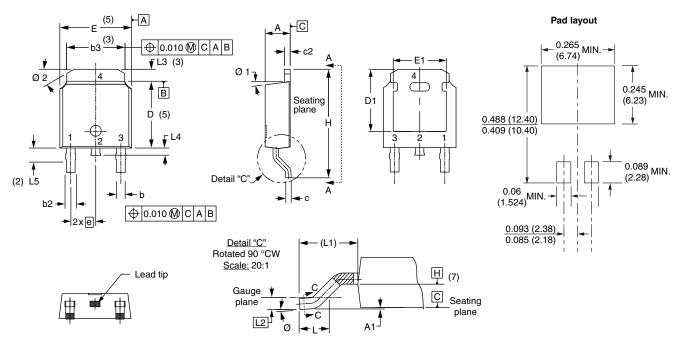
LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95627						
Part marking information	www.vishay.com/doc?95176						
Packaging information	www.vishay.com/doc?95033						



Vishay Semiconductors

D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS INCHES		INCHES		NOTES	TEC	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC		
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410		
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070		
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.		
b3	4.95	5.46	0.195	0.215	3		L2	0.51	1 BSC 0.020 BSC		BSC		
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3	
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040		
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2	
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°		
Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°		
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC® outline TO-252AA



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