Vishay Semiconductors



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# Hyperfast Rectifier, 2 x 3 A FRED $Pt^{(R)}$



#### 1, 2 • 7, 8 3, 4 • 5, 6

#### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 3 A					
V <sub>R</sub>	200 V					
V <sub>F</sub> at I <sub>F</sub>	0.71 V					
t <sub>rr</sub>	25 ns					
T <sub>J</sub> max.	175 °C					
Package	FlatPAK 5 x 6					
Circuit configuration	Separated cathode					

#### FEATURES

- Hyper fast recovery time, reduced Q<sub>rr</sub>, and soft recovery
- 175 °C maximum operating junction temperature
- Low forward voltage drop
- Low leakage current
- Specific for output and snubber operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260  $^{\circ}\mathrm{C}$
- Material categorization: for definitions of compliance please see <a href="http://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION / APPLICATIONS**

State of the art hyper fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyper fast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

#### **MECHANICAL DATA**

#### Case: FlatPAK 5 x 6

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

**Terminals:** matte tin plated leads, solderable per J-STD-002, meets JESD 201 class 2 whisker test

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage		V <sub>RRM</sub>		200				
Average rectified forward current			T <sub>Solderpad</sub> = 170 °C, DC	3	V			
Average rectilied forward current	per device	IF(AV)	$T_{Solderpad} = 169 \ ^{\circ}C, D = 0.5$	3				
Non-repetitive peak surge current per de		l	$T_J$ = 25 °C, 10 ms sinusoidal pulse	147	А			
Non-repetitive peak surge current	per diode	IFSM		70	A			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 $^{\circ}$ C unless otherwise specified)								
PARAMETER         SYMBOL         TEST CONDITIONS         MIN.         TYP.         MAX.         U								
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	200	-	-			
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 3 A	-	0.88	0.94	V		
Forward voltage		I <sub>F</sub> = 3 A, T <sub>J</sub> = 150 °C	-	0.71	0.74	1		
Reverse leakage current	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	2	μA		
neverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	6	40			
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	14	-	pF		

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COMPLIANT HALOGEN

FREE





### **Vishay Semiconductors**

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50$	0 A/µs, V <sub>R</sub> = 30 V	-	26	-			
Reverse recovery time	+	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		-	-	25			
	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	15	-	ns		
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 3 A dI <sub>F</sub> /dt = 200 A/μs V <sub>B</sub> = 160 V	-	25	-			
Deals receivers aurrent		T <sub>J</sub> = 25 °C		-	2	-	А		
Peak recovery current	IRRM	T <sub>J</sub> = 125 °C		-	3	-	A		
Davida and a second second	0	T <sub>J</sub> = 25 °C		-	12	-			
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	40	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C			
Thermal resistance, junction to ambient	R <sub>thJA</sub> <sup>(1)(2)</sup>		-	90	103				
Thermal resistance, junction to mount	R <sub>thJM</sub> <sup>(3)</sup>		-	2.3	2.6	°C/W			

#### Notes

 $^{(1)}$  The heat generated must be less than thermal conductivity from junction to ambient;  $dP_D/dT_J < 1 \times R_{thJA}$ 

 $^{(2)}$  Free air, mounted or recommended copper pad area; thermal resistance R<sub>thJA</sub> - junction to ambient

<sup>(3)</sup> Mounted on infinite heatsink

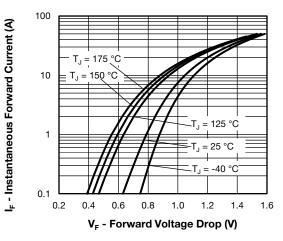


Fig. 1 - Typical Forward Voltage Drop Characteristics

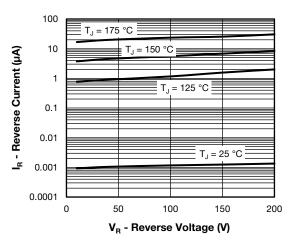


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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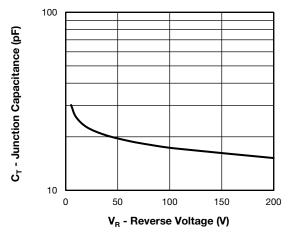


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

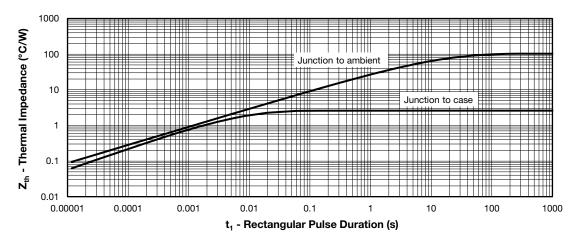


Fig. 4 - Maximum Thermal Impedance Zth Characteristics

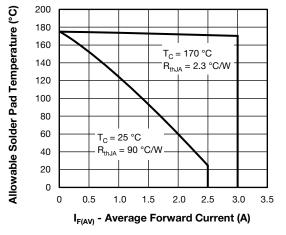
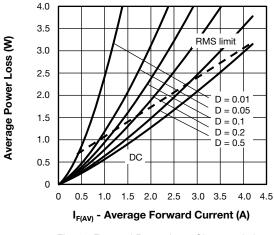


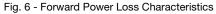
Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$  $Pd = forward power loss = I_{C(M)} \times V_{TM}$  at  $(I_{C(M)}/D)$  (see I

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{Fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 





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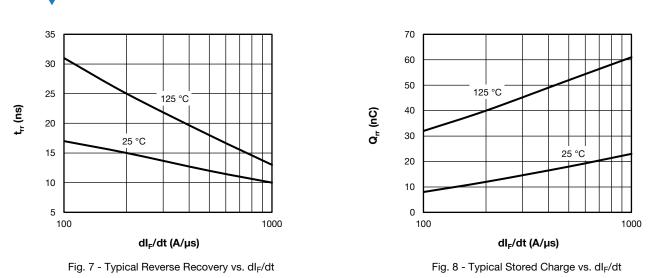
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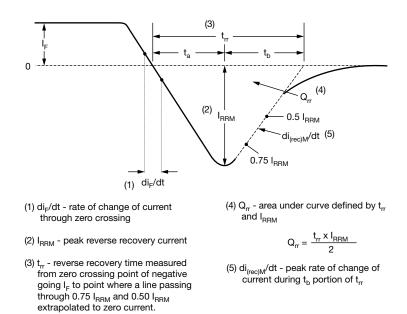


Fig. 9 - Reverse Recovery Waveform and Definitions

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### **ORDERING INFORMATION TABLE**

Device

**VISHAY** 

code	vs-	6	D	к	н	02	-M3
			(3)	(4)	(5)	6	
	1 2 3	- Cui	hay Sen rent rati cuit conf	niconduo ng (6 = 0	ctors pro 6 A)	$\bigcirc$	
	4	D = - K =	separat FlatPAł cess typ	ed cath K packa	ode		
	6 7	- Vol	hyperfa tage coc 3 = halog	le (02 =	200 V)	-complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	PACKAGING DESCRIPTION			
VS-6DKH02-M3/H	0.10	н	1500	7"diameter plastic tape and reel			
VS-6DKH02-M3/I	0.10	Ι	6000	13"diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?96056						
Part marking information	www.vishay.com/doc?96059						
Packaging information	www.vishay.com/doc?88869						
SPICE model	www.vishay.com/doc?96882						

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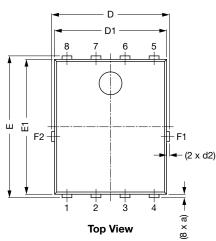


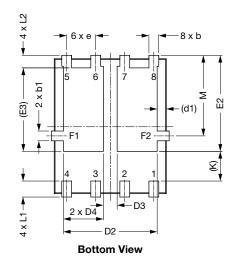
# **Outline Dimensions**

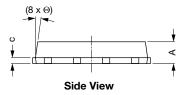
**Vishay Semiconductors** 

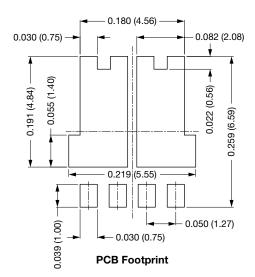
# FlatPAK 5 x 6 (Dual)

#### **DIMENSIONS** in inches (millimeters)









DIM		INCHES		MILLIMETERS				
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.		
А	0.035	0.039	0.043	0.89	0.99	1.09		
(a)	-	0.006	-	-	0.15	-		
b	0.013	0.017	0.020	0.32	0.43	0.52		
b1	0.013	0.017	0.020	0.32	0.43	0.52		
С	0.008	-	0.014	0.20	-	0.35		
D	0.197	0.203	0.209	5.00	5.15	5.30		
D1	0.189	0.193	0.197	4.80	4.90	5.00		
D2	0.154	0.161	0.169	3.90	4.10	4.30		
D3	0.020	0.024	0.031	0.50	0.60	0.80		
D4	0.063	0.069	0.075	1.60	1.75	1.90		
(d1)	-	0.016	-	-	0.40	-		
(d2)	-	0.005	-	-	0.125	-		
E	0.238	0.244	0.250	6.05	6.20	6.35		

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# **Outline Dimensions**

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DIM.		INCHES		MILLIMETERS				
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.		
E1	0.228	0.232	0.236	5.80	5.90	6.00		
E2	0.157	0.165	0.173	4.00	4.20	4.40		
(E3)	-	0.144	-	-	3.65	-		
е		0.050 BSC			1.27 BSC			
(K)	0.039	-	-	1.00	-	-		
L1	0.019	-	0.043	0.48	-	1.10		
L2	0.012	-	0.031	0.30	-	0.80		
М	0.128	0.138	0.148	3.25	3.50	3.75		
Θ	0°	-	10°	0°	-	10°		

Notes

• Dimensioning and tolerancing per ASME Y14.5-2009

• Dimensions D1 and E1 do not include mold flash or gate burrs

• Dimension (XX) means reference only



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 SM100
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 SCH10000
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 ACGRA4001-HF
 D1821SH45T PR
 D1251S45T
 NTE5990
 NTE6358
 NTE6162