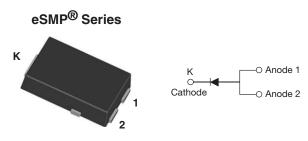
Vishay Semiconductors

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Hyperfast Rectifier, 6 A FRED Pt[®]



SMPC (TO-277A)

LINKS TO ADDITIONAL RESOURCES



SHA)

PRIMARY CHARACTERISTICS					
I _{F(AV)}	6 A				
V _R	200 V				
V _F at I _F	0.74 V				
t _{rr (typ.)}	28 ns				
T _J max.	175 °C				
Package	SMPC (TO-277A)				
Circuit configuration	Single				

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		200	V		
Average rectified forward current	I _{F(AV)}	T _{Sp} = 160 °C	6	٨		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	150	~		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	200	-	-		
	V	I _F = 6 A	-	0.87	0.94	V	
Forward voltage	V _F	I _F = 6 A, T _J = 125 °C	-	0.74	0.8		
Deverse leekees eurrent		$V_{R} = V_{R}$ rated	-	-	2		
Reverse leakage current	IR	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	3	15	μA	
Junction capacitance	CT	V _R = 200 V	-	33	-	pF	

Revision: 21-Jan-2021

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Document Number: 94982

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COMPLIANT

HALOGEN

FREE



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t =$	= 50 A/µs, V _R = 30 V	-	28	-		
Boyeroo receivor timo	+	I _F = 0.5 A, I _R = 1	-	-	25			
Reverse recovery time	t _{rr}	T _J = 25 °C		-	22	-	ns	
		T _J = 125 °C	I _F = 6 A dI _F /dt = 200 A/µs V _B = 160 V	-	33	-		
Deals receivers aurrent		T _J = 25 °C		-	2.4	-	А	
Peak recovery current	I _{RRM}	T _J = 125 °C		-	5.0	-	A	
	0	T _J = 25 °C		-	27	-	nC	
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	80	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C	
Thermal resistance, junction to mount	R _{thJM}		-	2.2	3	°C/W	
Thermal resistance, junction to ambient	R _{thJA}		-	85	-	C/W	
Approvimeto weight				0.1		g	
Approximate weight				0.0035		oz.	
Marking device		Case style SMPC (TO-277A)		NE	H2		



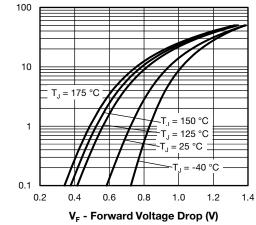


Fig. 1 - Typical Forward Voltage Drop Characteristics

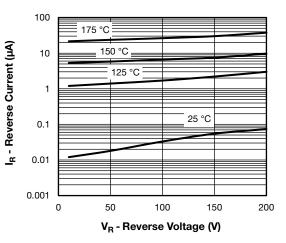
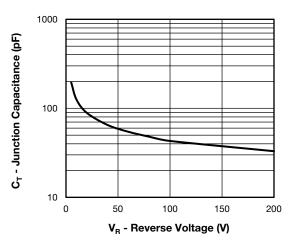


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

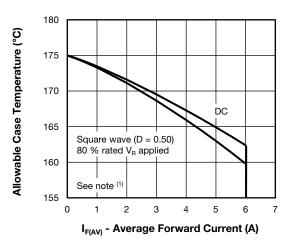


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

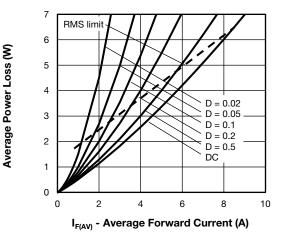


Fig. 5 - Forward Power Loss Characteristics

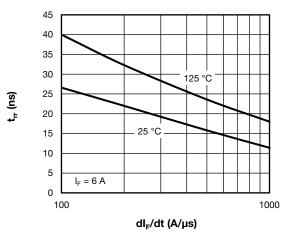


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

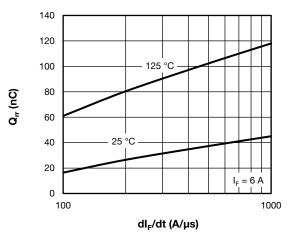


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

(1)

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 5); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

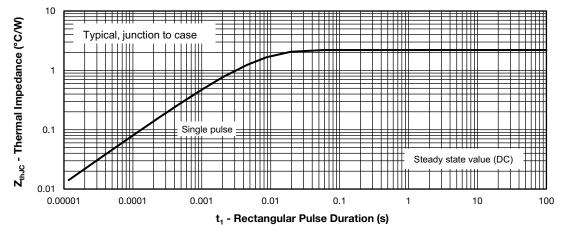
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Fig. 8 - Transient Thermal Impedance, Junction to Case

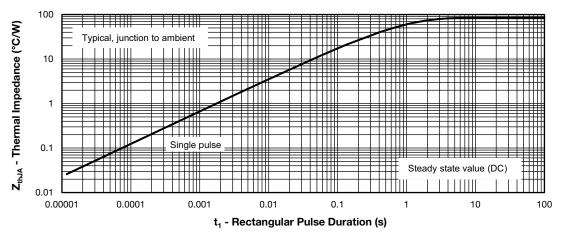


Fig. 9 - Transient Thermal Impedance, Junction to Ambient

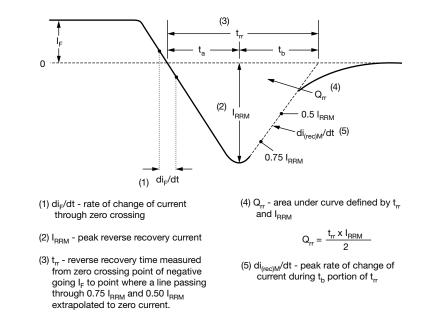


Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

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Device code	VS-	6	Е	S	Н	02	-M3
	1	2	3	4	5	6	7
	1	- Visl	nay Sen	niconduo	ctors pro	oduct	
	2	- Cur	rent rati	ng (6 =	6 A)		
	3	- Circ	uit conf	iguratior	ו:		
		E =	single c	liode			
	4	- S =	SMPC	package	;		
	5	- Pro	cess typ	e,			
	_	H =	hyperfa	ist recov	very		
	6	- Voli	age coo	le (02 =	200 V)		
	7	M3	s = halog	gen-free	, RoHS-	complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-6ESH02-M3/86A	1500	1500	7" diameter plastic tape and reel				
VS-6ESH02-M3/87A	6500	6500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95570				
Part marking information	www.vishay.com/doc?95565				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96111				

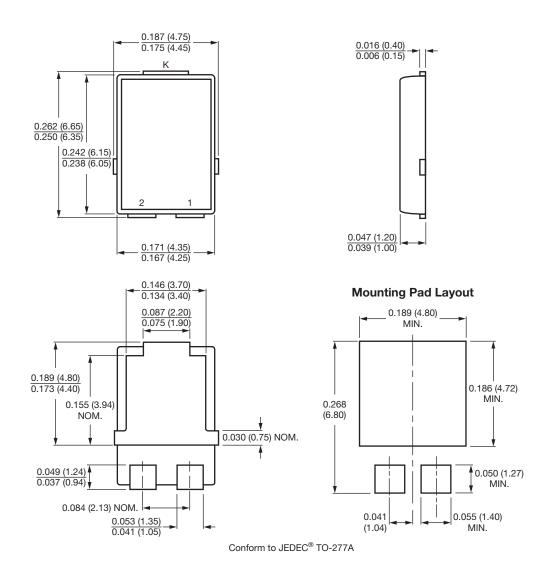
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TO-277A (SMPC)

DIMENSIONS in inches (millimeters)





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