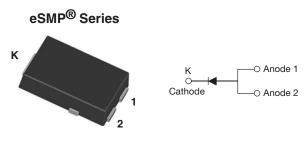
Vishay Semiconductors

Ultrafast Rectifier, 6 A FRED Pt[®]



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SMPC (TO-277A)

LINKS TO ADDITIONAL RESOURCES



SHA

PRIMARY CHARACTERISTICS				
I _{F(AV)}	6 A			
V _R	600 V			
V _F at I _F	0.95 V			
t _{rr (typ.)}	42 ns			
T _J max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

FEATURES

- Ultra fast recovery time, reduced Qrr, and soft recovery
- 175 °C maximum operating junction temperature
- For PFC, CRM snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 gualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art ultra fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultra fast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		600	V		
Average rectified forward current	I _{F(AV)}	T _{Sp} = 150 °C	6	٨		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	120	A		
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-		
Forward voltage	V _F	I _F = 6 A	-	1.10	1.30	V	
		I _F = 6 A, T _J = 150 °C	-	0.95	1.15		
Reverse leakage current	I _R	$V_R = V_R$ rated	-	-	5		
neverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	25	150	μΑ	
Junction capacitance	CT	V _R = 600 V	-	8	-	pF	

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COMPLIANT

HALOGEN FREE



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DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
			50 A/µs, V _R = 30 V	-	42	-	
Reverse recovery time	t _{rr}	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	60	
Reverse recovery time		T _J = 25 °C		-	58	-	ns
		T _J = 125 °C	I _F = 6 A dI _F /dt = 500 A/μs V _R = 400 V	-	85	-	
Deals reactions automat		T _J = 25 °C		-	10	-	А
Peak recovery current	IRRM	T _J = 125 °C		-	15	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	290	-	nC
		T _J = 125 °C		-	620	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to mount	R _{thJM}		-	2.4	3.5	°C/W
Approximate weight				0.1		g
Approximate weight				0.0035		oz.
Marking device		Case style SMPC (TO-277A)		NE	EU6	

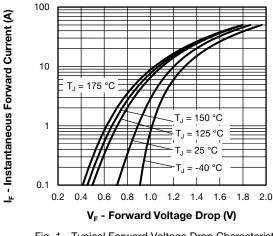


Fig. 1 - Typical Forward Voltage Drop Characteristics

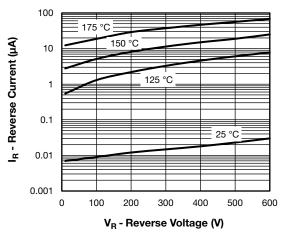
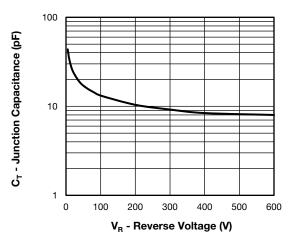


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

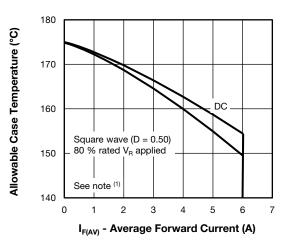


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

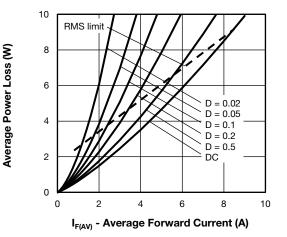


Fig. 5 - Forward Power Loss Characteristics

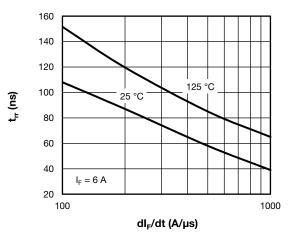


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

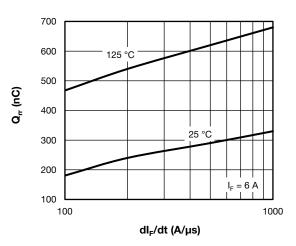


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

(1)

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 5); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

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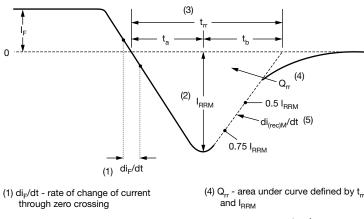


Fig. 8 - Reverse Recovery Waveform and Definitions

 $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(2) ${\rm I}_{\rm RRM}$ - peak reverse recovery current

(5) di_{(rec)M}/dt - peak rate of change of current during $t_{\rm b}$ portion of $t_{\rm rr}$

ORDERING INFORMATION TABLE

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Device code	VS-	6	Е	s	U	06	н	М3	
	1	2	3	4	5	6	7	8	
	2	- Cur - Circ	rent rati cuit conf	niconduo ng (6 = iguration	6 A)	oduct			
	H	- S = - Pro	E = single diode S = SMPC package Process type, U = ultra fast recovery						
	7	- Voli - H =	age coo AEC-Q	de (06 = 101 qua	600 V) alified	complia	nt, and	terminat	tions lead (Pb

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-6ESU06HM3/86A	1500	1500	7" diameter plastic tape and reel				
VS-6ESU06HM3/87A	6500	6500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95570				
Part marking information	www.vishay.com/doc?95565				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96703				

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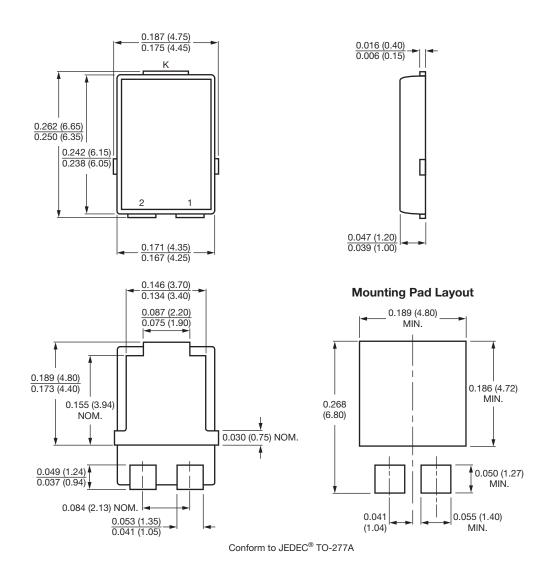
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TO-277A (SMPC)

DIMENSIONS in inches (millimeters)





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