RoHS

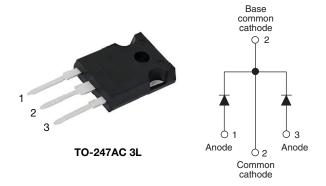
COMPLIANT

HALOGEN FREE



Vishay Semiconductors

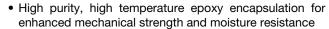
High Performance Schottky Rectifier, 2 x 40 A



PRIMARY CHARACTERISTICS					
I _{F(AV)} 2 x 40 A					
V _R	20 V				
V _F at I _F	0.36 V				
I _{RM} max.	1100 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	27 mJ				
Package	TO-247AC 3L				
Circuit configuration	Common cathode				

FEATURES

- 150 °C T_J operation
- Optimized for 3.3 V application
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability



- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for ultralow forward voltage drop specifically for 3.3 V output power supplies. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	80	Α			
V _{RRM}		20	V			
I _{FSM}	t _p = 5 μs sine	2200	Α			
V _F	40 A _{pk} , T _J = 150 °C (per leg)	0.32	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-80CPQ020-N3	UNITS
Maximum DC reverse voltage	V_{R}	20	V
Maximum working peak reverse voltage	V_{RWM}	20	ľ

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average	per leg		50.0/ duty avalant T 120.00 vactor gular vacyafarra		FO 0/ duty and at T 100 00 materials and annual forms		40	
forward current per device		I _{F(AV)}	50 % duty cycle at T_C = 138 °C, rectangular waveform		80			
Maximum peak one cycle non-repetitive surge current per leg		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	2200	A		
			10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	500			
Non-repetitive avalanche energy per leg E_{AS} $T_{J} = 25$ °C, $I_{AS} = 6$ A, L = 1.5 mH		27	mJ					
Repetitive avalanche current per leg I _{AR}		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		6	Α		



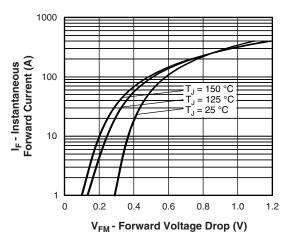
ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST C	TEST CONDITIONS		
		40 A	T _{.1} = 25 °C	0.46	
		80 A	1j=25 0	0.55	
Maximum forward	V _{FM} ⁽¹⁾	40 A	T _{.1} = 125 °C	0.36	V
voltage drop per leg	V _{FM} (·)	80 A	IJ = 125 C	0.46	V
		40 A	T _{.1} = 150 °C	0.32	
		80 A	- IJ = 150 C	0.43	
	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = 5 V	110	
Maximum reverse		T _J = 150 °C	V _R = 10 V	600	mA
leakage current per leg		T _J = 25 °C	V - Poted V	5.5	IIIA
		T _J = 125 °C	V_R = Rated V_R	1100	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.185	V
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		6500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

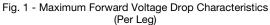
Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL	SPECIFIC	ATIONS		
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to 150	°C
Maximum thermal resistance, junction to case per leg		0.6		
Maximum thermal resistance, junction to case per package	R _{thJC}	DC operation	0.3	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.25	
Approximate weight			6	g
Approximate weight			0.21	oz.
Mounting torque minimum			6 (5)	kgf · cm
maximum			12 (10)	(lbf · in)
Marking device		Case style TO-247AC 3L	80CP	Q020







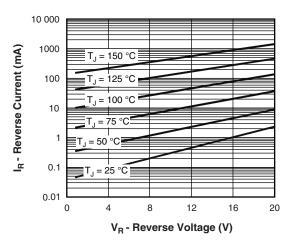


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

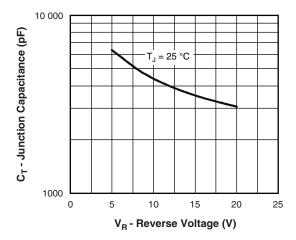


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

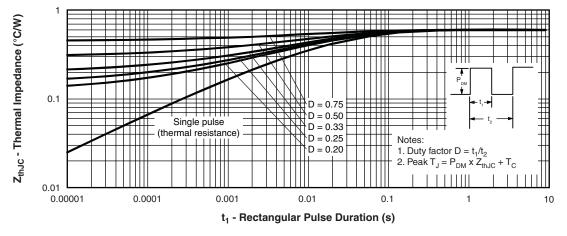


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



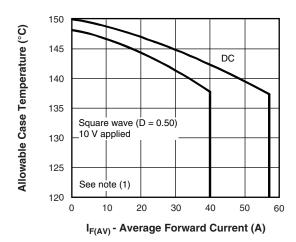


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

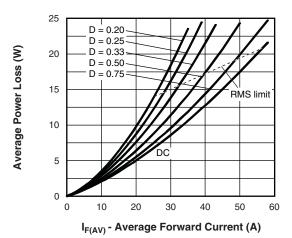


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

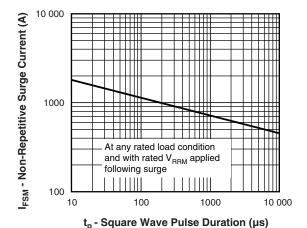


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

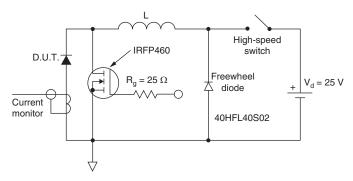


Fig. 8 - Unclamped Inductive Test Circuit

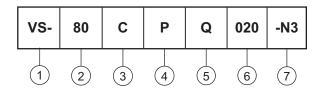
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; I_R \text{ at } V_{R1} = 10 \text{ V}. \end{array}$



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (80 = 80 A)
- Circuit configuration:

C = common cathode

4 - Package:

P = TO-247

- 5 Schottky "Q" series
- 6 Voltage code (020 = 20 V)
- 7 Environmental digit

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

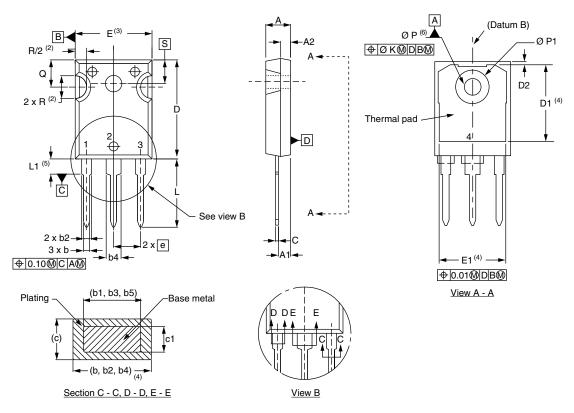
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-80CPQ020-N3	25	500	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?96138</u>			
Part marking information	www.vishay.com/doc?95007		
SPICE model	www.vishay.com/doc?95289		



TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.254		0.0)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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Vishay

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