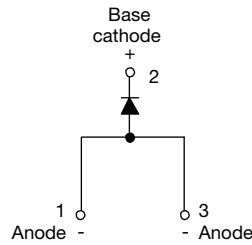


Surface Mount Fast Soft Recovery Rectifier Diode, 8 A



FEATURES

- Glass passivated pellet chip junction
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- On-board and off-board EV / HEV battery chargers
- Renewable energy inverters

DESCRIPTION

The VS-8EWF06SLHM3 fast soft recovery rectifier series has been optimized for combined short reverse recovery time, low forward voltage drop and low leakage current.

The glass passivation ensures stable reliable operation in the most severe temperature and power cycling conditions.

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	8 A
V_R	600 V
V_F at I_F	1.2 V
I_{FSM}	150 A
t_{rr}	55 ns
T_J max.	150 °C
Snap factor	0.5
Package	DPAK (TO-252AA)
Circuit configuration	Single

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Sinusoidal waveform	8	A
V_{RRM}		600	V
I_{FSM}		150	A
V_F	8 A, $T_J = 25$ °C	1.2	V
t_{rr}	1 A, 100 A/ μ s	55	ns
T_J	Range	-40 to +150	°C

VOLTAGE RATINGS

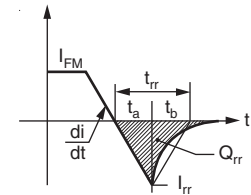
PART NUMBER	V_{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V_{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I_{RRM} AT 150 °C mA
VS-8EWF06SLHM3	600	700	3

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	$I_{F(AV)}$	$T_C = 96$ °C, 180° conduction half sine wave	8	A
Maximum peak one cycle non-repetitive surge current	I_{FSM}	10 ms sine pulse, rated V_{RRM} applied	125	
		10 ms sine pulse, no voltage reapplied	150	
Maximum I^2t for fusing	I^2t	10 ms sine pulse, rated V_{RRM} applied	78	A ² s
		10 ms sine pulse, no voltage reapplied	110	
Maximum $I^2\sqrt{t}$ for fusing	$I^2\sqrt{t}$	$t = 0.1$ ms to 10 ms, no voltage reapplied	1100	A ² \sqrt{s}

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V_{FM}	8 A, $T_J = 25\text{ }^\circ\text{C}$		1.2	V
Forward slope resistance	r_t	$T_J = 150\text{ }^\circ\text{C}$		16	$\text{m}\Omega$
Threshold voltage	$V_{F(TO)}$			1.13	V
Maximum reverse leakage current	I_{RM}	$T_J = 25\text{ }^\circ\text{C}$	$V_R = \text{rated } V_{RRM}$	0.1	mA
		$T_J = 150\text{ }^\circ\text{C}$		3	

RECOVERY CHARACTERISTICS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Reverse recovery time	t_{rr}	I_F at 1 A _{pk} 100 A/ μs $T_J = 25\text{ }^\circ\text{C}$	55	ns
			200	
Reverse recovery current	I_{rr}	I_F at 8 A _{pk} 25 A/ μs $T_J = 25\text{ }^\circ\text{C}$	2.6	A
Reverse recovery charge	Q_{rr}		0.25	μC
Snap factor	S		0.5	



THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		-40 to +150	$^\circ\text{C}$
Maximum thermal resistance, junction to case	R_{thJC}	DC operation	2.5	$^\circ\text{C}/\text{W}$
Typical thermal resistance, junction to ambient (PCB mount)	$R_{thJA}^{(1)}$		50	
Approximate weight			1	g
			0.03	oz.
Marking device		Case style DPAK (TO-252AA)	8EWF06SH	

Note

(1) When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 $^\circ\text{C}/\text{W}$

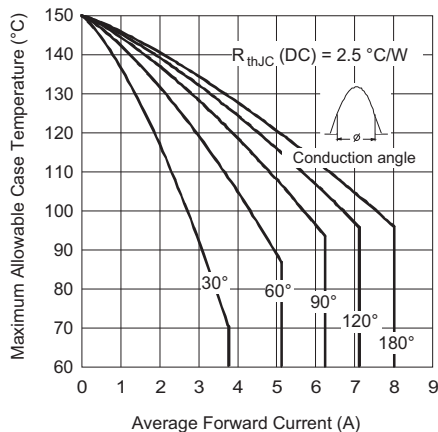


Fig. 1 - Current Rating Characteristics

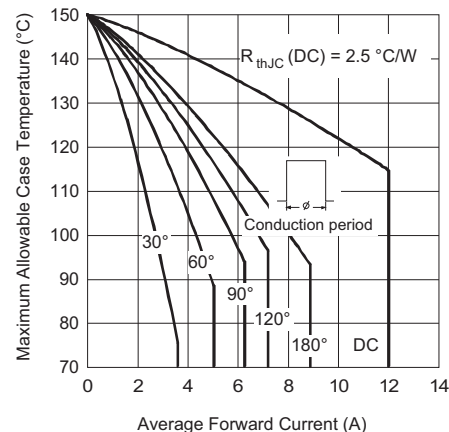


Fig. 2 - Current Rating Characteristics

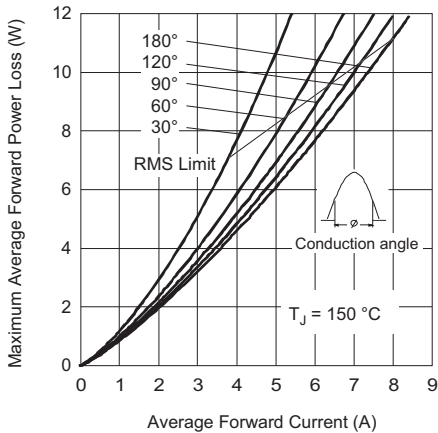


Fig. 3 - Forward Power Loss Characteristics

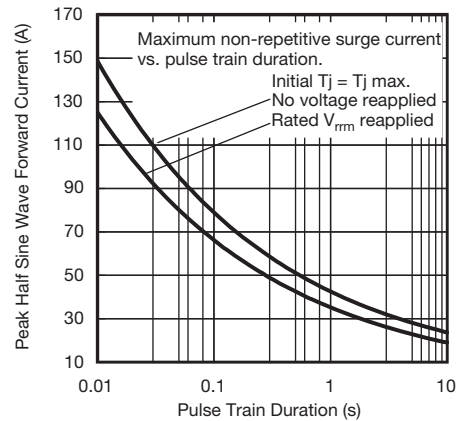


Fig. 6 - Maximum Non-Repetitive Surge Current

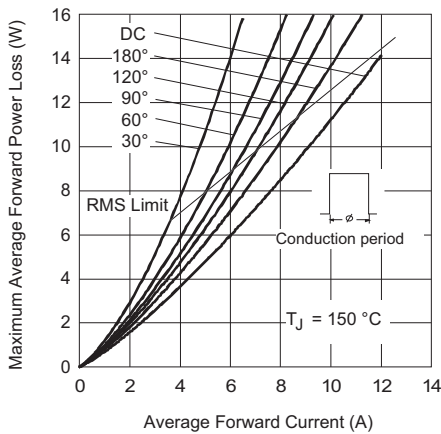


Fig. 4 - Forward Power Loss Characteristics

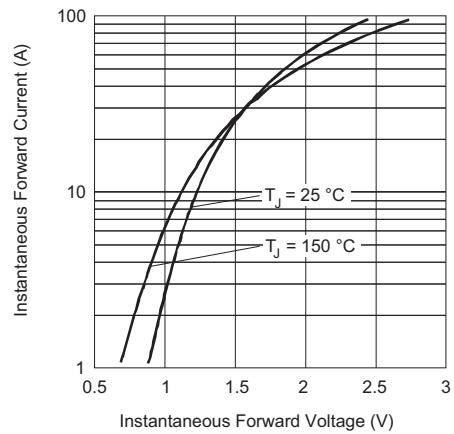


Fig. 7 - Forward Voltage Drop Characteristics

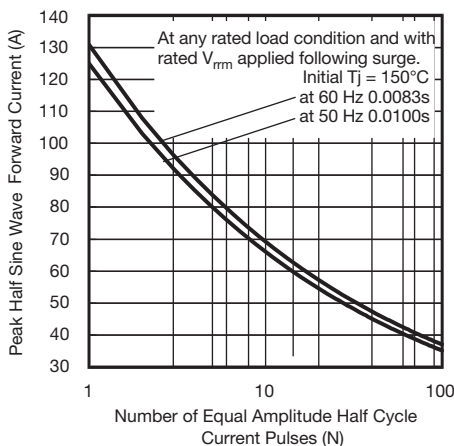


Fig. 5 - Maximum Non-Repetitive Surge Current

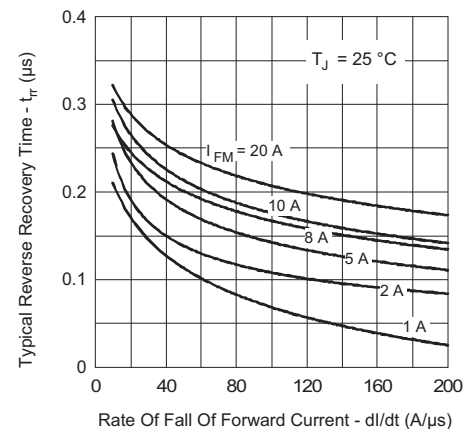


Fig. 8 - Recovery Time Characteristics, $T_J = 25\text{ °C}$

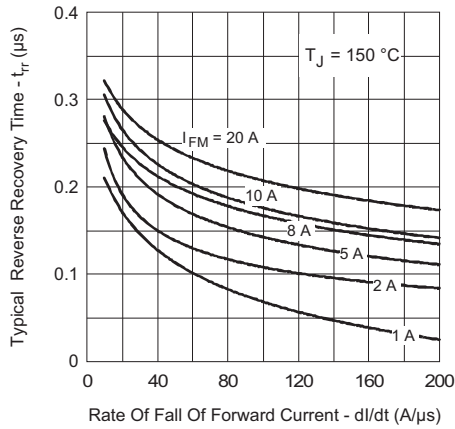


Fig. 9 - Recovery Time Characteristics, $T_J = 150\text{ }^\circ\text{C}$

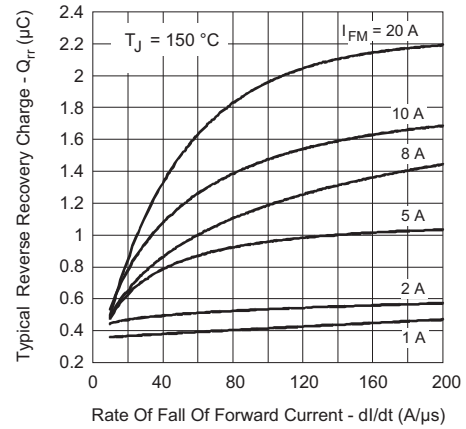


Fig. 11 - Recovery Charge Characteristics, $T_J = 150\text{ }^\circ\text{C}$

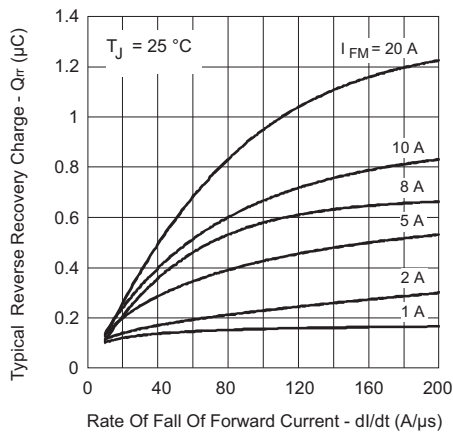


Fig. 10 - Recovery Charge Characteristics, $T_J = 25\text{ }^\circ\text{C}$

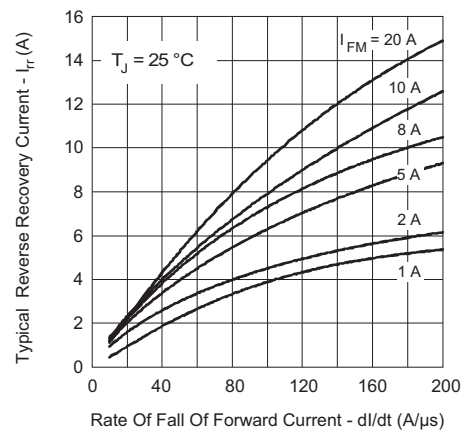


Fig. 12 - Recovery Current Characteristics, $T_J = 25\text{ }^\circ\text{C}$

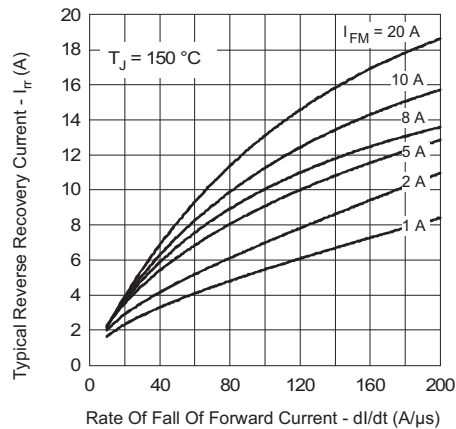
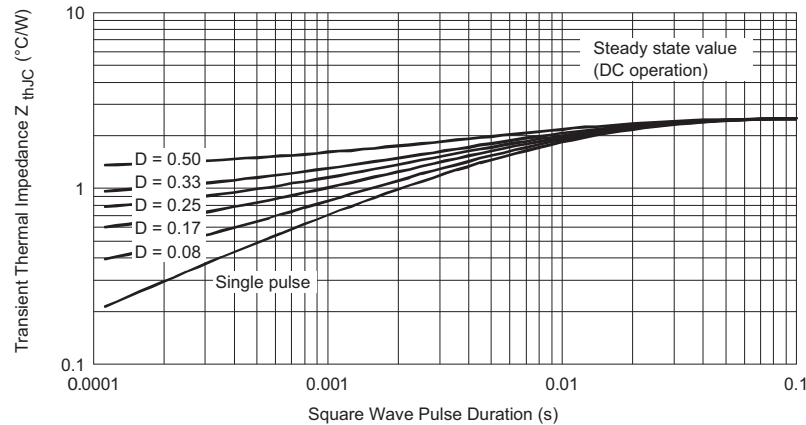


Fig. 13 - Recovery Current Characteristics, $T_J = 150\text{ }^\circ\text{C}$


 Fig. 14 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code	VS-	8	E	W	F	06	S	L	H	M3
	①	②	③	④	⑤	⑥	⑦	⑧	⑨	⑩

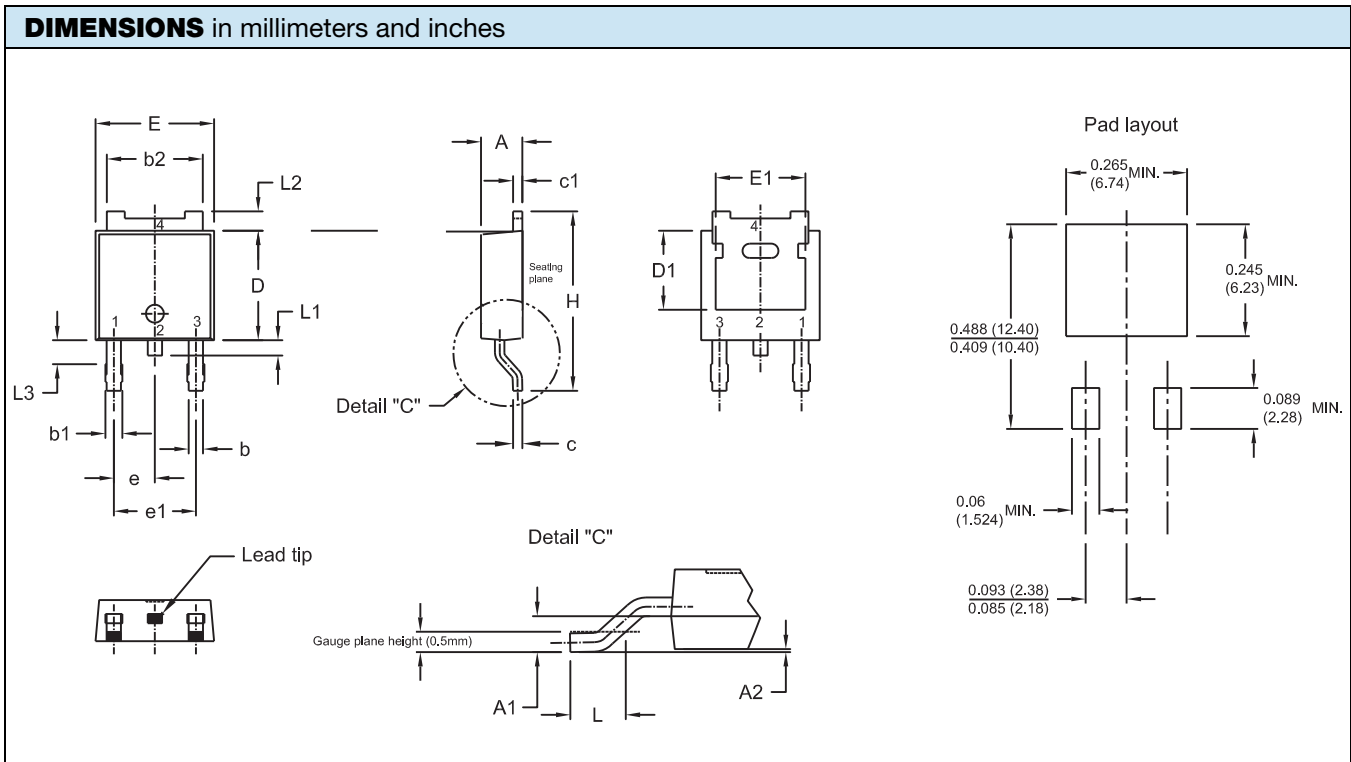
- 1** - Vishay Semiconductors product
- 2** - Current rating (8 = 8 A)
- 3** - Circuit configuration:
E = single
- 4** - Package:
W = DPAK (TO-252AA)
- 5** - Type of silicon:
F = fast soft recovery rectifier
- 6** - Voltage code x 100 = V_{RRM} — **06 = 600 V**
- 7** - S = surface mountable
- 8** - L = tape and reel (left oriented), for different orientation contact factory
- 9** - H = AEC-Q101 qualified
- 10** - Environmental digit:
M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-8EWF06SLHM3	3000	3000	13" diameter reel

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95519
Part marking information	www.vishay.com/doc?95518
Packaging information	www.vishay.com/doc?96495



D-PAK (TO-252AA)



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.			MIN.	MAX.	MIN.	MAX.	
A	2.21	2.38	0.087	0.094		A1	0.89	1.14	0.035	0.045	
A2	0.76	1.14	0.030	0.045		H	9.65	10.41	0.380	0.410	
b	0.71	0.88	0.028	0.035		L	1.40	1.78	0.055	0.070	
b1	0.76	1.14	0.030	0.045		e	2.28 BSC		0.09 BSC		
b2	5.23	5.44	0.206	0.214		e1	4.57 BSC		0.18 BSC		
C	0.46	0.58	0.018	0.023		L1	0.64	1.02	0.025	0.040	
C1	0.46	0.58	0.018	0.023		L2	0.89	1.27	0.035	0.050	
D	5.97	6.22	0.235	0.2455		L3	1.15	1.52	0.040	0.060	
D1	4.32	4.45	0.170	0.175							
E	6.48	6.73	0.255	0.2655							
E1	4.49	5.50	0.177	0.217							

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L3 only for reference
- (3) Dimension D1, E1, L2 and b2 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash.
- (5) Outline conforms to JEDEC outline TO-252AA



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