

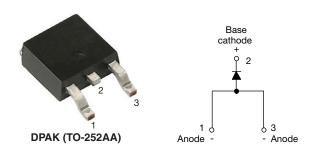
Vishay Semiconductors

RoHS

COMPLIANT HALOGEN

FREE

High Voltage Surface Mount Input Rectifier Diode, 8 A



PRIMARY CHARACTERISTICS					
I _{F(AV)} 8 A					
V_{R}	1600 V				
V _F at I _F	1.1 V				
I _{FSM}	150 A				
T _J max.	150 °C				
Package	DPAK (TO-252AA)				
Circuit configuration	Single				

FEATURES

- Glass passivated pellet chip junction
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- On-board and off-board EV / HEV battery chargers
- Renewable energy inverters

DESCRIPTION

The VS-8EWS16SLHM3 rectifier high voltage series has been optimized for very low forward voltage drop, with moderate leakage.

The **high reverse voltage** range available allows design of input stage primary rectification with **outstanding voltage surge** capability.

OUTPUT CURRENT IN TYPICAL APPLICATIONS							
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS							
NEMA FR-4 or G10 glass fabric-based epoxy with 4 oz. (140 µm) copper	1.2	1.6	٨				
Aluminum IMS, R _{thCA} = 15 °C/W	2.5	2.8	Α				
Aluminum IMS with heatsink, R _{thCA} = 5 °C/W	5.5	6.5					

Note

T_A = 55 °C, T_J = 125 °C, footprint 300 mm²

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS VALUES UNITS						
I _{F(AV)}	Sinusoidal waveform	8	A				
V _{RRM}		1600	V				
I _{FSM}		150	А				
V _F	8 A, T _J = 25 °C	1.10	V				
T _J		-40 to +150	°C				

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA				
VS-8EWS16SLHM3	1600	1700	0.5				



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ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum average forward current	I _{F(AV)}	T _C = 105 °C, 180° conduction half sine wave	8				
Maximum peak one cycle	1	10 ms sine pulse, rated V _{RRM} applied	125	Α			
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	150				
Maximum I ² t for fusing	I ² †	10 ms sine pulse, rated V _{RRM} applied	78	A ² s			
Waxiifidiff i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	ed 110				
Maximum I ² √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1100	A²√s			

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL TEST CONDITIONS VALUES UNITS				UNITS	
Maximum forward voltage drop	V_{FM}	8 A, T _J	= 25 °C	1.1	V	
Forward slope resistance	r _t	T _{.I} = 150 °C		20	mΩ	
Threshold voltage	V _{F(TO)}		150 C	0.82	V	
Maximum reverse leakage current	1	T _J = 25 °C		0.05	mA	
iviaximum reverse leakage current	I _{RM}	T _J = 150 °C	V _R = Rated V _{RRM}	0.50	IIIA	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C		
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	°C/W		
Typical thermal resistance, junction to ambient (PCB mount)	R _{thJA} ⁽¹⁾		62	C/VV		
Approximate weight			1	g		
Approximate weight			0.03	oz.		
Marking device		Case style DPAK (TO-252AA)	8EWS	16SH		

Note

⁽¹⁾ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 µm) copper 40 °C/W

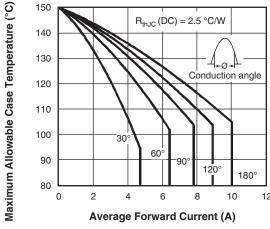


Fig. 1 - Current Rating Characteristics

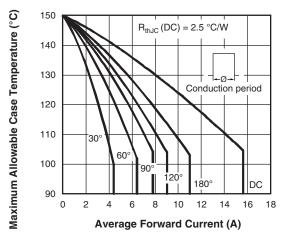


Fig. 2 - Current Rating Characteristics





Maximum Average Forward Power Loss (W)

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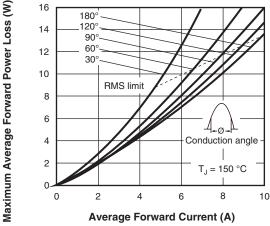
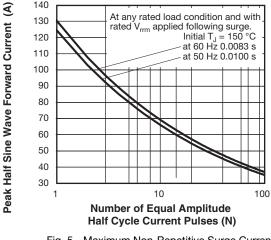


Fig. 3 - Forward Power Loss Characteristics



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Fig. 5 - Maximum Non-Repetitive Surge Current

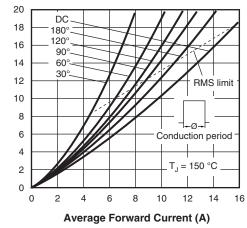


Fig. 4 - Forward Power Loss Characteristics

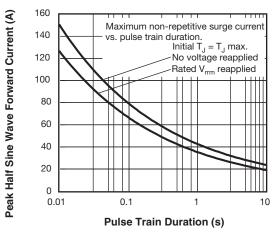


Fig. 6 - Maximum Non-Repetitive Surge Current

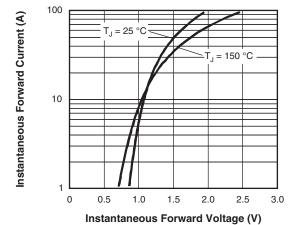


Fig. 7 - Forward Voltage Drop Characteristics

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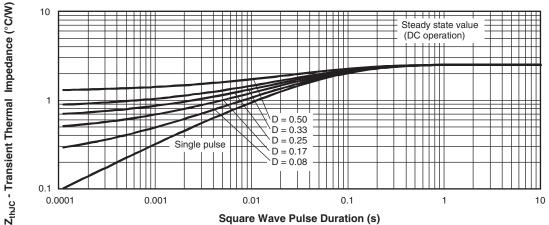


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code	VS-	8	Е	W	S	16	S	L	Н	М3

1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

3 - Circuit configuration:

E = single

4 - Package:

W = DPAK (TO-252AA)

5 - Type of silicon:

S = standard recovery rectifier

6 - Voltage code x 100 = V_{RRM} (16 = 1600 V)

7 - S = surface mountable

8 - L = tape and reel (left oriented), for different orientation contact factory

(6)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

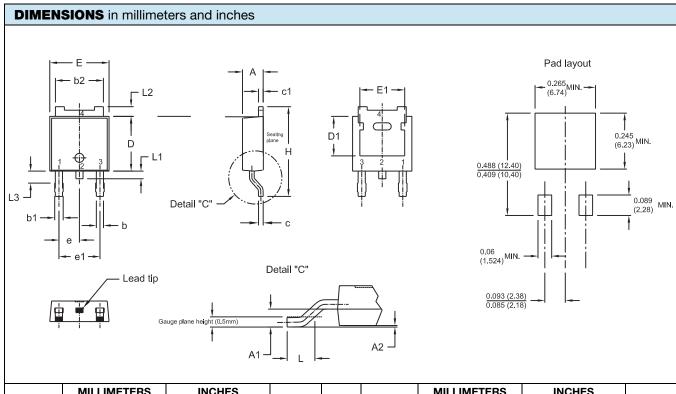
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-8EWS16SLHM3	3000	3000	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?96495			



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D-PAK (TO-252AA)



SYMBOL	MILLIN	MILLIMETERS		INCHES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.21	2.38	0.087	0.094	
A2	0.03	0.127	0.001	0.005	
b	0.71	0.88	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.44	0.206	0.214	
С	0.46	0.58	0.018	0.023	
C1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.2455	
D1	4.32	4.45	0.170	0.175	
Е	6.48	6.73	0.255	0.2655	
E1	4.49	5.50	0.177	0.217	

SYMBOL	MILLIM	IETERS	INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
A1	0.89	1.14	0.035	0.045	
Н	9.65	10.41	0.380	0.410	
L	1.40	1.78	0.055	0.070	
е	2.28	BSC	C 0.09 BSC		
e1	4.57	4.57 BSC		0.18 BSC	
L1	0.64	1.02	0.025	0.040	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.040	0.060	
	•				

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L3 only for reference
- (3) Dimension D1, E1, L2 and b2 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash.
- (5) Outline conforms to JEDEC outline TO-252AA



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