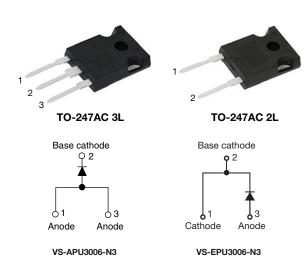


Vishay Semiconductors

Ultrafast Rectifier, 30 A FRED Pt®



PRIMARY CHARACTERISTICS						
I _{F(AV)}	30 A					
V_R	600 V					
V _F at I _F	1.15 V					
t _{rr} typ.	30 ns					
T _J max.	175 °C					
Package	TO-247AC 3L, TO-247AC 2L					
Circuit configuration	Single					

FEATURES

- · Low forward voltage drop
- Ultrafast recovery time
- 175 °C operating junction temperature
- Designed and qualified according to JEDEC®-JESD 47





DESCRIPTION

Ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units, and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS				
Repetitive peak reverse voltage	V_{RRM}		600	V				
Average rectified forward current	I _{F(AV)}	T _C = 127 °C	30	^				
Non-repetitive peak surge current	I _{FSM}	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	220	Α				
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C				

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-			
Established	V _F	I _F = 30 A - 1.4		1.4	2	V		
Forward voltage		I _F = 30 A, T _J = 150 °C	-	1.15	1.35			
D		$V_R = V_R$ rated	-	-	30			
Reverse leakage current	I _R	T _J = 150 °C, V _R = V _R rated	-	-	250	μA		
Junction capacitance	C _T	V _R = 600 V	-	20	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH		



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST C	ONDITIONS	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A, } dI_F/dt = 50$) A/μs, V _R = 30 V	-	30	45			
Reverse recovery time	t _{rr}	T _J = 25 °C		-	45	-	ns		
		T _J = 125 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	100	-			
Peak recovery current		T _J = 25 °C		-	5.6	-	А		
	IRRM	T _J = 125 °C		-	10	-			
Reverse recovery charge	0	T _J = 25 °C		-	127	-			
	Q _{rr}	T _J = 125 °C		-	580	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T_J, T_{Stg}		-65	-	175	°C	
Thermal resistance, junction to case	R_{thJC}			0.7	1.1	°C/W	
Thermal resistance, junction to ambient per leg	R_{thJA}	Typical socket mount	-	-	40		
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased		0.5	-		
Weight			-	2.0	-	g	
weight			-	0.07	-	OZ.	
Mounting torque			1.2 (10)	-	2.4 (20)	kgf · cm (lbf · in)	
Maddan dada		Case style TO-247AC 3L	APU3006				
Marking device		Case style TO-247AC 2L	EPU3006				

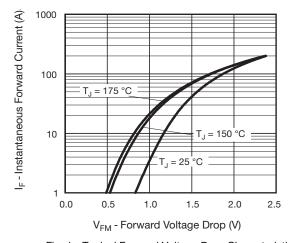


Fig. 1 - Typical Forward Voltage Drop Characteristics

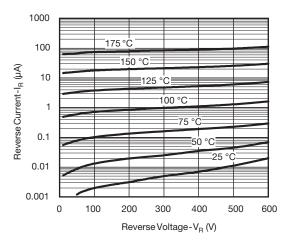


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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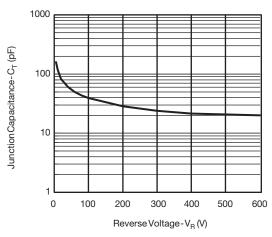


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

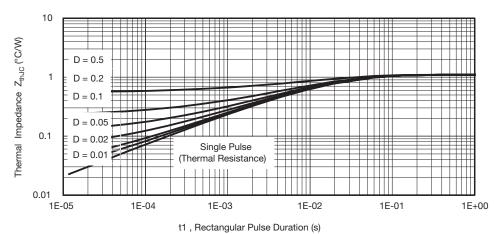


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

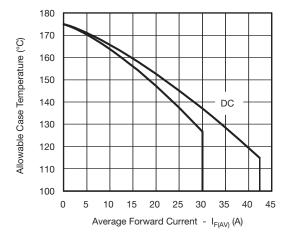


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

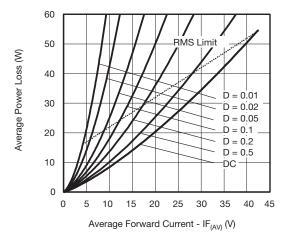


Fig. 6 - Forward Power Loss Characteristics

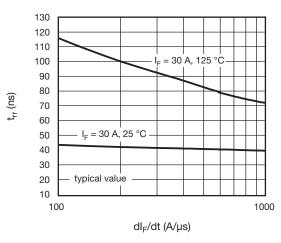


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

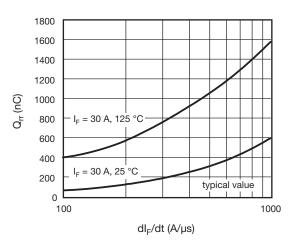
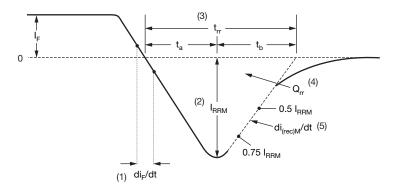


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_F$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

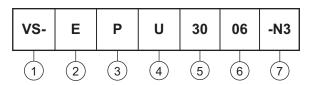
Fig. 9 - Reverse Recovery Waveform and Definitions

VS-APU3006-N3, VS-EPU3006-N3

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Circuit configuration

• E = single diode, 2 pins

• A = single diode, 3 pins

3 - P = TO-247AC

U = ultrafast recovery time

5 - Current code (30 = 30 A)

6 - Voltage code (06 = 600 V)

7 - Environmental digit:

-N3 = halogen-free, RoHS-compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-APU3006-N3	25	500	Antistatic plastic tube			
VS-EPU3006-N3	25	500	Antistatic plastic tube			

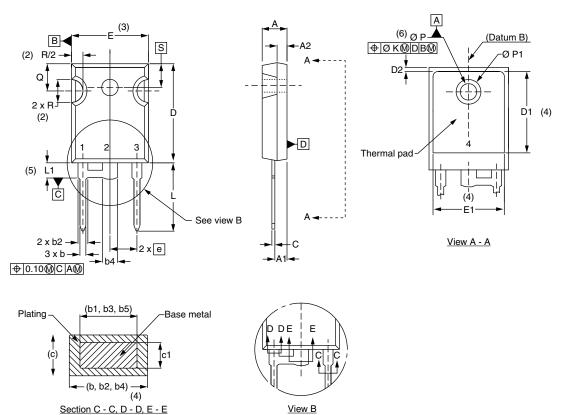
LINKS TO RELATED DOCUMENTS				
Dimensions	TO-247AC 3L	www.vishay.com/doc?93138		
Dimensions	TO-247AC 2L	www.vishay.com/doc?96144		
Port marking information	TO-247AC 3L	www.vishay.com/doc?95007		
Part marking information	TO-247AC 2L	www.vishay.com/doc?95442		



Vishay Semiconductors

TO-247AC modified - 50 mils L/F

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIM	IETERS	INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	254	0.0)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	=	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	S 5.51 BSC 0.217 BSC				

Notes

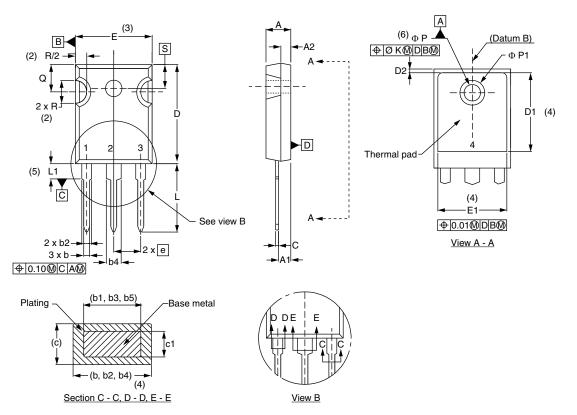
- (1) Dimensioning and tolerance per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



Vishay Semiconductors

TO-247AC - 50 mils L/F

DIMENSIONS in millimeters and inches



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