VS-E4PH6006L-N3

Vishay Semiconductors



Revision: 19-Feb-2019

Hyperfast Soft Recovery Diode, 60 A FRED Pt® Gen 4



PRIMARY CHARACTERISTICS								
I _{F(AV)}	60 A							
V _R	600 V							
V _F at I _F	1.48 V							
t _{rr} typ.	see Recovery table							
T _J max.	175 °C							
Package	TO-247AD 2L							
Circuit configuration	Single							

FEATURES

- Gen 4 FRED Pt[®] technology
- Low I_{BBM} and reverse recovery charge
- · Very low forward voltage drop
- · Polyimide passivated chip for high reliability standard
- 175 °C operating junction temperature
- Designed and gualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

Gen 4 Fred technology, state of the art, ultralow V_F, soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS					
Cathode to anode voltage	V _R		600	V					
Average rectified forward current	I _{F(AV)}	T _C = 106 °C	60	٨					
Single pulse forward current	I _{FSM}	T_C = 25 °C, t_p = 8.3 ms, half sine wave	425	A					
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C					

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)										
PARAMETER SYMBOL TEST CONDITIONS MIN. TYP. MAX										
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-					
		I _F = 50 A	-	1.68	-	1				
		I _F = 60 A	-	1.75	2.0	V				
Forward voltage	V	I _F = 50 A, T _J = 125 °C	-	1.44	-					
	V _F	I _F = 60 A, T _J = 125 °C	-	1.55	-					
		I _F = 50 A, T _J = 150 °C	-	1.39	-					
		I _F = 60 A, T _J = 150 °C	-	1.48	1.65	1				
Poverse leskage surrent	1	V _R = V _R rated	-	-	50					
Reverse leakage current	IR	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA 00				
Junction capacitance	CT	V _R = 600 V	-	30	-	pF				

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \ ^{\circ}C$ unless otherwise specified)										
PARAMETER	SYMBOL	TEST C	ONDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time	+	T _J = 25 °C		-	68	-	ns			
	t _{rr}	T _J = 125 °C	I _F = 60 A dI _F /dt = 1000 A/μs V _B = 400 V	-	92	-				
Peak recovery current	I _{RRM}	T _J = 25 °C		-	20	-	A nC			
Feat recovery current		T _J = 125 °C		-	40	-				
Reverse recovery charge	Q _{rr}	$T_J = 25 \ ^\circ C$		-	945	-				
		T _J = 125 °C		-	2500	-				

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Thermal resistance, junction to case	R _{thJC}		-	-	0.6					
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	40	°C/W				
Thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.25	-					
Weight			-	6.0	-	g				
Weight			-	0.21	-	oz.				
Mounting torque			6.0 (5)	-	12 (20)	kgf · cm (lbf · in)				
Marking device		Case style TO-247AD 2L	E4PH6006L							

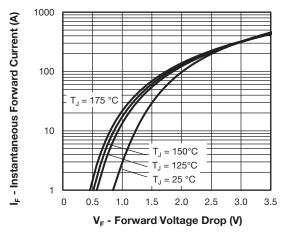


Fig. 1 - Typical Forward Voltage Drop Characteristics

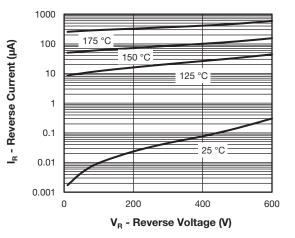


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



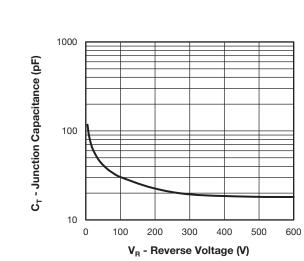


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

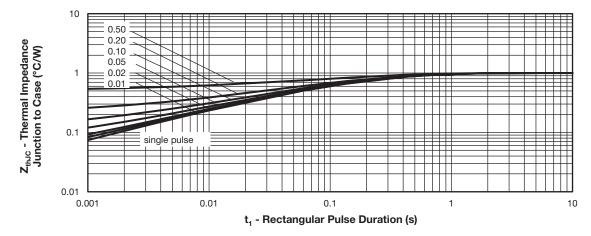
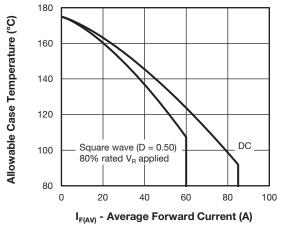
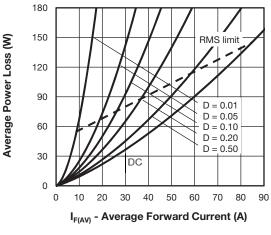


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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Note

⁽¹⁾ Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;

 $\begin{array}{l} Pd = forward \ power \ loss = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ Fig.5) \\ P_{dREV} = inverse \ power \ loss = V_{R1} \ x \ I_{R} \ (1 - D); \ I_{R} \ at \ V_{R} = rated \ V_{R} \end{array}$

Revision: 19-Feb-2019

3

Document Number: 95898

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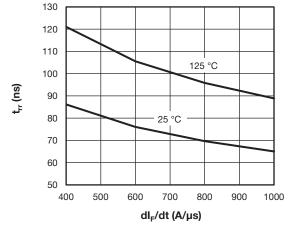


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

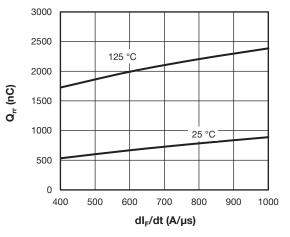


Fig. 8 - Typical Stored Charge vs. dl_F/dt

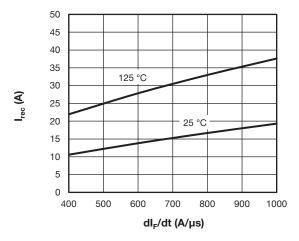


Fig. 9 - Typical Reverse Recovery vs. dl_F/dt



ORDERING INFORMATION TABLE

Device code	VS-	E	4	Р	Н	60	06	L	-N3
	1	2	3	4	5	6	7	8	9
	 Vishay Semiconductors product Circuit configuration: E = single diode 2 pins 								
	 3 - FRED Gen 4 4 - P = TO-247 package 5 - Process type: H = hyperfast recovery 								
	6 - 7 - 8 - 9 -	Cur Volt L = Env	rent rati age rati long lea ironmen	ng (60 = ng (06 = d ital digit:	60 A) 600 V)		nt, and	totally I	ead (Pb

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-E4PH6006L-N3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95536						
Part marking information	www.vishay.com/doc?95648						



TO-247AD 2L

DIMENSIONS in millimeters and inches



Section C - C, D - D

(b, b2)

(4)

View	<u>/ B</u>

SYMBOL	MILLIN	IETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES		STMDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	5 BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0	010	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	
D2	0.51	1.35	0.020	0.053				•		•		•

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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