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Vishay Semiconductors

COMPLIANT

HALOGEN

FREE

Hyperfast Rectifier, 30 A FRED Pt®



2L TO-220 FullPAK

PRIMARY CHARACTERISTICS					
I _{F(AV)} 30 A					
V _R	600 V				
V _F at I _F	1.4 V				
t _{rr} (typ.)	27 ns				
T _J max.	175 °C				
Package	2L TO-220 FullPAK				
Circuit configuration	Single				

FEATURES

- Hyperfast soft recovery time
- · Low forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V_{RRM}		600	V		
Average rectified forward current in DC	I _{F(AV)}	T _C = 51 °C	30	۸		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	180	A		
Operating junction and storage temperatures	T_J , T_{Stg}		-65 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-		
Forward voltage		I _F = 30 A	-	2.0	2.65	V	
	V _F	I _F = 30 A, T _J = 150 °C	-	1.4	1.8		
Daviera lackage surrent		$V_R = V_R$ rated	-	0.02	30		
Reverse leakage current I _R		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	50	300	μA	
Junction capacitance	C _T	V _R = 600 V	-	20	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH	





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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t _{rr}	$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	26	35	
Reverse recovery time		T _J = 25 °C	I _F = 30 A, dI _F /dt = 200 A/μs, V _R = 200 V	-	26	-	ns
		T _J = 125 °C		-	70	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	3.5	-	- A - nC
		T _J = 125 °C		-	7.6	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	50	-	
		T _J = 125 °C		-	280	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction-to-case	R _{thJC}		ı	3.2	3.8	
Thermal resistance, junction-to-ambient	R _{thJA}	Typical socket mount	-	-	70	°C/W
Typical thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Maight			-	2	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style 2L TO-220 FullPAK	ETH3006FP			



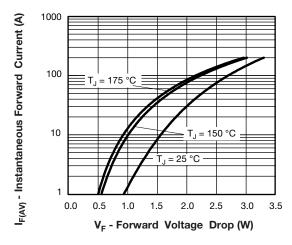


Fig. 1 - Typical Forward Voltage Drop Characteristics

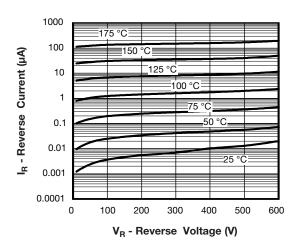


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

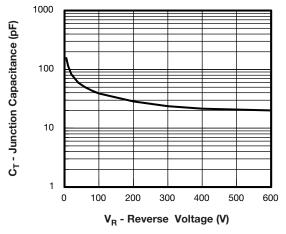


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

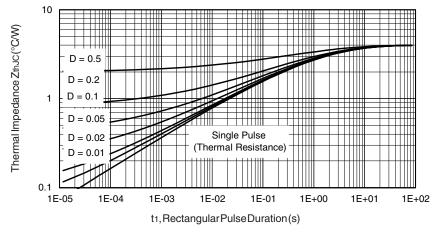


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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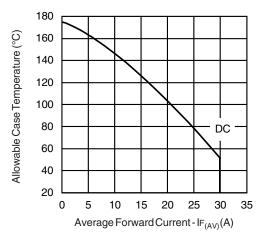


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

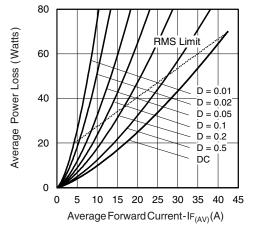


Fig. 6 - Forward Power Loss Characteristics

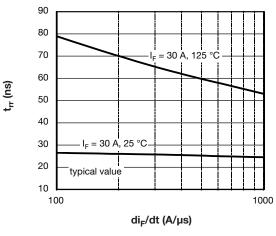


Fig. 7 - Typical Reverse Recovery vs. dI_F/dt

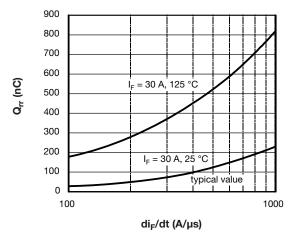
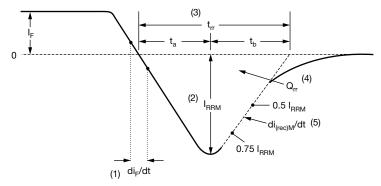


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RBM}$ and 0.50 $\rm I_{RBM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

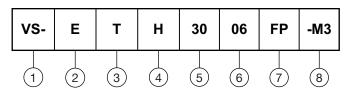
(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Circuit configuration:

E = single

3 - T = TO-220

H = hyperfast recovery time

5 - Current code: 30 = 30 A

6 - Voltage code: 06 = 600 V

7 - FP = 2L TO-220 FullPAK

8 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

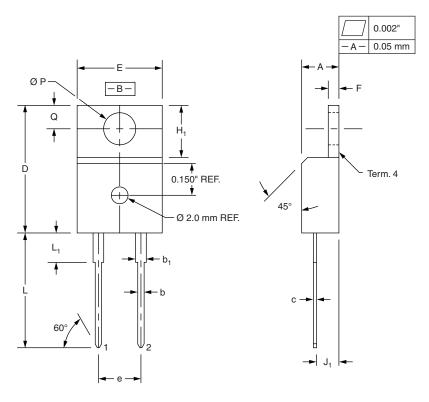
ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-ETH3006FP-M3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96157			
Part marking information	www.vishay.com/doc?95392			
SPICE model	www.vishay.com/doc?96817			



True 2 Pin TO-220

DIMENSIONS in millimeters and inches



SYMBOL —	MILLIN	METERS	INC	HES
	MIN.	MAX.	MIN.	MAX.
A	4.32	4.57	0.170	0.180
b	0.71	0.91	0.028	0.036
b ₁	1.15	1.39	0.045	0.055
С	0.36	0.53	0.014	0.021
D	14.99	15.49	0.590	0.610
Е	10.04	10.41	0.395	0.410
е	5.08	BSC	0.200	BSC
F	1.22	1.37	0.048	0.054
H ₁	5.97	6.47	0.235	0.255
J ₁	2.54	2.79	0.100	0.110
L	13.47	13.97	0.530	0.550
L ₁ ⁽¹⁾	3.31	3.81	0.130	0.150
Ø P	3.79	3.88	0.149	0.153
Q	2.60	2.84	0.102	0.112

Notes

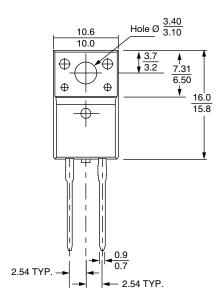
- (1) Lead dimension and finish uncontrolled in L₁
- These dimensions are within allowable dimensions of JEDEC TO-220AB rev. J outline dated 3-24-87
- Controling dimension: Inch

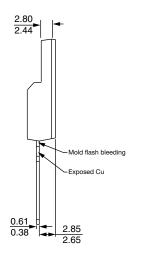


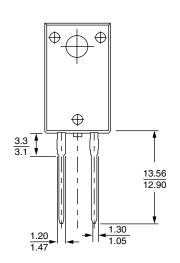


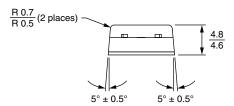
2L TO-220 FullPAK

DIMENSIONS in millimeters









Bottom view



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