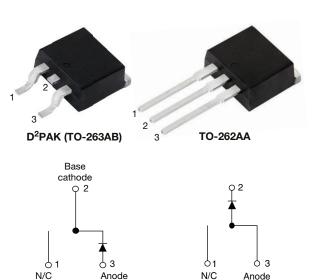
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VS-ETX1506S-M3, VS-ETX1506-1-M3

Vishay Semiconductors

Hyperfast Rectifier, 15 A FRED Pt[®]



PRIMARY CHARACTERISTICS I_{F(AV)} 15 A V_R 600 V 1.55 V V_F at I_F 18 ns t_{rr} (typ.) T_J max. 175 °C D²PAK (TO-263AB), TO-262AA Package Circuit configuration Single

VS-ETX1506-1-M3

VS-ETX1506S-M3

FEATURES

- Hyperfast recovery time, extremely low Q_{rr}
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Designed and gualified according to JEDEC[®]-JESD 47
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recover time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Repetitive peak reverse voltage	V _{RRM}		600	V		
Average rectified forward current	I _{F(AV)}	T _C = 127 °C	15	٨		
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	120	A		
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C		

ELECTRICAL SPECIFICATIONS ($T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-	
Forward voltage	VF	I _F = 15 A	-	- 2.5 3.4 V		
Torward voltage	۷F	I _F = 15 A, T _J = 150 °C	-	1.55	2	
Reverse leakage current	1	$V_{R} = V_{R}$ rated	-	0.02	36	μA
neverse leakage current	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	40	250	μA
Junction capacitance	CT	V _R = 600 V	-	12	-	pF
Series inductance	LS	Measured lead to lead 5 mm from package body	-	8.0	-	nH

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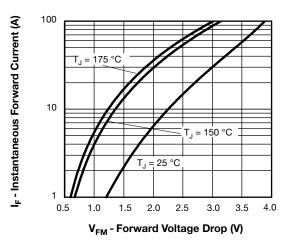


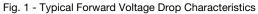
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DYNAMIC RECOVERY	CHARACI	TERISTICS (T _J =	25 °C unless otherw	vise specif	ied)		
PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 1$	00 A/µs, V _R = 30 V	-	17	23	
Reverse recovery time	t _{rr}	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 1000 $	100 A/µs, V _R = 30 V	-	18	30	ns
neverse recovery time	۲r	T _J = 25 °C		-	20	-	115
		T _J = 125 °C	I _F = 15 A dI _F /dt = 200 A/µs V _R = 390 V	-	45	-	
Peak recovery current	I	T _J = 25 °C		-	2.7	-	А
Feak recovery current	I _{RRM}	T _J = 125 °C		-	5.5	-	~
Reverse recovery charge	0	T _J = 25 °C		-	26	-	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	130	-	10
Reverse recovery time	t _{rr}		I _F = 15 A	-	32	-	ns
Peak recovery current	I _{RRM}	T _J = 125 °C	dI _F /dt = 800 A/µs	-	17	-	А
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	290	-	nC

THERMAL - MECHANI	CAL SPEC	IFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	1.3	1.51	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking davias		Case style D ² PAK (TO-263AB)		ETX1	506S	
Marking device		Case style TO-262		ETX1	506-1	





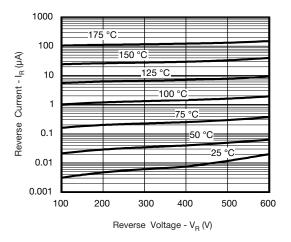


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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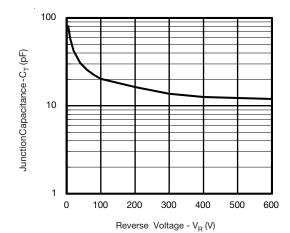


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

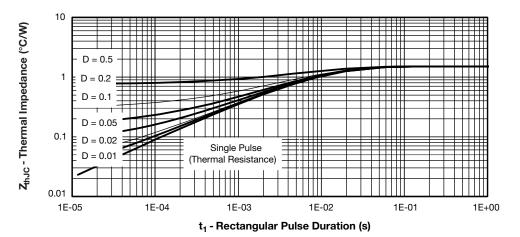
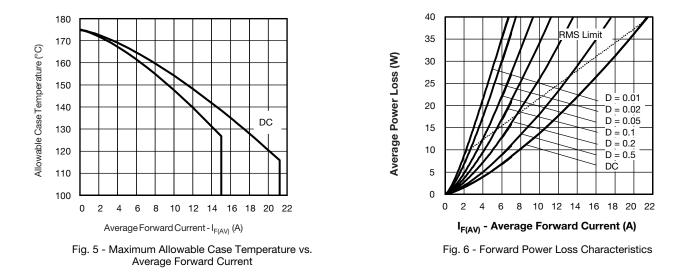


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics



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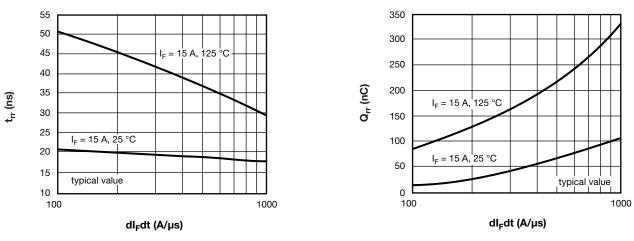


Fig. 7 - Typical Reverse Recovery vs. dl_F/dt

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Fig. 8 - Typical Stored Charge vs. dl_F/dt

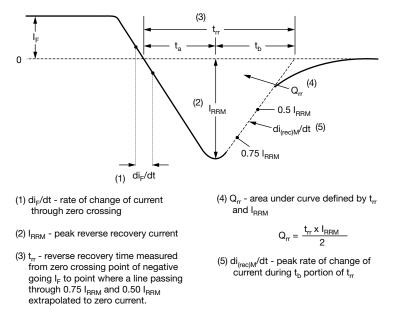


Fig. 9 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE

Device code	VS-	E	т	x	15	06	S	TRL	-M3
	1	2	3	4	5	6	7	8	9
	1 -	· Visł	nay Sem	niconduc	ctors pro	oduct			
	2 -		uit confi single c	-	ı				
	3 -		TO-220						
	4 -	- X =	hyperfa	st recov	ery time	9			
	5 -	- Cur	rent cod	le (15 =	15 A)				
	6 -	· Volt	age coo	le (06 =	600 V)				
	7 -	• S	= D ² PAI	K (TO-2	63AB)				
	-	• •-1	= TO-20	62AA					
	8 -	- • No	one = tu	be (50 p	oieces)				
	-	• TF	RL = tap	e and re	el (left o	oriented	l, for D ²	PAK (T	O-263A
	-	• TF	RR = tap	e and re	eel (righ	t oriente	ed, for I	D ² PAK	(TO-263
	9 -	M3	= halog	gen-free	, RoHS-	complia	ant and	termina	ations le

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-ETX1506S-M3	50	1000	Antistatic plastic tube			
VS-ETX1506-1-M3	50	1000	Antistatic plastic tube			
VS-ETX1506STRR-M3	800	800	13" diameter reel			
VS-ETX1506STRL-M3	800	800	13" diameter reel			

	LINKS TO RELATED DO	CUMENTS
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164
	TO-262AA	www.vishay.com/doc?96165
Dort marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444
Part marking information	TO-262AA	www.vishay.com/doc?95443
Packaging information	D ² PAK (TO-263AB)	www.vishay.com/doc?96424

Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



ota	ted	90	°C
<u>S</u>	cale	<u>ə:</u> 8	:1

SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
с	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL		MILLIM	ETERS	INC	NOTES	
	STNDUL	MIN.	MAX.	MIN.	MAX.	NOTES
	D1	6.86	8.00	0.270	0.315	3
	E	9.65	10.67	0.380	0.420	2, 3
	E1	7.90	8.80	0.311	0.346	3
	е	2.54 BSC		0.100		
	Н	14.61	15.88	0.575	0.625	
	L	1.78	2.79	0.070	0.110	
	L1	-	1.65	-	0.066	3
	L2	1.27	1.78	0.050	0.070	
	L3	0.25	BSC	0.010	BSC	
	L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Datum A and B to be determined at datum plane H

(6) Controlling dimension: inches

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 13-Jul-17

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Document Number: 96164

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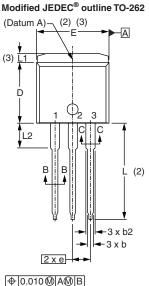
Outline Dimensions

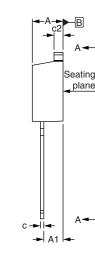


Vishay Semiconductors

TO-262AA

DIMENSIONS in millimeters and inches





F D1 (3) (3) Section A - A Base (4) Plating b1. b3 metal ≰ c1 (4) -(b, b2)-Section B - B and C - C Scale: None





Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode

Lead assignments

CVMPOI	MILLIN	IETERS	INC	HES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100) BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

 ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
 ⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the second dimensioner of the second dimensis of the second dimensioner of the second dimensioner of the the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only (5)

Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

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