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## Hyperfast Rectifier, 15 A FRED Pt®



2L TO-220 FullPAK

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub> 15 A					
V <sub>R</sub>	600 V				
V <sub>F</sub> at I <sub>F</sub>	1.55 V				
t <sub>rr</sub> (typ.)	18 ns				
T <sub>J</sub> max.	175 °C				
Package	2L TO-220 FullPAK				
Circuit configuration	Single				

#### **FEATURES**

- Hyperfast recovery time, extremely low Q<sub>rr</sub>
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- True 2 pin package
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V
Average rectified forward current in DC	I <sub>F(AV)</sub>	T <sub>C</sub> = 71 °C	15	А
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	120	A
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	2.5	3.4	V	
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	1.55	2		
Deverse leakers aurrent		$V_{R} = V_{R}$ rated	-	0.02	36		
Reverse leakage current I <sub>R</sub>		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	40	250	μΑ	
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	12	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH	

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### Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, \ dI_F/dt = 100$	A/µs, V <sub>R</sub> = 30 V	-	17	23		
Reverse recovery time	+	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 10$	0 A/µs, V <sub>R</sub> = 30 V	-	18	30	nc	
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	20	-	ns	
		T <sub>J</sub> = 125 °C	l <sub>F</sub> = 15 A, dl <sub>F</sub> /dt = 200 A/μs, V <sub>R</sub> = 390 V	-	45	-		
Peak recovery current	lanu.	T <sub>J</sub> = 25 °C		-	2.7	-	A	
Feat recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	5.5	-		
Poweree recovery charge	0	T <sub>J</sub> = 25 °C		-	26	-	nC	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	130	-	110	
Reverse recovery time	t <sub>rr</sub>		I <sub>F</sub> = 15 A,	-	32	-	ns	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 800 A/µs,	-	17	-	А	
Reverse recovery charge	Q <sub>rr</sub>		V <sub>R</sub> = 390 V	-	290	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C		
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	3.7	4.3			
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W		
Typical thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-			
Weight			-	2	-	g		
Weight			-	0.07	-	oz.		
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)		
Marking device		Case style 2L TO-220 FullPAK		ETX18	506FP			



25°C

500

500

600

600

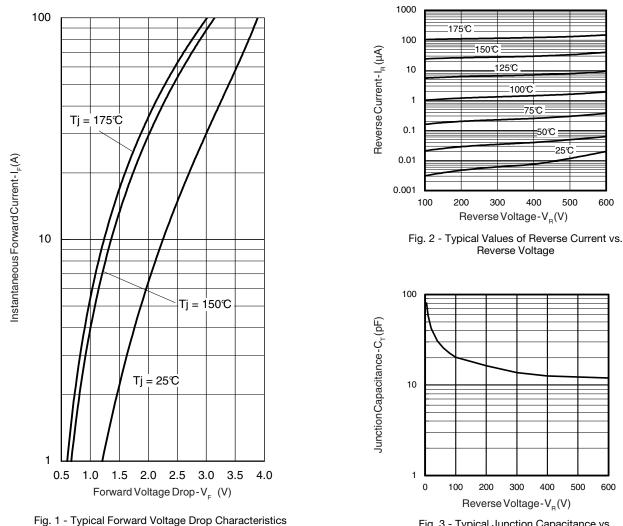
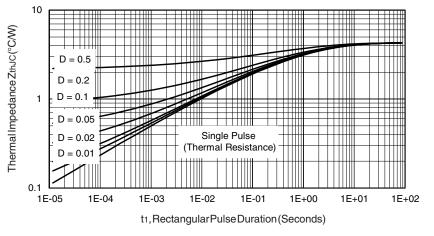


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage





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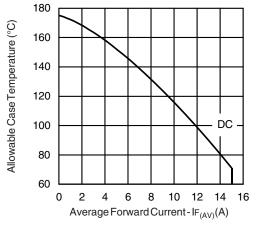


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

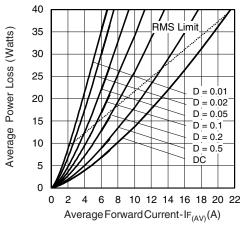
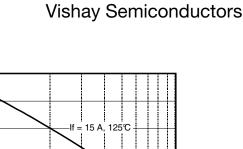
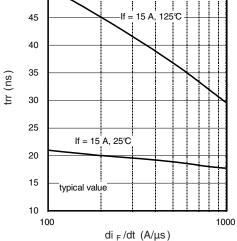


Fig. 6 - Forward Power Loss Characteristics



**VS-ETX1506FP-M3** 



55

50

Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

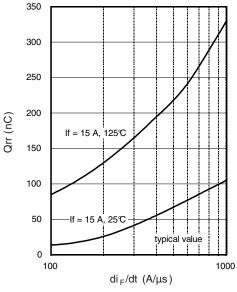


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



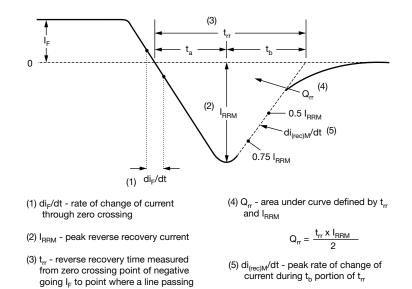


Fig. 9 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

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Device code	VS-	E	т	x	15	06	FP	-M3
		2	3	4	5	6	7	8
	1	- Visl	nay Sem	nicondu	ctors pr	oduct		
	2	- Circ	cuit cont	figuratio	n:			
		E =	single					
	3	- T=	TO-220					
	4	- X =	hyperfa	st reco	ery time	е		
	5	- Cur	rent coo	de: 15 =	15 A			
	6	- Vol	tage coo	de: 06 =	600 V			
	7.	- FP	= 2L TO	-220 Fu	IIPAK			
	8	- Env	ironmer	ntal digit				
		-M3	B = halog	gen-free	e, RoHS	-compli	iant, and	d termir

through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.

ORDERING INFORMATION (Example)							
PREFERRED P/N	REFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-ETX1506FP-M3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96157				
Part marking information	www.vishay.com/doc?95392				

Revision: 25-Jul-2019

Document Number: 93549

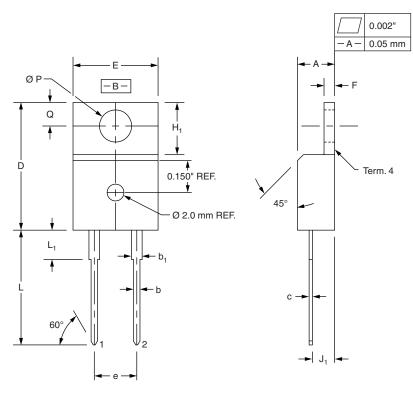
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### True 2 Pin TO-220

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INCH	ES	
SYMBOL	MIN.	MAX.	MIN.	MAX.	
A	4.32	4.57	0.170	0.180	
b	0.71	0.91	0.028	0.036	
b <sub>1</sub>	1.15	1.39	0.045	0.055	
с	0.36	0.53	0.014	0.021	
D	14.99	15.49	0.590	0.610	
E	10.04	10.41	0.395	0.410	
e	5.08	BSC	0.200 BSC		
F	1.22	1.37	0.048	0.054	
H <sub>1</sub>	5.97	6.47	0.235	0.255	
J <sub>1</sub>	2.54	2.79	0.100	0.110	
L	13.47	13.97	0.530	0.550	
L <sub>1</sub> <sup>(1)</sup>	3.31	3.81	0.130	0.150	
ØP	3.79	3.88	0.149	0.153	
Q	2.60	2.84	0.102	0.112	

#### Notes

 $^{\left(1\right)}$  Lead dimension and finish uncontrolled in  $L_{1}$ 

These dimensions are within allowable dimensions of JEDEC TO-220AB rev. J outline dated 3-24-87 ٠

Controling dimension: Inch ٠

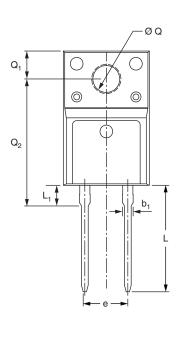
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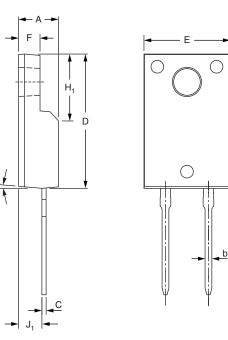


True 2 Pin TO-220 FULL-PAK

θ

#### **DIMENSIONS** in millimeters and inches





SYMBOL	MILLIN	IETERS	INCI	HES
SYMBOL	MIN.	MAX.	MIN.	MAX.
A	4.53	4.93	0.178	0.194
b	0.71	0.91	0.028	0.036
b <sub>1</sub>	1.15	1.39	0.045	0.055
С	0.36	0.53	0.014	0.021
D	15.67	16.07	0.617	0.633
E	9.96	10.36	0.392	0.408
е	5.08 t	ypical	0.200	typical
F	2.34	2.74	0.092	0.107
H <sub>1</sub>	6.50	6.90	0.256	0.272
J <sub>1</sub>	2.56	2.96	0.101	0.117
L	12.78	13.18	0.503	0.519
L <sub>1</sub>	2.23	2.63	0.088	0.104
ØQ	2.98	3.38	0.117	0.133
Q <sub>1</sub>	3.10	3.50	0.122	0.138
Q <sub>2</sub>	14.80	15.20	0.583	0.598
θ	0°	5°	0°	5°



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