

Insulated Gate Bipolar Transistor Ultralow V_{CE(on)}, 250 A



DDIMARY OHARACTERISTICS						
PRIMARY CHARACTERISTICS						
$V_{\sf CES}$	600 V					
V _{CE(on)} (typical) at 200 A, 25 °C	1.33 V					
I _C at T _C = 90 °C	250 A					
Speed	DC to 1 kHz					
Package	SOT-227					
Circuit configuration	Single switch no diode					

FEATURES

· Standard: optimized for minimum saturation voltage and low speed



· Lowest conduction losses available

- Fully isolated package (2500 V_{AC})
- Very low internal inductance (5 nH typical)
- Industry standard outline
- · Designed and qualified for industrial level
- UL approved file E78996



• Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- Designed for increased operating efficiency in power conversion: UPS, SMPS, TIG welding, induction heating
- Easy to assemble and parallel
- · Direct mounting to heatsink
- Plug-in compatible with other SOT-227 packages

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V _{CES}		600	V		
Continuous collector current		T _C = 25 °C	400			
Continuous collector current	Ic	T _C = 90 °C	250			
Pulsed collector current	Ісм	Repetitive rating; V _{GE} = 20 V, pulse width limited by maximum junction temperature	400	А		
Clamped Inductive load current	I _{LM}	V_{CC} = 80 % (V_{CES}), V_{GE} = 20 V, L = 10 μ H, R_g = 2.0 Ω	400			
Gate to emitter voltage	V_{GE}		± 20	V		
Danier diameter	0	T _C = 25 °C	961	w		
Power dissipation	P _D	T _C = 90 °C	462	VV		
Isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V		

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T _J , T _{Stg}		-40	-	150	°C
Thermal resistance junction to case	R _{thJC}		-	-	0.13	°C/W
Thermal resistance case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	C/VV
Weight			-	30	-	g
Mounting toward		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style			SOT-227			



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITI	ONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	$V_{GE} = 0 \text{ V}, I_{C} = 1 \text{ mA}$		600	-	-	
Emitter to collector breakdown voltage	V _{(BR)ECS} (1)	V _{GE} = 0 V, I _C = 1.0 A		18	-	-	
		I _C = 100 A		-	1.10	1.3	V
		I _C = 200 A		-	1.33	1.66	
Collector to emitter voltage	V	I _C = 100 A, T _J = 125 °C	V _{GE} = 15 V	-	1.02	-	
Collector to emitter voltage	VCE(on)	I _C = 200 A, T _J = 125 °C		-	1.32	-	
		I _C = 100 A, T _J = 150 °C		-	1.02	-	
		I _C = 200 A, T _J = 150 °C		-	1.33	-	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}, I_{C} = 250 \mu A$		3.0	4.5	6.0	
date threshold voltage		$V_{CE} = V_{GE}, I_{C} = 250 \mu A,$, T _J = 125 °C	-	3.1	-	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$	$V_{CE} = V_{GE}$, $I_{C} = 1$ mA, 25 °C to 125 °C		-	-12	-	mV/°C
	I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}$		-	20	1000	μA
Collector to emitter leakage current		V _{GE} = 0 V, V _{CE} = 600 V, T _J = 125 °C		ı	0.2	-	mA
		$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$		1	0.6	10	IIIA
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V		-	-	± 250	nA

Note

 $^{^{(1)}~}$ Pulse width $\leq 80~\mu s;~duty~factor \leq 0.1~\%$

SWITCHING CHARACTERIST					T)/D	MAN	LINUTO
PARAMETER	SYMBOL	TEST CONDIT	UNS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I _C = 100 A, V _{CC} = 600 V, V _{GE} = 15 V		-	770	1200	
Gate-to-emitter charge (turn-on)	Q_ge			ı	100	150	nC
Gate-to-collector charge (turn-on)	Q_{gc}			-	260	380	
Turn-on switching loss	E _{on}			-	0.55	-	
Turn-off switching loss	E _{off}	T _{.1} = 25 °C	Energy losses	-	25	-	mJ
Total switching loss	E _{tot}	I _C = 100 A		-	25.5	-	
Turn-on delay time	t _{d(on)}	V _{CC} = 480 V V _{GE} = 15 V		-	267	-	ns
Rise time	t _r	$R_q = 5.0 \Omega$		-	42	-	
Turn-off delay time	t _{d(off)}	L = 500 μH		-	310	-	
Fall time	t _f			-	450	-	
Turn-on switching loss	E _{on}		include tail and diode	-	0.67	-	
Turn-off switching loss	E _{off}	$T_J = 125 ^{\circ}\text{C}$ $I_C = 100 ^{\circ}\text{A}$ $V_{CC} = 480 ^{\circ}\text{V}$	recovery. Diode used 60APH06	-	43.0	-	mJ
Total switching loss	E _{tot}			-	43.7	-	
Turn-on delay time	t _{d(on)}	V _{GE} = 15 V		-	275	-	
Rise time	t _r	$R_g = 5.0 \Omega$ L = 500 µH		-	50	-	1
Turn-off delay time	t _{d(off)}	Σ = 000 μπ		-	350	-	ns
Fall time	t _f			-	700	-	
Internal emitter inductance	LE	Between lead and center of die contact		-	5.0	-	nH
Input capacitance	C _{ies}	V _{GE} = 0 V, V _{CC} = 30 V, f = 1.0 MHz		-	16 250	-	
Output capacitance	C _{oes}			-	1040	-	рF
Reverse transfer capacitance	C _{res}			-	190	-	



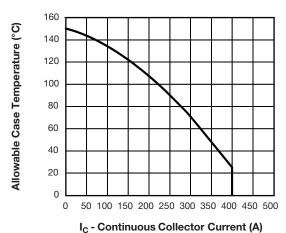
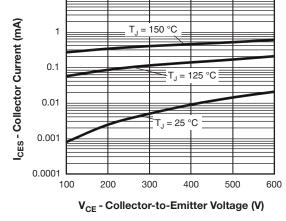


Fig. 1 - Maximum DC IGBT Collector Current vs. Case Temperature



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Fig. 4 - Typical IGBT Zero Gate Voltage Collector Current

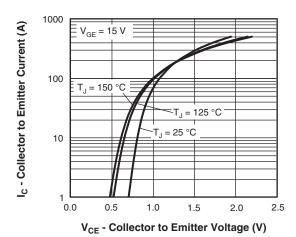


Fig. 2 - Typical Collector to Emitter Current Output Characteristics

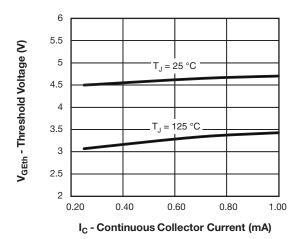


Fig. 5 - Typical IGBT Threshold Voltage

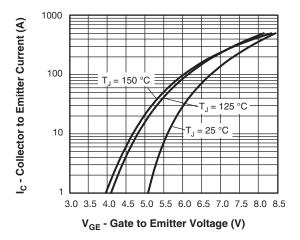


Fig. 3 - Typical IGBT Transfer Characteristics

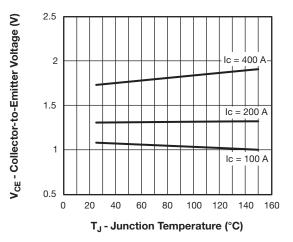


Fig. 6 - Typical IGBT Collector to Emitter Voltage vs. Junction Temperature, $V_{GE} = 15 \text{ V}$

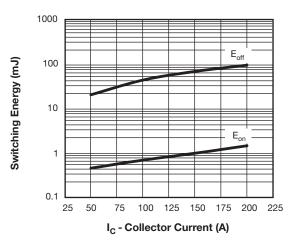


Fig. 7 - Typical IGBT Energy Losses vs. I_C, T_J = 125 °C, V_{CC} = 480 V, V_{GE} = 15 V, L = 500 μ H, R_g = 5 Ω , Diode used: 60APH06

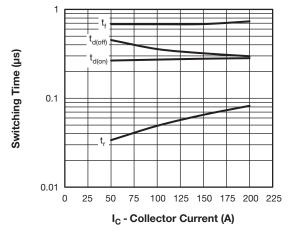


Fig. 8 - Typical IGBT Switching Time vs. $I_C,$ T_J = 125 °C, V_{CC} = 480 V, V_{GE} = 15 V, L = 500 $\mu H,~R_g$ = 5 $\Omega,$ Diode used: 60APH06

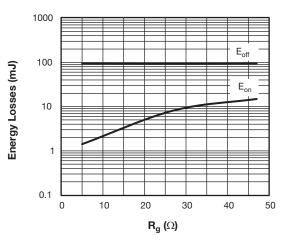


Fig. 9 - Typical IGBT Energy Losses vs. $R_g,$ T_J = 125 °C, I_C = 200 A, V_{CC} = 480 V, V_{GE} = 15 V, L = 500 $\mu H,$ Diode used: 60APH06

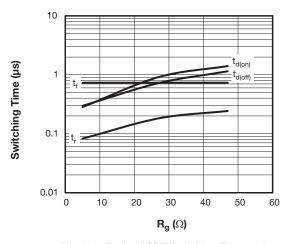


Fig. 10 - Typical IGBT Switching Time vs. $R_g,$ T_J = 125 °C, I_C = 200 A, V_{CC} = 480 V, V_{GE} = 15 V, L = 500 $\,$ µH, Diode used: 60APH06

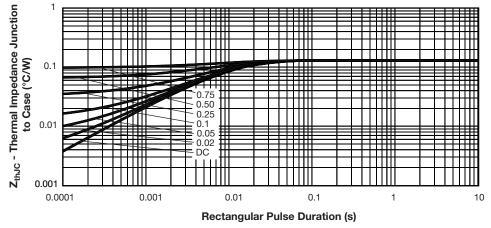


Fig. 11 - Maximum Thermal Impedance Zth,IC Characteristics

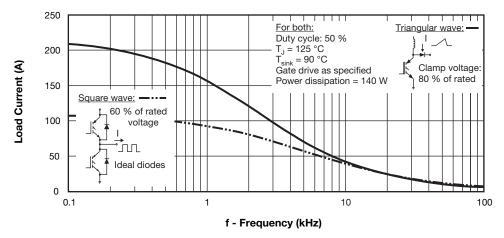


Fig. 12 - Typical Load Current vs. Frequency (Load Current = I_{RMS} of Fundamental)

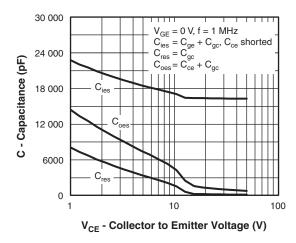


Fig. 13 - Typical Capacitance vs. Collector to Emitter Voltage

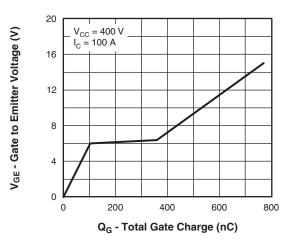


Fig. 14 - Typical Gate Charge vs. Gate to Emitter Voltage

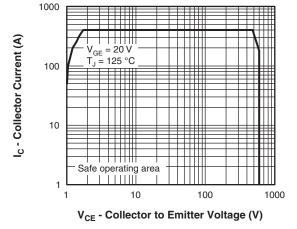
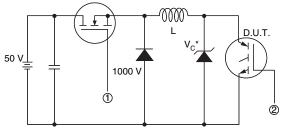


Fig. 15 - Turn-Off SOA



* Driver same type as D.U.T.; V_C = 80 % of V_{CE} (max)

Note: Due to the 50 V power supply, pulse width and inductor will increase to obtain rated $I_{\rm d}$

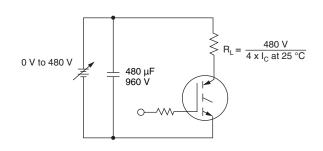


Fig. 16a - Clamped Inductive Load Test Circuit

Fig. 16b - Pulsed Collector Current Test Circuit

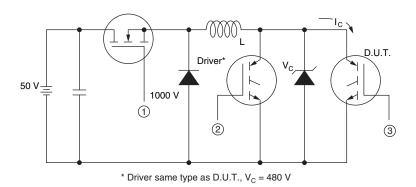


Fig. 17a - Switching Lost Test Circuit

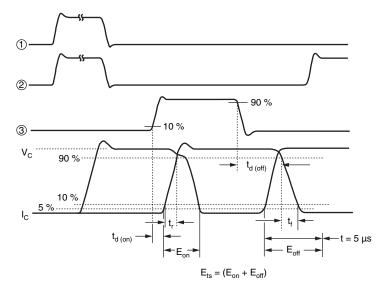
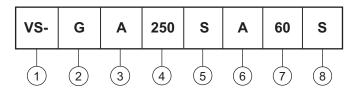


Fig. 17b - Switching Loss Waveforms



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Insulated gate bipolar transistor (IGBT)

3 - Gen 4, IGBT silicon

Current rating (250 = 250 A)

5 - Circuit configuration (S = single switch no diode)

6 - Package indicator (A = SOT-227)

7 - Voltage rating (60 = 600 V)

Speed/type (S = standard speed)

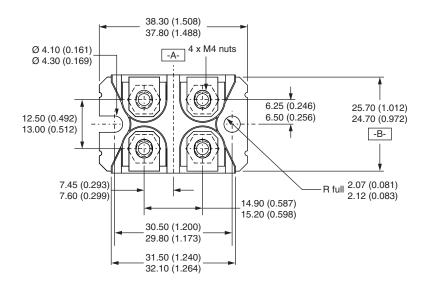
CIRCUIT CONFI	GURATION	
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Single switch, no diode	S	2 (G) O Lead Assignment 1 N-channel

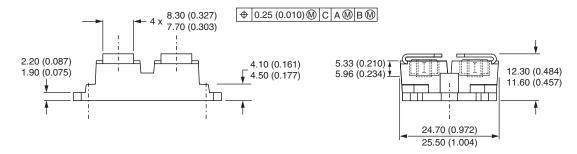
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95423</u>					
Packaging information	www.vishay.com/doc?95425				



SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



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 TIG058E8-TL-H
 VS-CPV364M4KPBF
 NGTB25N120FL2WAG
 NGTG40N120FL2WG
 RJH60F3DPQ-A0#T0

 APT40GR120B2SCD10
 APT15GT120BRG
 APT20GT60BRG
 NGTB75N65FL2WAG
 NGTG15N120FL2WG
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 IMBG120R220M1HXTMA1
 XD15H120CX1
 XD25H120CX0
 XP15PJS120CL1B1
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 STGWA15H120F2
 IKA10N60TXKSA1
 IHW20N120R5XKSA1
 IKW25N120T2FKSA1
 IKP20N60TXKSA1
 IHW20N65R5XKSA1

 IDW40E65D2FKSA1
 APT70GR120JD60
 AOD5B60D
 STGWT60H65FB
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