COMPLIANT



Vishay Semiconductors

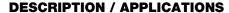
HEXFRED® Ultrafast Soft Recovery Diode, 220 A



| PRIMARY CHARACTERISTICS | | | | |
|---------------------------------|----------------|--|--|--|
| V_{R} | 1200 V | | | |
| V _F (typical) | 2.68 V | | | |
| t _{rr} (typical) | 58 ns | | | |
| $I_{F(AV)}$ per module at T_C | 220 A at 38 °C | | | |
| Package | SOT-227 | | | |

FEATURES

- · Fast recovery time characteristic
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and qualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



The dual diode series configuration (VS-HFA220FA120) is used for output rectification or freewheeling/clamping operation and high voltage application.

The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as HV power supplies, electronic welders, motor control and inverters.

| ABSOLUTE MAXIMUM RATINGS | | | | | |
|--|-----------------------------------|------------------------------------|-------------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MAX. | UNITS | |
| Cathode to anode voltage | V _R | | 1200 | V | |
| Continuous forward current | I _F | T _C = 68 °C | 110 | ^ | |
| Single pulse forward current | I _{FSM} | T _J = 25 °C | 700 | Α | |
| Martin and advantage of the control of | П | T _C = 25 °C | 500 | W | |
| Maximum power dissipation per leg P _D | | T _C = 100 °C | 400 | VV | |
| RMS isolation voltage | V _{ISOL} | Any terminal to case, t = 1 minute | 2500 | V | |
| Operating junction and storage temperature range | T _J , T _{Stg} | | -55 to +150 | °C | |

| ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified) | | | | | | |
|--|-----------------|---|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Cathode to anode breakdown voltage | V_{BR} | I _R = 100 μA | 1200 | - | - | |
| Forward voltage | V _{FM} | I _F = 100 A | - | 2.68 | 3.60 | V |
| | | I _F = 200 A | - | 3.41 | 4.70 | |
| | | I _F = 100 A, T _J = 150 °C | - | 2.62 | 2.89 | |
| | | I _F = 200 A, T _J = 150 °C | - | 3.59 | 3.89 | |
| | | V _R = V _R rated | - | 10 | 75 | μΑ |
| Reverse leakage current | I _{RM} | $T_J = 125 ^{\circ}\text{C}, V_R = V_R \text{rated}$ | - | 2 | - | mΛ |
| | | $T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$ | - | 6 | 15 | mA |



| DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified) | | | | | | | |
|---|------------------|-------------------------|---|----|------|------|-------|
| PARAMETER | SYMBOL | TEST | TEST CONDITIONS | | TYP. | MAX. | UNITS |
| | | $I_F = 1 A$; dI_F/dt | = - 200 A/μs; V _R = 30 V | - | 58 | - | |
| Reverse recovery time | t _{rr} | T _J = 25 °C | | =. | 157 | - | ns |
| | | T _J = 125 °C | | - | 255 | - | |
| Peak recovery current | | T _J = 25 °C | $I_F = 50 \text{ A}$ | - | 15 | - | Α |
| Feak recovery current | I _{RRM} | T _J = 125 °C | $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$ | =. | 22.5 | - | _ ^ |
| Daylaraa waaayaa aharaa | | T _J = 25 °C | | =. | 1150 | - | nC |
| Reverse recovery charge | Q _{rr} | T _J = 125 °C | | - | 2850 | - | i iic |
| Junction capacitance | C _T | V _R = 1200 V | | - | 53 | - | pF |

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | |
|---|---------------------|-----------------------|------|------|------------|-------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Junction to case, single leg conducting | В | | - | - | 0.25 | |
| Junction to case, both legs conducting | - R _{thJC} | | - | - | 0.125 | °C/W |
| Case to heatsink | R _{thCS} | Flat, greased surface | - | 0.10 | - | |
| Weight | | | - | 30 | - | g |
| Manadiantanana | | Torque to terminal | - | - | 1.1 (9.7) | Nm (lbf.in) |
| Mounting torque | | Torque to heatsink | - | - | 1.8 (15.9) | Nm (lbf.in) |
| Case style | | | | | SOT-227 | • |

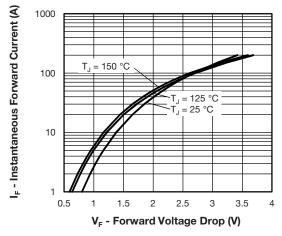


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

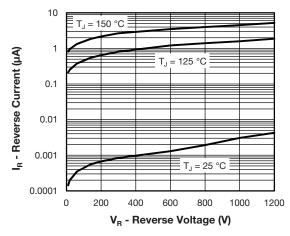


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

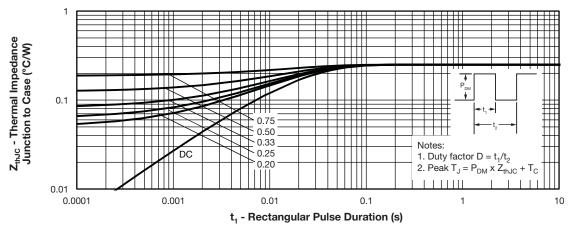


Fig. 3 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

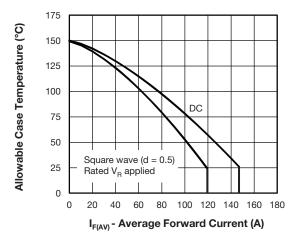


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

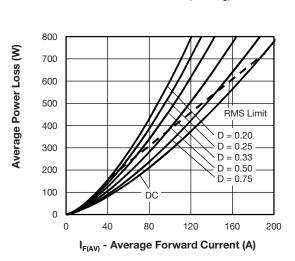


Fig. 5 - Forward Power Losses Characteristics (Per Leg)

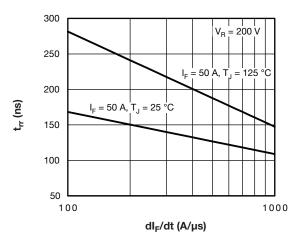


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

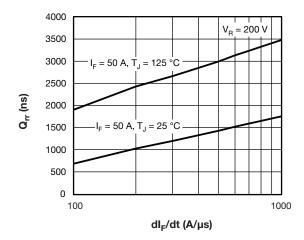


Fig. 7 - Typical Stored Charge vs. dl_F/dt



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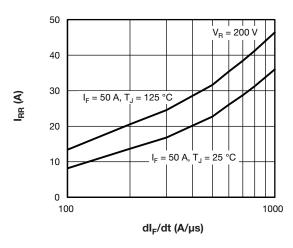


Fig. 8 - Typical Peak Recovery Current vs. dl_F/dt

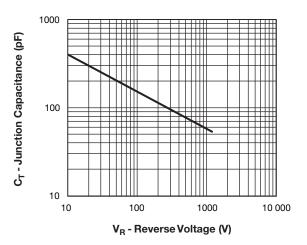


Fig. 9 - Typical Junction Capacitance vs. Reverse Voltage

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

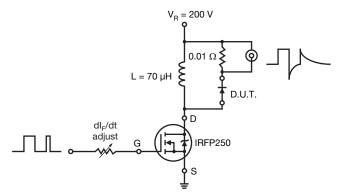
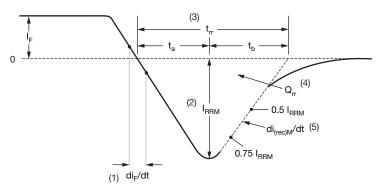


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_{F}$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

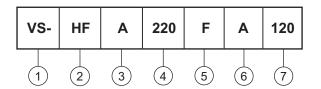
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 11 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



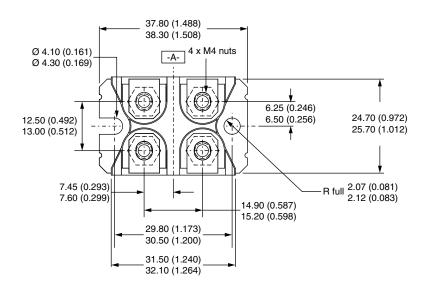
- 1 Vishay Semiconductors product
- 2 HEXFRED® family
- Process designator (A = electron irradiated)
- 4 Average current (220 = 220 A)
- 5 Circuit configuration (two separate diodes, parallel pin-out)
- 6 Package indicator (SOT-227 standard insulated base)
- 7 Voltage rating (120 = 1200 V)

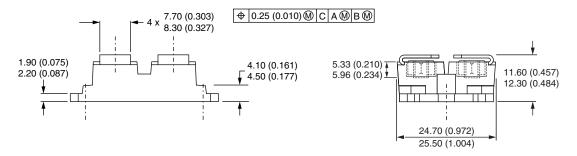
| CIRCUIT CONFIGURATION | | | | | |
|---------------------------------------|-------------------------------|--------------------|--|--|--|
| CIRCUIT | CIRCUIT CONFIGURATION CODE | CIRCUIT DRAWING | | | |
| Two separate diodes, parallel pin-out | F | Lead Assignment 4 | | | |

| LINKS TO RELATED DOCUMENTS | | | | |
|----------------------------|--------------------------|--|--|--|
| Dimensions | www.vishay.com/doc?95423 | | | |
| Packaging information | www.vishay.com/doc?95425 | | | |

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



Legal Disclaimer Notice

Vishay

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