Vishay Semiconductors

Schottky Rectifier, 2 x 20 A

Base common cathode cathode Anode Anode Cathode Anode

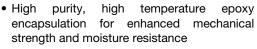
PRODUCT SUMMARY				
Package	TO-263AB (D ² PAK), TO-262AA			
I _{F(AV)}	2 x 20 A			
V _R	45 V			
V _F at I _F	0.58 V			
I _{RM} max.	95 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Common cathode			
E _{AS}	20 mJ			

TO-262

D²PAK

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL CHARACTERISTICS VALUES UNITS					
I _{F(AV)}	Rectangular waveform (per device)	40	Δ.		
I _{FRM}	T _C = 117 °C (per leg)	40			
V _{RRM}		45	V		
I _{FSM}	t _p = 5 μs sine	900	A		
V _F	20 A _{pk} , T _J = 125 °C	0.58	V		
TJ	Range	-65 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-MBRB4045CTHM3 VS-MBR4045CT-1HM3	UNITS		
Maximum DC reverse voltage	V_{R}	45	V		
Maximum working peak reverse voltage	V_{RWM}	45	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS	
Maximum average per leg	1	T 440.00		20		
forward current per device	$T_{C} = 118 ^{\circ}\text{C}$, rated V_{R}		40	ı		
Peak repetitive forward current per leg	I _{FRM}	Rated V _R , square wave, 20 kHz, T _C = 117 °C		40	Α	
Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse	Following any rated	900		
peak surge current per leg	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	210		
Non-repetitive avalanche energy per leg E _{AS}		$T_J = 25 ^{\circ}\text{C}$, $I_{AS} = 3 \text{A}$, $L = 4.4 \text{mH}$		20	mJ	
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	А	

Revision: 06-Mar-14 Document Number: 94721



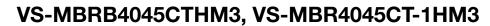
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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TES	TEST CONDITIONS			
		20 A	T _{.1} = 25 °C	0.60		
Maximum forward voltage drop	V (1)	40 A	1j = 25 C	0.80	V	
	V _{FM} ⁽¹⁾	20 A	T 105 °C	0.58		
		40 A	T _J = 125 °C	0.80		
		T _J = 25 °C		1		
Maximum instantaneous reverse current	I _{RM} (1)	T _J = 100 °C	Rated DC voltage	50	mA	
reverse current		T _J = 125 °C		95		
Maximum junction capacitance	Ст	V _R = 5 V _{DC} (test signs	900	pF		
Typical series inductance	L _S	Measured from top	8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R	Rated V _B			

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANIC PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range		1201 001121110110	-65 to 150	
Maximum storage temperature ra	nge T _{Stg}		-65 to 175	°C
Maximum thermal resistance, junction to case per leg		DC operation	1.5	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased (Only for TO-220)	0.50	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation (For D ² PAK and TO-262)	50	
Approximate weight			2	g
Approximate weight			0.07	OZ.
Matina taurus	num	Name to be signed at the second	6 (5)	kgf · cm
Mounting torque maxir	num	Non-lubricated threads	12 (10)	(lbf \cdot in)
Madiandaria		Case style D ² PAK	MBRB4	045CTH
Marking device		Case style TO-262	MBR404	I5CT-1H





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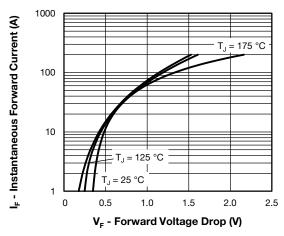


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

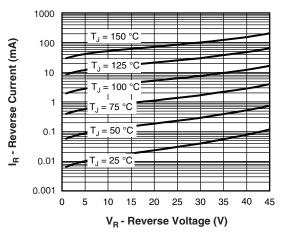


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

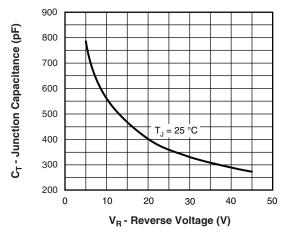


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

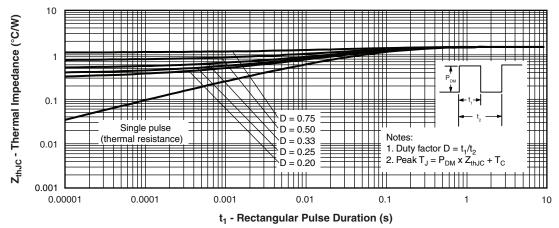


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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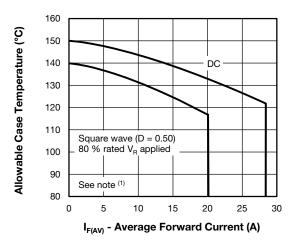


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

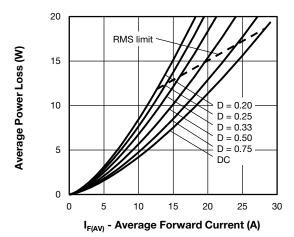


Fig. 6 - Forward Power Loss Characteristics

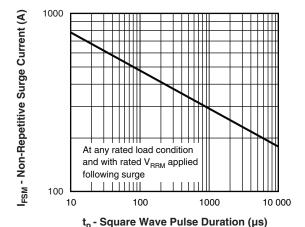


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

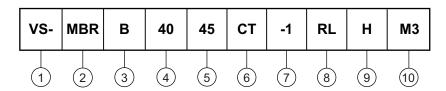
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Essential part number

3 - • B = D^2PAK 7 None

• None = TO-262 **7** = -1

Current rating (40 = 40 A)

Voltage rating (45 = 45 V)

- CT = Essential part number

- • None = D^2PAK 3 = B

• -1 = TO-262 **3** None

8 - • None = Tube

• L = Tape and reel (left oriented - for D²PAK only)

• R = Tape and reel (right oriented - for D²PAK only)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = Halogen-free, RoHS-compliant and termination lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-MBRB4045CTHM3	50	1000	Antistatic plastic tube		
VS-MBR4045CT-1HM3	50	1000	Antistatic plastic tube		
VS-MBRB4045CTLHM3	800	800	13" diameter reel		
VS-MBRB4045CTRHM3	800	800	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046		
Differsions	TO-262AA	www.vishay.com/doc?95419		
Part marking information	TO-263AB (D ² PAK)	www.vishay.com/doc?95444		
Part marking information	TO-262AA	www.vishay.com/doc?95443		
Packaging information	TO-263AB (D ² PAK)	www.vishay.com/doc?95032		



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

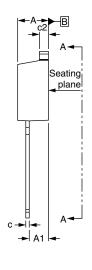


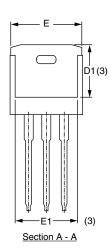
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TO-262

DIMENSIONS in millimeters and inches

Modified JEDEC outline TO-262 (Datum A) (2) (3) (3) L1 D D D C C C C A (2) A (2) A (3) L2 B B B B C C C A (2)



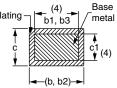


⊕ 0.010**⋒**|A**⋒**|B

Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode
3. - Anode



Section B - B and C - C Scale: None

CVMPOL	MILLIN	METERS	INCH	IES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

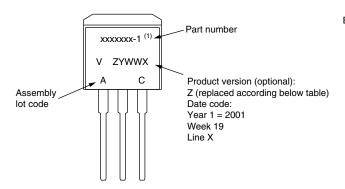
- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



Part Marking Information

Vishay Semiconductors

TO-262



Example: This is a xxxxxxx-1 ⁽¹⁾ with assembly lot code AC, assembled on WW 19, 2001

in the assembly line "X"

Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

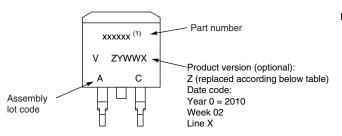
ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION			
A Termination lead (Pb)-free				
B Totally lead (Pb)-free				
E	RoHS-compliant and termination lead (Pb)-free			
F	RoHS-compliant and totally lead (Pb)-free			
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free			
N	N Halogen-free, RoHS-compliant and totally lead (Pb)-free			
G	Green			



Part Marking Information

Vishay Semiconductors

D²PAK



Example: This is a xxxxxx ⁽¹⁾ with assembly lot code AC, assembled on WW 02, 2010

Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z) PRODUCT DEFINITION			
A Termination lead (Pb)-free			
B Totally lead (Pb)-free			
E	RoHS-compliant and termination lead (Pb)-free		
F RoHS-compliant and totally lead (Pb)-free			
M	Halogen-free, RoHS-compliant, and termination lead (Pb)-free		
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free		
G	Green		



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