End of Life July-2021 - Alternative Device: VSML3710



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### **Vishay Semiconductors**

VSMS3700

### Infrared Emitting Diode, 950 nm, GaAs



#### DESCRIPTION

VSMS3700 is an infrared, 950 nm emitting diode in GaAs technology, molded in a PLCC-2 package for surface mounting (SMD).

#### **FEATURES**

- Package type: surface-mount
- Package form: PLCC-2
- Dimensions (L x W x H in mm): 3.5 x 2.8 x 1.75
- Peak wavelength:  $\lambda_p = 950 \text{ nm}$
- High reliability
- Angle of half intensity:  $\varphi = \pm 60^{\circ}$
- Low forward voltage
- · Suitable for high pulse current operation
- Good spectral matching with Si photodetectors
- Package matched with IR emitter series VEMT3700
- Floor life: 168 h, MSL 3, acc. J-STD-020
- · Lead (Pb)-free reflow soldering
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- Infrared source in tactile keyboards
- IR diode in low space applications
- PCB mounted infrared sensors
- Emitter in miniature photo-interrupters

PRODUCT SUMMARY				
COMPONENT	l <sub>e</sub> (mW/sr)	φ <b>(°)</b>	λ <sub>P</sub> (nm)	t <sub>r</sub> (ns)
VSMS3700	4.5	± 60	950	800

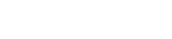
Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	GING REMARKS PACK		
VSMS3700-GS08	Tape and reel	MOQ: 7500 pcs, 1500 pcs/reel	PLCC-2	
VSMS3700-GS18	Tape and reel	MOQ: 8000 pcs, 8000 pcs/reel	PLCC-2	

#### Note

· MOQ: minimum order quantity







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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Reverse voltage		V <sub>R</sub>	5	V		
Forward current		١ <sub>F</sub>	100	mA		
Peak forward current	$t_p/T = 0.5, t_p = 100 \ \mu s$	I <sub>FM</sub>	200	mA		
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	1.5	A		
Power dissipation		Pv	170	mW		
Junction temperature		Тj	100	°C		
Operating temperature range		T <sub>amb</sub>	-40 to +85	°C		
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C		
Soldering temperature	According to Fig. 11, J-STD-020	T <sub>sd</sub>	260	°C		
Thermal resistance junction-to-ambient	J-STD-051, soldered on PCB	R <sub>thJA</sub>	250	K/W		

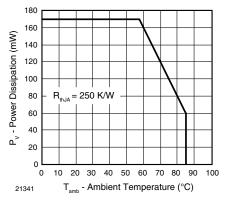


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

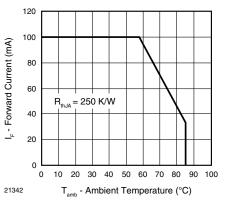


Fig. 2 - Forward Current Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	VF	-	1.3	1.7	V
Forward voltage	$I_F = 1 \text{ A}, t_p = 100 \ \mu \text{s}$	V <sub>F</sub>	-	1.8	-	V
Temperature coefficient of $V_F$	I <sub>F</sub> = 100 mA	TK <sub>VF</sub>	-	-1.3	-	mV/K
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	100	μA
Junction capacitance	$V_{R} = 0 V, f = 1 MHz, E = 0$	Cj	-	30	-	pF
Redient intensity	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	l <sub>e</sub>	1.6	4.5	8	mW/sr
Radiant intensity	$I_F = 1.5 \text{ A}, t_p = 100 \ \mu s$	l <sub>e</sub>	-	35	-	mW/sr
Radiant power	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	\$e	-	15	-	mW
Temperature coefficient of $\phi_{e}$	I <sub>F</sub> = 100 mA	TKφ <sub>e</sub>	-	-0.8	-	%/K
Angle of half intensity		φ	-	± 60	-	0
Peak wavelength	I <sub>F</sub> = 100 mA	λρ	-	950	-	nm
Spectral bandwidth	I <sub>F</sub> = 100 mA	Δλ	-	50	-	nm
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 100 mA	ΤΚλρ	-	0.2	-	nm/K
Rise time	I <sub>F</sub> = 20 mA	t <sub>r</sub>	-	800	-	ns
	I <sub>F</sub> = 1 A	t <sub>r</sub>	-	400	-	ns
	I <sub>F</sub> = 20 mA	t <sub>f</sub>	-	800	-	ns
Fall time	I <sub>F</sub> = 1 A	t <sub>f</sub>	-	400	-	ns
Virtual source diameter	EN 60825-1	d	-	0.5	-	mm

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**VSMS**3700

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### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

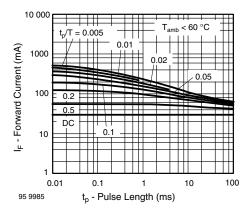


Fig. 3 - Pulse Forward Current vs. Pulse Duration

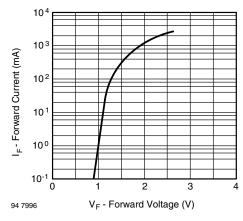


Fig. 4 - Forward Current vs. Forward Voltage

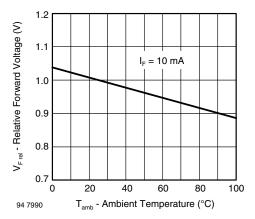


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

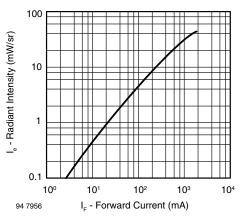


Fig. 6 - Radiant Intensity vs. Forward Current

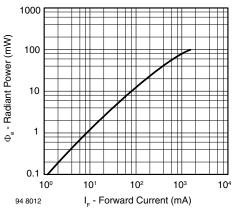


Fig. 7 - Radiant Power vs. Forward Current

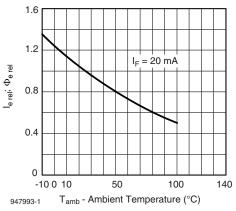


Fig. 8 - Relative Radiant Intensity/Power vs. Ambient Temperature

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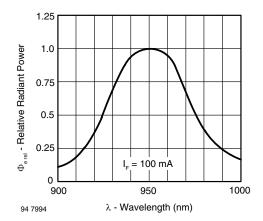


Fig. 9 - Relative Radiant Power vs. Wavelength

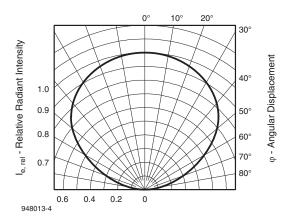


Fig. 10 - Relative Radiant Intensity vs. Angular Displacement

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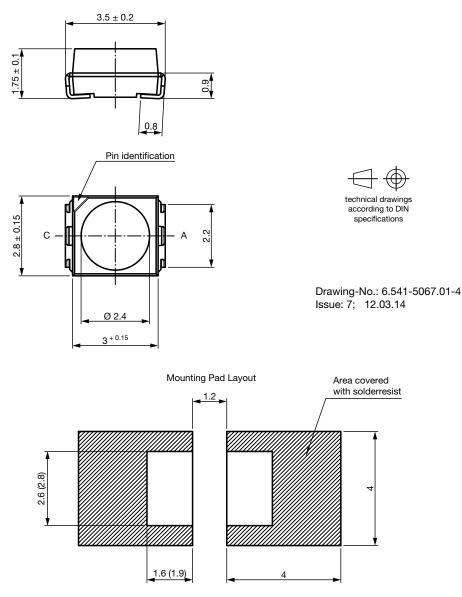


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**VSMS3700** 

#### **PACKAGE DIMENSIONS** in millimeters



Dimensions: reflow and vapor phase (wave soldering)



### **VSMS**3700

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#### SOLDER PROFILE

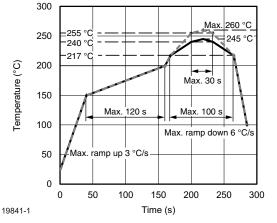


Fig. 11 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

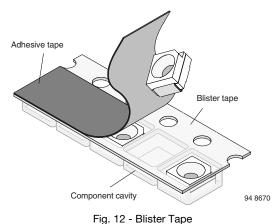
Conditions: T<sub>amb</sub> < 30 °C, RH < 60 % Moisture sensitivity level 3, acc. to J-STD-020

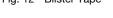
### DRYING

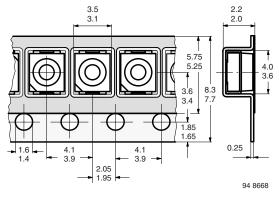
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

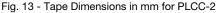
### TAPE AND REEL

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.



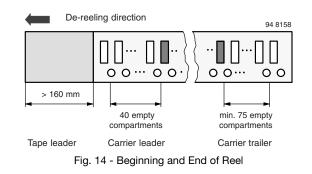






### **MISSING DEVICES**

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.



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### **VSMS**3700

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The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartments and sealed with cover tape.



The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.

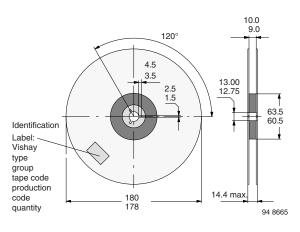


Fig. 15 - Dimensions of Reel-GS08

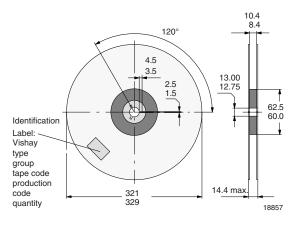


Fig. 16 - Dimensions of Reel-GS18



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