# VSMY1940X01



**Vishay Semiconductors** 

### High Speed Infrared Emitting Diodes, 940 nm, Surface Emitter Technology



#### DESCRIPTION

As part of the <u>SurfLight<sup>TM</sup></u> portfolio, the VSMY1940X01 is an infrared, 940 nm emitting diode based on GaAlAs surface emitter chip technology with high radiant intensity, high optical power and high speed, molded in clear, untinted 0805 plastic package for surface mounting (SMD).

#### FEATURES

- Package type: surface mount
- Package form: 0805
- Dimensions (L x W x H in mm): 2 x 1.25 x 0.85
- AEC-Q101 qualified
- Peak wavelength:  $\lambda_p = 940 \text{ nm}$
- High reliability
- High radiant power
- High radiant intensity
- High speed
- Angle of half sensitivity:  $\phi = \pm 60^{\circ}$
- · Suitable for high pulse current operation
- 0805 standard surface-mountable package
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### APPLICATIONS

- IrDA compatible data transmission
- Miniature light barrier
- Photointerrupters
- Optical switch
- Emitter source for proximity sensors
- IR touch panels
- IR flash
- IR illumination
- 3D TV

PRODUCT SUMMARY				
COMPONENT	l <sub>e</sub> (mW/sr)	φ (deg)	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)
VSMY1940X01	10	± 60	940	10

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMY1940X01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805		

Note

• MOQ: minimum order quantity

1 For technical questions, contact: <u>emittertechsupport@vishay.com</u>







### VSMY1940X01



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ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	5	V	
Forward current		I <sub>F</sub>	100	mA	
Peak forward current	$t_p/T = 0.5, t_p = 100 \ \mu s$	I <sub>FM</sub>	200	mA	
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	1	A	
Power dissipation		Pv	180	mW	
Junction temperature		Тj	100	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +85	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C	
Soldering temperature	According to Fig. 10, J-STD-020	T <sub>sd</sub>	260	°C	
Thermal resistance junction / ambient	JESD 51	R <sub>thJA</sub>	250	K/W	

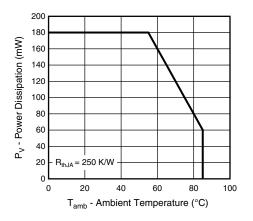


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

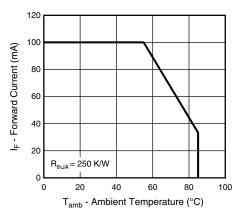


Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	V <sub>F</sub>	-	1.45	1.8	V
	I <sub>F</sub> = 1 A, t <sub>p</sub> = 100 μs	V <sub>F</sub>	-	2.2	-	V
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 100 mA	TK <sub>VF</sub>	-	-2	-	mV/K
Reverse current		I <sub>R</sub>	Not designed for reverse operation µA			μA
Junction capacitance	$V_{R} = 0 \text{ V, } f = 1 \text{ MHz,}$ $E = 0 \text{ mW/cm}^{2}$	CJ	-	125	-	pF
Radiant intensity	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	Ι <sub>e</sub>	5	10	15	mW/sr
	I <sub>F</sub> = 1 A, t <sub>p</sub> = 100 μs	le	-	75	-	mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	фе	-	50	-	mW
Temperature coefficient of radiant power	I <sub>F</sub> = 100 mA	TKφ <sub>e</sub>	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 60	-	deg
Peak wavelength	I <sub>F</sub> = 100 mA	λρ	920	940	960	nm
Spectral bandwidth	I <sub>F</sub> = 100 mA	Δλ	-	40	-	nm
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 30 mA	TK <sub>λp</sub>	-	0.25	-	nm/K
Rise time	$I_F = 100 \text{ mA}, 20 \% \text{ to } 80 \%$	t <sub>r</sub>	-	10	-	ns
Fall time	I <sub>F</sub> = 100 mA, 20 % to 80 %	t <sub>f</sub>	-	10	-	ns

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#### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

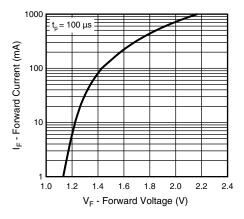


Fig. 3 - Forward Current vs. Forward Voltage

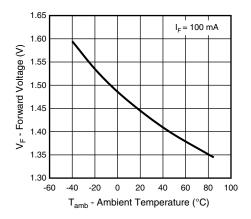


Fig. 4 - Forward Voltage vs. Ambient Temperature

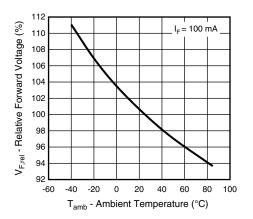


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

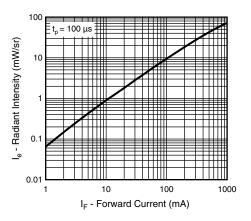


Fig. 6 - Radiant Intensity vs. Forward Current

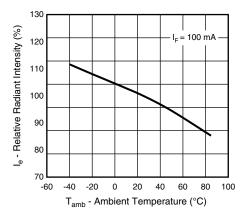


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature

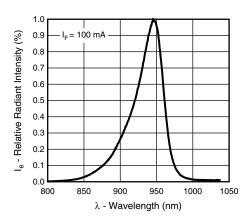


Fig. 8 - Relative Radiant Intensity vs. Wavelength

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#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 3

Floor life: 168 h

Conditions:  $T_{amb} < 30$  °C, RH < 60 %

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



0

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0°

10°

20°

<u>max. 26</u>0 °C

-245 °C

30°

40°

50°

60'

70'

80°

φ - Angular Displacement

#### **REFLOW SOLDER PROFILE**

255 °C

240 °C 217 °C

0.6

0.4 0.2

l<sub>e, rel</sub> - Relative Radiant Intensity

1.0

0.9

0.8

0.7

948013-1

300

250

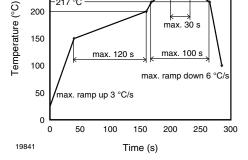


Fig. 10 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

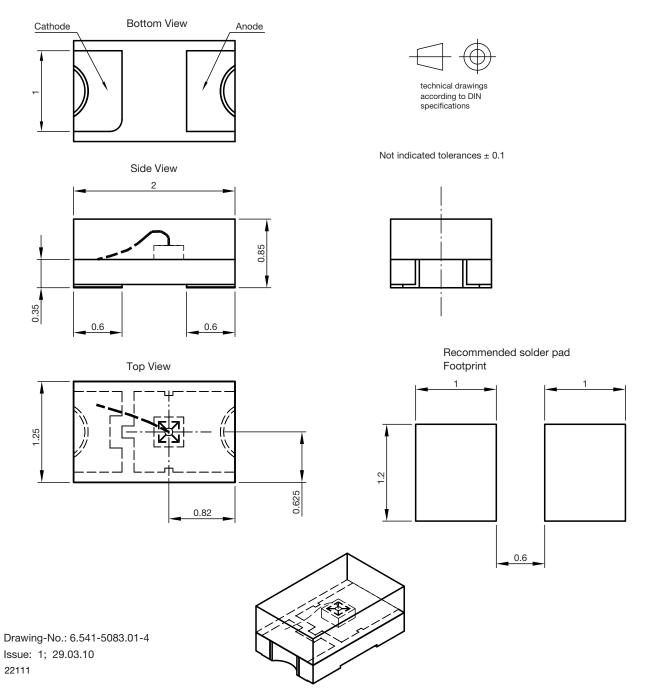
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#### **PACKAGE DIMENSIONS** in millimeters



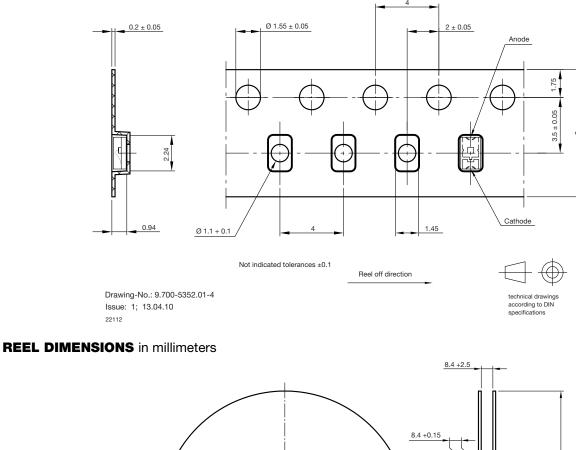


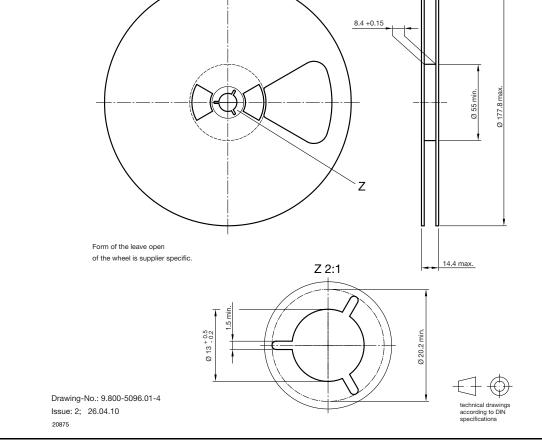




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