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Vishay Semiconductors

High Power Infrared Emitting Diode, 850 nm, Surface Emitter Technology



DESCRIPTION

As part of the <u>SurfLightTM</u> portfolio, the VSMY7850X01 is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant power and high speed, molded in low thermal resistance Little Star package. A 42 mil chip provides outstanding low forward voltage and allows DC operation of the device up to 1 A.

FEATURES

Package type: surface-mount

• Package form: Little Star®



Peak wavelength: λ_p = 850 nm

High reliability

• High radiant power

High radiant intensity

• Angle of half intensity: $\varphi = \pm 60^{\circ}$

Low forward voltage

 Designed for high drive currents: up to 1 A_{DC} and up to 5 A pulses

Low thermal resistance: R_{thJP} = 10 K/W

• Floor life: 1 year, MSL 2, according to J-STD-020

· Lead (Pb)-free reflow soldering

 Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

APPLICATIONS

- Infrared illumination for CMOS cameras (CCTV)
- Machine vision IR data transmission
- 3D TV

PRODUCT SUMMARY					
COMPONENT	I _e (mW/sr)	φ (°)	$\lambda_{\mathbf{p}}$ (nm)	t _r (ns)	
VSMY7850X01	200	± 60	850	15	

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY7850X01-GS08	Tape and reel	MOQ: 2000 pcs, 2000 pcs/reel	Little Star	

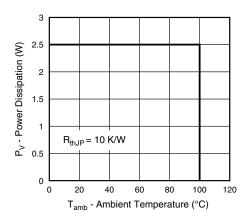
Note

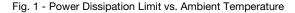
• MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V_{R}	5	V	
Forward current		I _F	1	Α	
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I _{FM}	2	Α	
Surge forward current	t _p = 100 μs	I _{FSM}	5	Α	
Power dissipation		P _V	2.5	W	
Junction temperature		T _j	125	°C	
Operating temperature range		T _{amb}	-40 to +100	°C	
Storage temperature range		T _{stg}	-40 to +100	°C	
Soldering temperature	According to Fig. 7, J-STD-20	T _{sd}	260	°C	
Thermal resistance junction-to-pin	According to J-STD-051, soldered on PCB	R _{thJP}	10	K/W	



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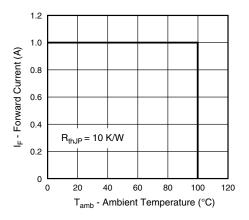


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	V _F	-	2.0	2.5	V
Temperature coefficient of V _F	I _F = 1 A	TK _{VF}	-	-0.2	-	mV/K
Reverse current	V _R = 5 V	I _R	not designed for reverse operation			μΑ
Radiant intensity	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	l _e	130	200	390	mW/sr
Radiant power	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	φ _e	-	800	-	mW
Temperature coefficient of φ _e	I _F = 1 A	TKφ _e	-	-0.5	-	%/K
Angle of half intensity		φ	-	± 60	-	0
Peak wavelength	I _F = 1 A	λ_{p}	-	850	-	nm
Spectral bandwidth	I _F = 1 A	Δλ	-	30	-	nm
Temperature coefficient of λ_p	I _F = 1 A	TKλ _p	-	0.2	-	nm/K
Rise time	I _F = 1 A	t _r	-	15	-	ns
Fall time	I _F = 1 A	t _f	-	18	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

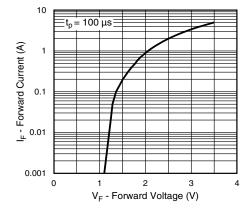


Fig. 3 - Forward Current vs. Forward Voltage

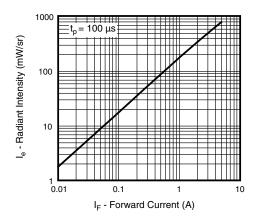


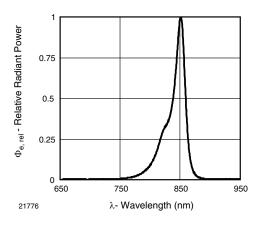
Fig. 4 - Radiant Intensity vs. Forward Current





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20°



30° le, rel. - Relative Radiant Intensity φ - Angular Displacement 40° 1.0 0.9 50° 8.0 60° 70° 0.7 80° 0.4 94 8013-3

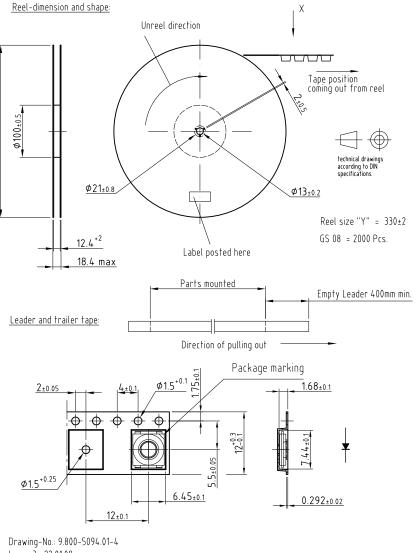
0°

10°

Fig. 5 - Relative Radiant Power vs. Wavelength

Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

TAPING DIMENSIONS in millimeters



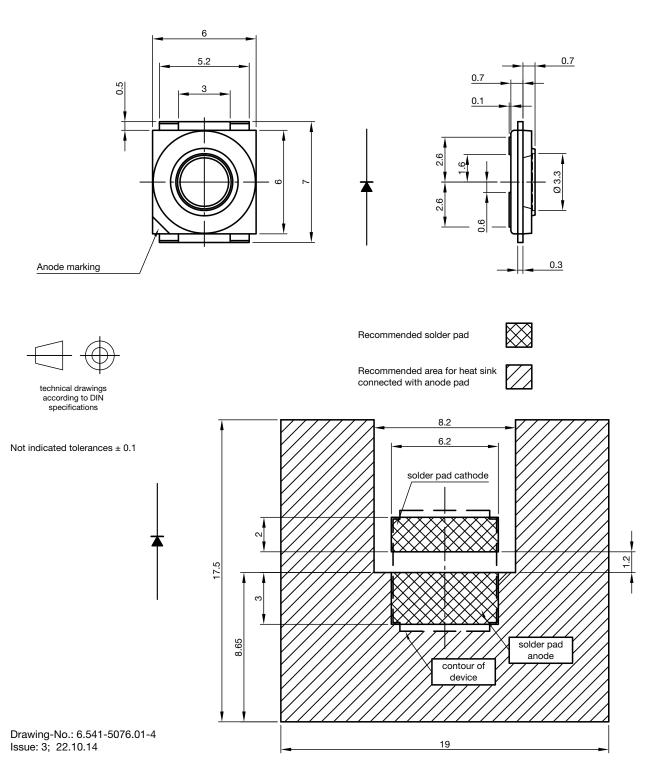
Issue: 3; 22.01.08

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PACKAGE DIMENSIONS in millimeters







SOLDER PROFILE

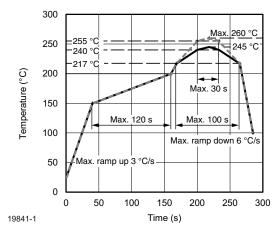


Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for Preconditioning According to JEDEC $^\$$, Level 2

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DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 1 year

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 2, according to J-STD-020B

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 %.



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OS5RKAZ5D1P OSB56LZE31D OSG58AZ5D1P OSI3CA5111A OSI3NAS1C1A OSI5LA56A1A OSI5XNE3E1E OSIXCA5121A

OSIXCAS1C1A OSM54LZ5D1P OSM5D3Z2C1P OSMR43Z2C1P OSO5PAZ161D OSOR7161D OSPW7161D OSPW71B1P

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