

Standard Recovery Diodes, (Hockey PUK Version), 650 A



A-PUK (DO-200AA)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	650 A			
Package	A-PUK (DO-200AA)			
Circuit configuration	Single			

FEATURES

- Wide current range
- High voltage ratings up to 3200 V
- High surge current capabilities
- Diffused junction
- Hockey PUK version
- Case style A-PUK (DO-200AA)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Converters
- Power supplies
- Machine tool controls
- · High power drives
- · Medium traction applications

MAJOR RATINGS AND CHARACTERISTICS					
DADAMETED	TEST CONDITIONS	SD30	UNITS		
PARAMETER	TEST CONDITIONS	04 to 20 25 to 32		UNITS	
1		650	540	Α	
I _{F(AV)}	T _{hs}	55	55	°C	
I _{F(RMS)}		1150	995	Α	
	T _{hs}	25	25	°C	
I _{FSM}	50 Hz	6050	6050	Α	
	60 Hz	6335	6335	A	
l ² t	50 Hz	183	183	kA ² s	
	60 Hz	167	167	KA-S	
V _{RRM}	Range	400 to 2000	2500 to 3200	V	
T_J		-40 to +180	-40 to +150	°C	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = T _J MAXIMUM mA		
	04	400	500			
VS-SD300CC	08	800	900			
	12	1200	1300			
	16	1600	1700	15		
	20	2000	2100	15		
	25	2500	2600			
	28	2800	2900			
	32	3200	3300			



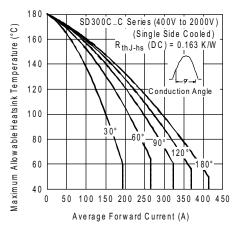
FORWARD CONDUCTION							
PARAMETER	SYMBOL	CYMAROL TEST COMPLICATE		SD30	LINUTO		
PARAMETER	STINIBUL	TEST CONDITIONS			04 to 20	25 to 32	UNITS
Maximum average forward current	I _{F(AV)}		ction, half sine		650 (380)	540 (250)	Α
at heatsink temperature	. ()	Double side	e (single side) c	oolea	55 (85)	55 (85)	°C
Maximum RMS forward current	I _{F(RMS)}	25 °C heats	ink temperatur	e double side cooled	1150	995	
		t = 10 ms	No voltage		6050	6050	А
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied		6335	6335	
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}		5090	5090	
		t = 8.3 ms	reapplied	Sinusoidal half wave,	5330	5330	
	l ² t	t = 10 ms	No voltage	initial $T_J = T_J$ maximum	183	183	kA ² s
Maximum I ² t for fusing		t = 8.3 ms	reapplied		167	167	
		t = 10 ms	100 % V _{RRM} reapplied		129	129	
		t = 8.3 ms			118	118	
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied		1830	1830	kA²√s	
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum		0.95	0.95	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			1.00	1.00	V
Low level values of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum		0.75	0.75	mW	
High level values of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			0.72	0.72	IIIVV
Maximum forward voltage drop	V _{FM}	$I_{pk} = 1500 \text{ A}, T_J = T_J \text{ maximum};$ $t_p = 10 \text{ ms sinusoidal wave}$		2.08	2.08	V	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	SD300CC		UNITS	
		TEST CONDITIONS	04 to 20	25 to 32	UNITS	
Maximum operating temperature range	T_J		-40 to 180	-40 to 150	°C	
Maximum storage temperature range	T _{Stg}		-55 to 200			
Maximum thermal resistance,	В	DC operation single side cooled	0.163		K/W	
junction to heatsink	R _{thJ-hs}	DC operation double side cooled	0.073		IV VV	
Mounting force, ± 10 %			4900	(500)	N (kg)	
Approximate weight			7	0	g	
Case style		See dimensions - link at the end of datasheet	A-Pl	JK (DO-200 <i>F</i>	AA)	

△R _{thJ-hs} CONDUCTION							
SINUSOIDAL CONDU		ONDUCTION	NDUCTION RECTANGULAR CONDUCTION			LIMITO	
CONDUCTION ANGLE	SINGLE SIDE	DOUBLE SIDE	SINGLE SIDE	DOUBLE SIDE	TEST CONDITIONS	UNITS	
180°	0.017	0.017	0.011	0.012			
120°	0.020	0.020	0.020	0.020	1		
90°	0.025	0.025	0.027	0.027	$T_J = T_J$ maximum	K/W	
60°	0.036	0.036	0.038	0.038			
30°	0.064	0.062	0.065	0.062			

Note

• The table above shows the increment of thermal resistance R_{thJ-hs} when devices operate at different conduction angles than DC



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Fig. 1 - Current Ratings Characteristics

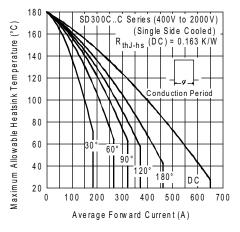


Fig. 2 - Current Ratings Characteristics

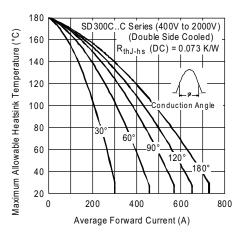


Fig. 3 - Current Ratings Characteristics

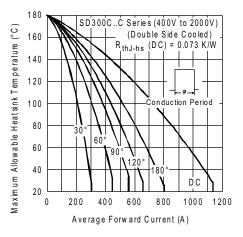


Fig. 4 - Current Ratings Characteristics

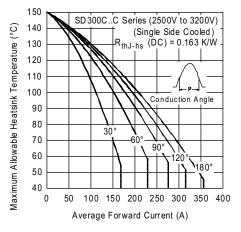


Fig. 5 - Current Ratings Characteristics

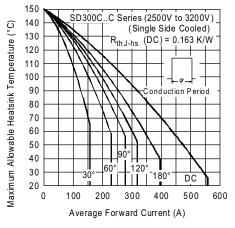


Fig. 6 - Current Ratings Characteristics

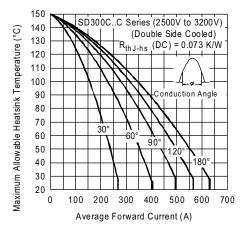


Fig. 7 - Current Ratings Characteristics

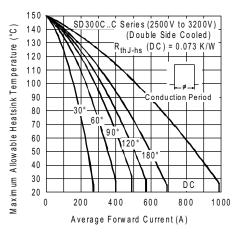


Fig. 8 - Current Ratings Characteristics

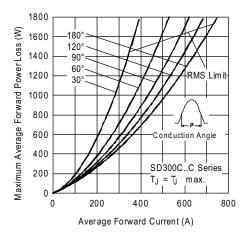


Fig. 9 - Forward Power Loss Characteristics

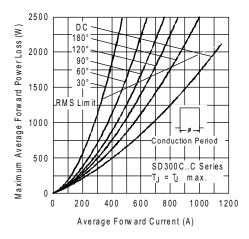


Fig. 10 - Forward Power Loss Characteristics

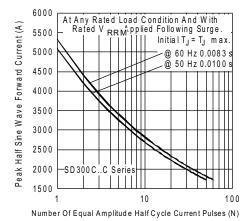


Fig. 11 - Maximum Non-Repetitive Surge Current Single and Double Side Cooled

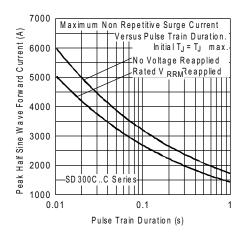


Fig. 12 - Maximum Non-Repetitive Surge Current Single and Double Side Cooled

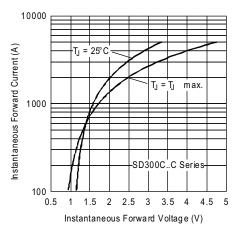


Fig. 13 - Forward Voltage Drop Characteristics

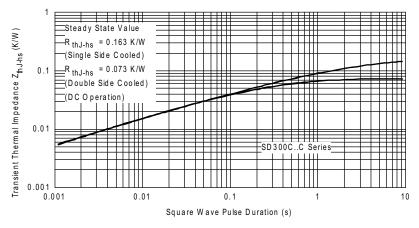


Fig. 14 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

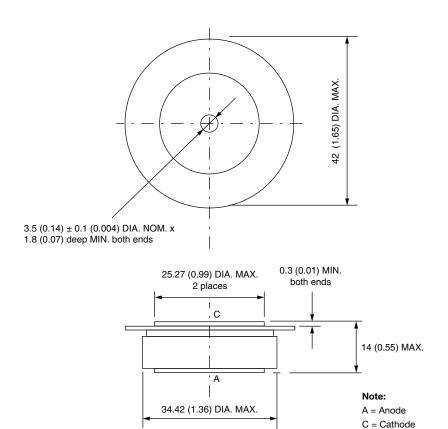
Device code VS-SD 30 0 C 32 C 2 3 4 (5) (6)(7) Vishay Semiconductors product Diode Essential part number 0 = standard recovery C = ceramic PUK Voltage code x 100 = V_{RRM} (see Voltage Ratings table) C = PUK case A-PUK (DO-200AA)

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95248			



DO-200AA

DIMENSIONS in millimeters (inches)



Quote between upper and lower pole pieces has to be considered after application of mounting force (see Thermal and Mechanical Specifications)



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