VS-T20HF220

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Power Rectifier Diodes (T-Modules), 2200 V, 20 A



D-55 (T-module)

PRIMARY CHARACTERISTICS					
I _{F(AV)}	20 A				
Туре	Modules - diode, high voltage				
V _{RRM}	2200 V				
Package	D-55 (T-module)				
Circuit configuration	Single diode				

FEATURES

- Electrically isolated base plate
- 2200 V_{RRM}
- Industrial standard packaging
- UL approved file E78996
- · Simplified mechanical designs, rapid assembly
- Large creepage distances
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

These series of D-55 (T-modules) use standard recovery power rectifier diodes. The semiconductors are electrically isolated from the metal base, allowing common heatsink and compact assembly to be built.

Applications include power supplies, battery charges, welders, motor controls, and solar panel application.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
1		20	A		
I _{F(AV)}	T _C	85	°C		
I _{F(RMS)}		31			
	50 Hz	450	А		
IFSM	60 Hz	470			
l ² t	50 Hz	1015	A ² s		
1-1	60 Hz	920	A-5		
l²√t		10 125	A²√s		
V _{RRM}		2200	V		
TJ		-40 to +150	°C		

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 150 °C mA		
VS-T20HF220	22	2200	2250	18		

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FORWARD CONDUCTION				Г		
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average forward current at case	I _{F(AV)}	180° condu	ction, half sine	wave	20	А
temperature	·F(AV)			navo	85	°C
Maximum RMS forward current	I _{F(RMS)}				31	А
		t = 10 ms	No voltage		450	
Maximum peak, one-cycle forward,	1=0.4	t = 8.3 ms	reapplied		470	А
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}		380	~
		t = 8.3 ms	reapplied	Sinusoidal half wave, initial	400]
Maximum I ² t for fusing		t = 10 ms	No voltage	$T_J = T_J$ maximum	1015	A ² s
	l ² t	t = 8.3 ms	reapplied		920	
		t = 10 ms	100 % V _{RRM}		715	
		t = 8.3 ms	reapplied		650	
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied			10 125	A²√s
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J maximum			0.77	v
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi x I_{F(AV)}), T_J$ maximum			0.89	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)} < I < \pi$ x $I_{F(AV)}$), T _J maximum			8.5	mΩ
High level value of forward slope resistance	r _{f2}	$(I > \pi x I_{F(AV)}), T_J$ maximum			6.7	
Maximum forward voltage drop	V _{FM}	$I_{FM} = 60 \text{ A}, T_J = 25 \text{ °C}, t_p = 400 \mu\text{s} \text{ square pulse}$ Average power = $V_{F(TO)} \times I_{F(AV)} + r_f \times (I_{F(RMS)})^2$			1.50	V

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak reverse leakage current	I _{RRM}	T _J = 150 °C	18	mA
RMS isolation voltage	V _{ISOL}	50 Hz, circuit to base, all terminals shorted T_J = 25 °C, t = 1 s	3500	V

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TES	F CONDITIONS	VALUES	UNITS	
Maximum junction operating and temperature range	d storage	T _J , T _{Stg}			-40 to +150	°C	
Maximum thermal resistance, jur per junction	nction to case	R _{thJC}	DC operation		2.53	K/W	
Maximum thermal resistance, ca	se to heatsink	R _{thCS}	Mounting surface smooth, flat and greased		0.2	1	
to heatsink			Non-lubricated	M3.5 mounting screws ⁽¹⁾	1.3 ± 10 %	Nm	
Mounting torque, ± 10 % ——	terminals		threads M5 screw terminals		3 ± 10 %	INITI	
Approximate weight			See dimensions - link at the end of datasheet		54	g	
Case style					D-55 (T-m	odule)	

Note

(1) A mounting compound is recommended and the torque should be rechecked after a period of about 3 hours to allow for the spread of the compound

DEVICES	SINUSC	DIDAL COM	DUCTION	AT T _J MA	XIMUM	RECTA	NGULAR CO	ONDUCTIO	N AT T _J MA	XIMUM	UNITS
DEVICES	180°	120°	90°	60°	30°	180°	120°	90 °	60°	30 °	UNITS
T20HF	0.29	0.34	0.43	0.64	1.10	0.20	0.35	0.47	0.67	1.11	K/W

Note

• Table shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

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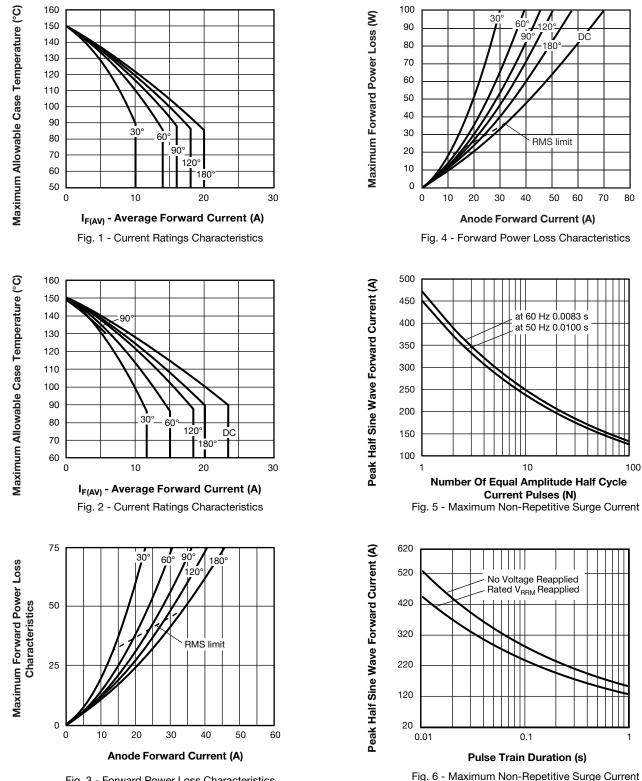


Fig. 3 - Forward Power Loss Characteristics

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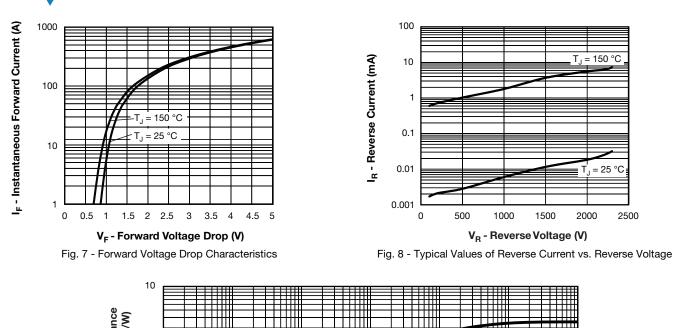
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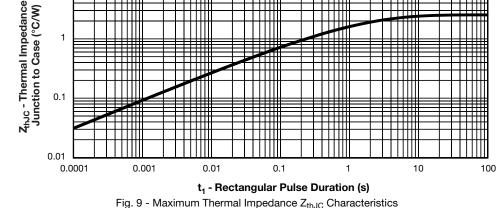
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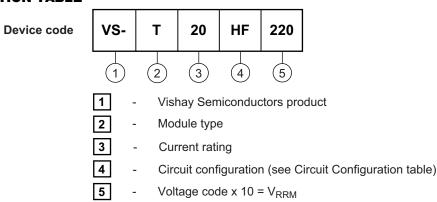
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CIRCUIT CONFIGURATION						
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Single diode	HF	2 0 0 1				

LINKS TO RELATED DOCUMENTS					
Dimensions	ww	w.vishay.com/doc?95313			
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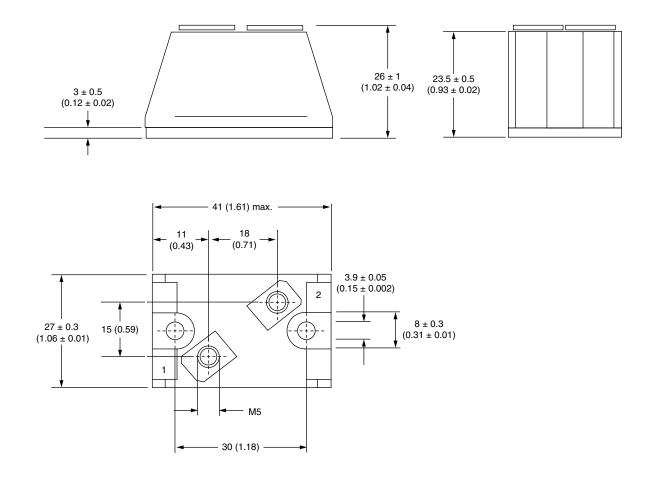
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D-55 T-Module Diode Standard and Fast Recovery

DIMENSIONS in millimeters (inches)



Note

1 = Anode

2 = Cathode



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