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Vishay Semiconductors

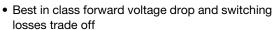
# FRED Pt® Gen 5 Hyperfast Rectifier Diode, 1200 V, 60 A



PRIMARY CHARACTERISTICS						
V <sub>R</sub>	1200 V					
V <sub>F</sub> (typical) at 30 A, per diode	2.91 V					
t <sub>rr</sub> (typical) at 30 A, per diode	41 ns					
I <sub>F(DC)</sub> per module at T <sub>C</sub> = 85 °C	60 A					
Package	SOT-227					
Circuit configuration	Two separate diodes, parallel pin-out					

#### **FEATURES**

Hyperfast and optimized Q<sub>rr</sub>





COMPLIAN

- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and qualified for industrial level
- UL pending
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION / APPLICATIONS**

Featuring a unique combination of low conduction and switching losses, the VS-U5FX60FA120 is the right choice for high frequency converters, both soft switched / resonant. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters, and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	$V_{R}$		1200	V		
Continuous forward current per diode	I <sub>F</sub>	T <sub>C</sub> = 85 °C	30	۸		
Single pulse forward current per diode	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	180	А		
Maximum power dissipation per module	P <sub>D</sub>	T <sub>C</sub> = 85 °C	164	W		
RMS isolation voltage	V <sub>ISOL</sub>	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP.		MAX.	UNITS	
Cathode to anode breakdown voltage	$V_{BR}$	I <sub>R</sub> = 100 μA	1200	-	-	
Forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 30 A	-	2.91	3.57	V
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	2.19	-	
		V <sub>R</sub> = 1200 V	-	0.5	60	
Reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = 1200 V	-	60	-	μΑ
		T <sub>J</sub> = 150 °C, V <sub>R</sub> = 1200 V	-	214	-	



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Poverse receivery time		T <sub>J</sub> = 25 °C		-	41	-	20
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C	$I_F = 30 \text{ A},$ $di_F/dt = 1000 \text{ A/}\mu\text{s},$ $V_R = 800 \text{ V}$	-	68		ns
Dools recovery assured		T <sub>J</sub> = 25 °C		-	19	-	^
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	32	-	A
Poverse recovery charge	0	T <sub>J</sub> = 25 °C		-	0.8	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	2.3	-	μC
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 1200 V, f = 1 MHz			13.4	-	pF

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance junction to case, per diode	Б		-	-	1.1	
Thermal resistance junction to case, per module	R <sub>thJC</sub>		-	-	0.55	°C/W
Thermal resistance case to heatsink, per module	R <sub>thCS</sub>	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT	-227	

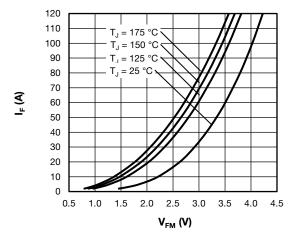


Fig. 1 - Typical Forward Voltage Drop Characteristics

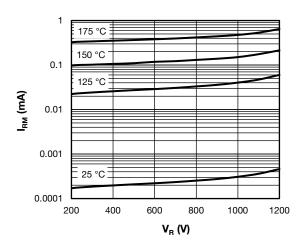


Fig. 2 - Typical Values of Reverse Current

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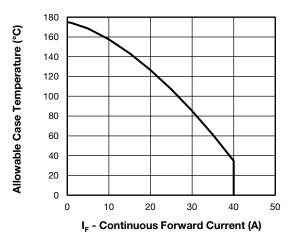


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

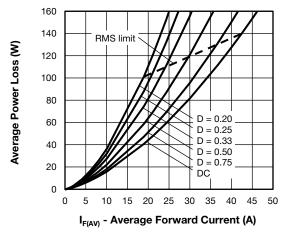


Fig. 4 - Average Power Loss vs. Average Forward Current

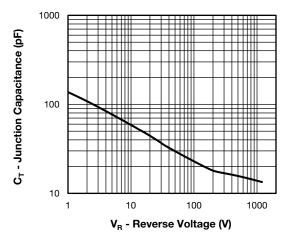


Fig. 5 - Junction Capacitance vs. Reverse Voltage

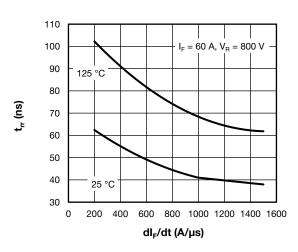


Fig. 6 - Diode Reverse Recovery Time vs. dI<sub>F</sub>dt

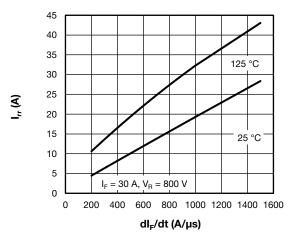


Fig. 7 - Diode Reverse Recovery Current vs. dl<sub>F</sub>dt

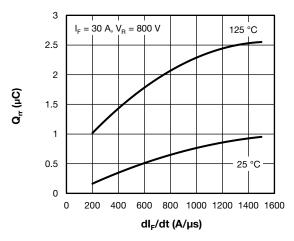


Fig. 8 - Diode Reverse Recovery Charge vs. dl<sub>F</sub>dt

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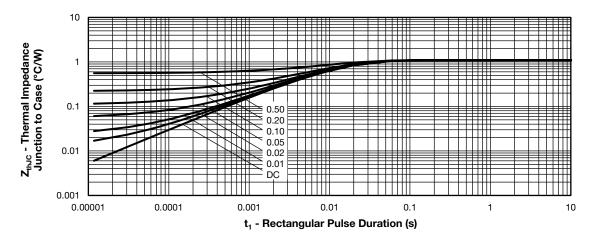


Fig. 9 - Maximum Thermal Impedance Junction to Case

### **ORDERING INFORMATION TABLE**

- 1 Vishay Semiconductors product
- U5F = Gen 5 FRED Pt® family
- X = Hyperfast FRED Pt<sup>®</sup> diode
- Current rating per module (60 = 60 A)
- F = circuit configuration (two separate diodes, parallel pin-out)
- 6 Package indicator (SOT-227 standard insulated base)
- 7 Voltage rating (120 = 1200 V)

CIRCUIT CONFI	CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Two separate diodes, parallel pin-out	F	Lead Assignment  4				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95423			
Packaging information	www.vishay.com/doc?95425			

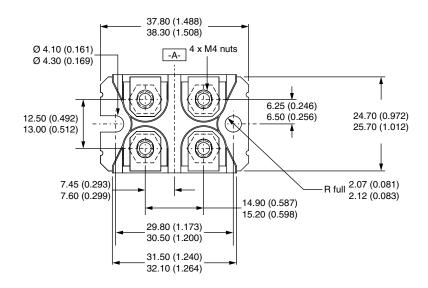
Revision: 08-Feb-2022 4 Document Number: 96946

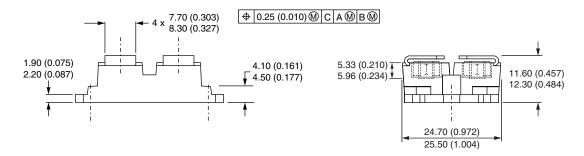


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# **SOT-227 Generation 2**

## **DIMENSIONS** in millimeters (inches)





#### Note

• Controlling dimension: millimeter



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