Insulated Ultrafast Rectifier Module, 130 A



PRIMARY CHARACTERISTICS						
V _R	200 V					
$I_{F(AV)}$ per module at $T_C = 126 \ ^{\circ}C$	130 A					
t _{rr}	32 ns					
Туре	Modules - diode FRED Pt®					
Package	SOT-227					

FEATURES

- Two fully independent diodes
- Fully insulated package
- RoHS • Ultrafast, soft reverse recovery, with high COMPLIANT operation junction temperature (T_{.1} max. = 175 °C)
- Low forward voltage drop
- · Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- Industry standard outline
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-UFB130FA20 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		200	V	
Continuous forward current per diode	l _F	T _C = 132 °C	65	•	
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	890	A	
Maximum power dissipation per module	PD	T _C = 132 °C	119	W	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	







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ELECTRICAL SPECIFICATIONS PER DIODE ($T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	200	-	-	
	V	I _F = 60 A	-	0.96	1.13	V
Forward voltage	V _{FM}	I _F = 60 A, T _J = 175 °C	-	0.75	0.89	
Reverse leakage current I _{RM}		V _R = V _R rated	-	2	50	μA
	$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	-	-	1	mA	
Junction capacitance	CT	V _R = 200 V	-	105	-	pF

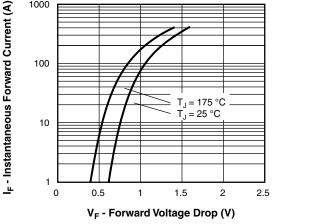
DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	32	-	
Reverse recovery time		T _J = 25 °C	I _F = 50 A dI _F /dt = 200 A/μs V _R = 100 V	-	42	-	ns
		T _J = 125 °C		-	68	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	4.0	-	A
	IRRM	T _J = 125 °C		-	9.0	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	82	-	nC
		T _J = 125 °C		-	295	-	

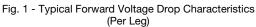
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	В		-	-	0.72	
Junction to case, both leg conducting	R _{thJC}		-	-	0.36	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.10	-	
Weight			-	30	-	g
Mounting torque	Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)	
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				S	OT-227	

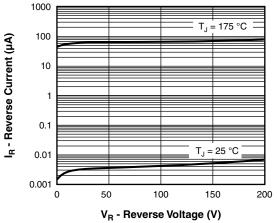
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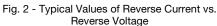
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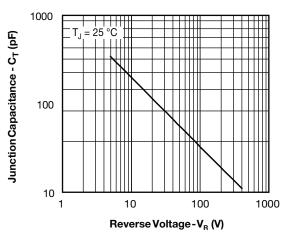
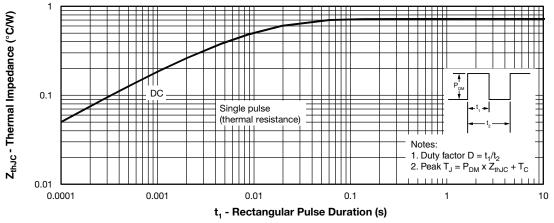
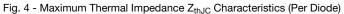


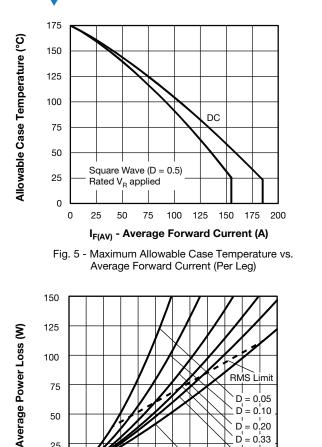
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



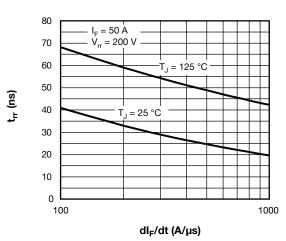


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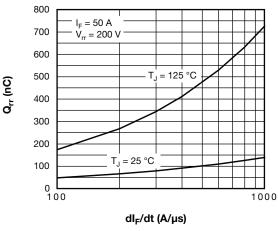


Fig. 8 - Typical Stored Charge vs. dl_F/dt

75

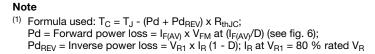
50

25

0

0

SHA



DC

10 20 30 40 50 60 70 80 90 100 110 120

I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics (Per Leg)

RMS Limit

D = 0.05

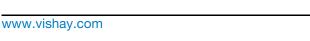
D = 0.10

D = 0.20

D = 0.33

D = 0.50

VS-UFB130FA20



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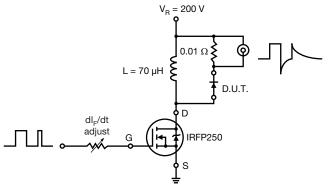
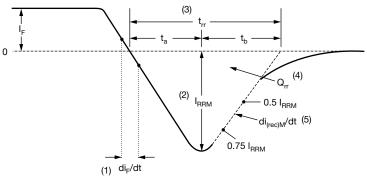


Fig. 9 - Reverse Recovery Parameter Test Circuit



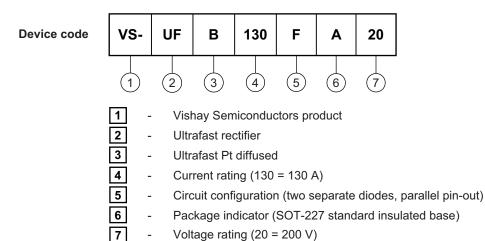
- (1) di_F/dt rate of change of current through zero crossing
- (4) ${\rm Q}_{\rm rr}$ area under curve defined by ${\rm t}_{\rm rr}$ and ${\rm I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (5) $di_{(rec)M}/dt$ peak rate of change of current during t_b portion of t_{rr}
- Fig. 10 Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE



CIRCUIT CONFIGURATION CIRCUIT CIRCUIT **CIRCUIT DRAWING CONFIGURATION CODE** Lead Assignment 4 3 O 3 $4 \cap$ Two separate diodes, F parallel pin-out () 10 $\cap 2$ 2 1

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				



SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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