## **VS-VS5HD480CW60**

Vishay Semiconductors

# FRED Pt<sup>®</sup> Gen 5, Ultrafast Rectifier Diode, 600 V, 480 A



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PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub> at 92 °C (per module)	480 A				
V <sub>R</sub>	600 V				
Q <sub>rr</sub> (typical)	525 nC				
t <sub>rr</sub>	61 ns				
Туре	Modules - diode, FRED Pt <sup>®</sup>				
Package	TO-244				
Circuit configuration	Two diodes common cathode				

### FEATURES

- Ultrafast and optimized Qrr
- Best in class forward voltage drop and switching losses trade off COMPLIANT
- Optimized for high speed operation
- 175 °C maximum operation junction temperature
- UL approved file E222165
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- · Reduced parts count

#### **DESCRIPTION / APPLICATIONS**

Featuring a unique combination of low conduction and switching losses the FRED Pt<sup>®</sup> Gen 5 is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters.

These devices are also ideally suited for HF welding, power converters, and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V <sub>R</sub>		600	V	
		T <sub>C</sub> = 25 °C	448		
Continuous forward current per diode	I <sub>F(DC)</sub>	T <sub>C</sub> = 85 °C	313		
		T <sub>C</sub> = 113 °C	240	А	
Non-repetitive single pulse forward current per diode	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	2200		
	PD	T <sub>C</sub> = 25 °C	789	W	
Maximum power dissipation per diode		T <sub>C</sub> = 113 °C	326	vv	
Storage temperature range	T <sub>Stg</sub>		-40 to +150	°C	
Operating junction temperature range	TJ		-40 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS	
Breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 400 μA	600	-	-		
Forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 240 A	-	1.5	1.7	v	
		I <sub>F</sub> = 480 A	-	1.69	2.1		
		I <sub>F</sub> = 240 A, T <sub>J</sub> = 150 °C	-	1.21	-		
		I <sub>F</sub> = 480 A, T <sub>J</sub> = 150 °C	-	1.46	-		
Reverse leakage current	I <sub>RM</sub>	$T_{\rm J} = 150 \ ^{\circ}\text{C}, \ V_{\rm R} = 600 \ \text{V}$	-	0.3	1.0	mA	
Series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		5	-	nH	
Maximum junction capacitance per leg	CT	V <sub>DC</sub> = 5 V, f = 1 MHz, 25 °C		1.2	nF		

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS			TYP.	MAX.	UNITS
		T <sub>J</sub> = 25 °C		-	61	-	
Reverse recovery time t <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	157	-	ns	
Peak recovery current I <sub>RRM</sub>	I	T <sub>J</sub> = 25 °C	$I_{\rm F} = 50  {\rm A},$	-	6.25	-	Α
	T <sub>J</sub> = 125 °C	dI <sub>F</sub> /dt = 200 A/μs, V <sub>B</sub> = 300 V	-	20.35	-		
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		-	525	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	2860	-	nC

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Thermal resistance,	per leg	P	-	-	0.19		
junction to case	per module	R <sub>thJC</sub>	-	-	0.095	°C/W	
Thermal resistance, case	to heatsink	R <sub>thCS</sub>	-	0.10	-		
			-	68	-	g	
Weight			-	2.4	-	oz.	
Mounting torque Mounting torque center hole			30 (3.4)	-	40 (4.6)		
			12 (1.4)	-	18 (2.1)	lbf ⋅ in (N ⋅ m)	
Terminal torque			30 (3.4)	-	40 (4.6)	(11 * 111)	
Vertical pull			80		80	llaf in	
2" lever pull			-	-	35	lbf ⋅ in	
Case style			TO-244				

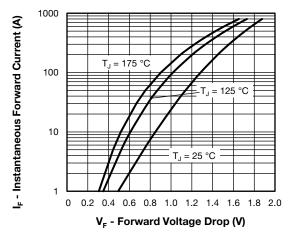


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

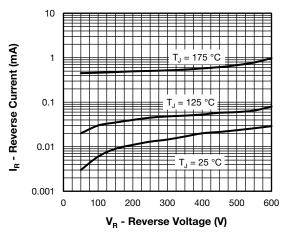
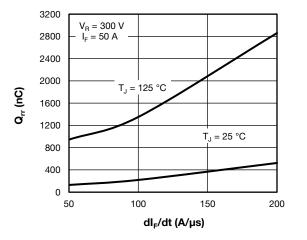


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

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Fig. 3 - Typical Reverse Recovery Charge vs dl<sub>F</sub>/dt (Per Diode)

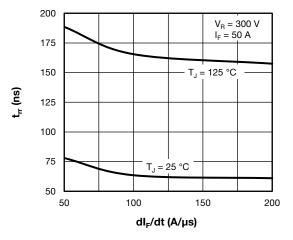


Fig. 4 - Typical Reverse Recovery Time vs dl<sub>F</sub>/dt (Per Diode)

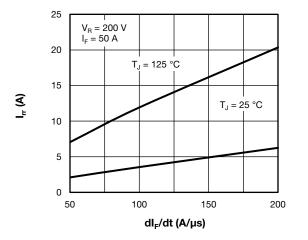


Fig. 5 - Typical Reverse Recovery Current vs dI<sub>F</sub>/dt (Per Diode)

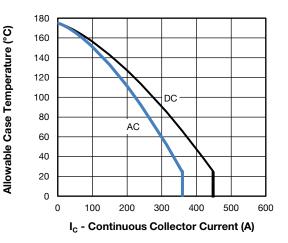


Fig. 6 - Maximum Continuous Forward Current vs. Case Temperature

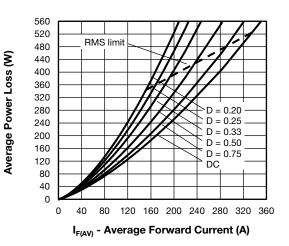


Fig. 7 - Average Power Loss vs. Average Forward Current (Forward Power Loss Characteristics)

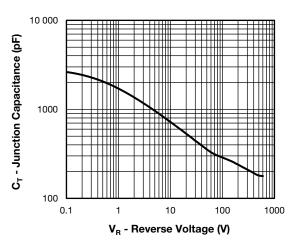


Fig. 8 - Typical Junction Capacitance vs. Reverse Voltage

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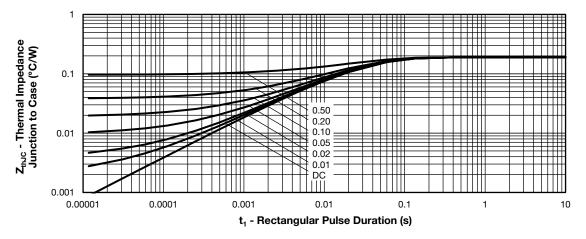


Fig. 9 - ZthJC Maximum Thermal Impedance Junction to Case vs. t1 Rectangular Pulse Duration

#### **ORDERING INFORMATION TABLE**

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**ISHAY** 

Device code	vs-vs	5HD	480	С	w	60	
		2	3	4	5	6	
	1 - 2 -		ay Semi = high s		•		5
	3 -	Curr	ent ratin	g (480 =	= 480 A	)	
	4 -	Circ	uit config	guration	:		
		C =	two diod	les com	mon cat	thode	
	5 -	VV =	TO-244	wire bo	ndable	not isola	ated
	6 -	Volta	age ratin	ig (60 =	600 V)		

CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two diodes common cathode	С	Lug terminal anode 1 terminal anode 2			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

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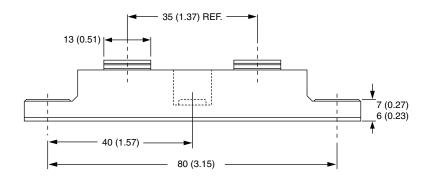
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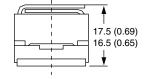


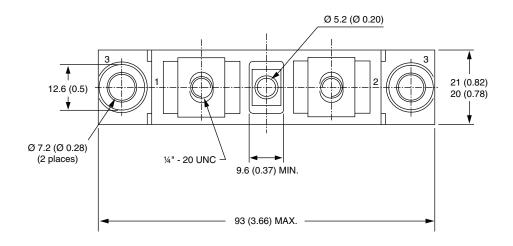
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#### **DIMENSIONS** in millimeters (inches)







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