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Vishay Semiconductors

FRED Pt® Gen 5, Ultrafast Rectifier Diode, 600 V, 600 A



PRIMARY CHARACTERISTICS					
I _{F(AV)} at 77 °C (per module)	600 A				
V_{R}	600 V				
Q _{rr} (typical)	800 nC				
t _{rr}	78 ns				
Type	Modules - diode, FRED Pt®				
Package	TO-244				
Circuit configuration	Two diodes common cathode				

FEATURES

- Ultrafast and optimized Q_{rr}
- Best in class forward voltage drop and switching losses trade off



- Optimized for high speed operation
- 175 °C maximum operation junction temperature
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- · Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- · Reduced parts count

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses the FRED Pt® Gen 5 is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters.

These devices are also ideally suited for HF welding, power converters, and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V_R		600	V	
	I _{F(DC)}	T _C = 25 °C	505	А	
Continuous forward current per diode		T _C = 85 °C	359		
		T _C = 106 °C	271		
Non-repetitive single pulse forward current per diode	I _{FSM}	T _C = 25 °C	2200		
Maximum power dissipation per diode	P _D	T _C = 25 °C	938	10/	
		T _C = 106 °C	431	W	
Storage temperature range	T _{Stg}		-40 to +150	°C	
Operating junction temperature range	TJ		-40 to +175	°C	

ELECTRICAL SPECIFICATIONS PER LEG (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage	V_{BR}	I _R = 400 μA	600	-	-		
Forward voltage	V _{FM}	I _F = 300 A	-	1.34	1.70		
		I _F = 600 A	-	1.52	2.25	V	
		I _F = 300 A, T _J = 150 °C	-	1.11	-		
		I _F = 600 A, T _J = 150 °C	-	1.35	-		
Reverse leakage current	I _{RM}	$T_J = 150 ^{\circ}\text{C}, V_R = 600 ^{\circ}\text{V}$	-	0.4	1.0	mA	
Series inductance	L _S	From top of terminal hole to mounting plane	-	5	=.	nH	
Maximum junction capacitance per leg	C _T	V _{DC} = 5 V, f = 1 MHz, 25 °C	-	-	1.8	nF	

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS			TYP.	MAX.	UNITS
Devenue ve estrem times	leverse recovery time t _{rr}	T _J = 25 °C		-	78	-	ns
Reverse recovery time		T _J = 125 °C	$I_F = 50 \text{ A},$ $dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_R = 300 \text{ V}$	-	193	-	
De als sea a servicio de la companya	eak recovery current I _{RRM}	T _J = 25 °C		-	9.0	-	А
Peak recovery current		T _J = 125 °C		-	25	-	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	880	-	nC
	Q _{rr}	T _J = 125 °C		-	4000	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Thermal resistance,	per leg	Б	-	-	0.16	°C/W	
junction to case	per module	R _{thJC}	-	-	0.08		
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-		
Weight			-	68	-	g	
			-	2.4	-	oz.	
Mounting torque Mounting torque center hole			30 (3.4)	-	40 (4.6)		
			12 (1.4)	-	18 (2.1)	lbf · in (N · m)	
Terminal torque			30 (3.4)	-	40 (4.6)	(14 · 111)	
Vertical pull	ertical pull		-	-	80	llaf in	
2" lever pull			-	-	35	- lbf ⋅ in	
Case style			TO-244				



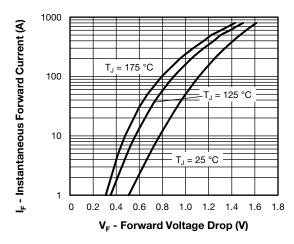


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

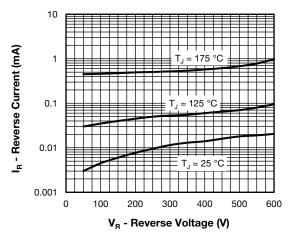


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

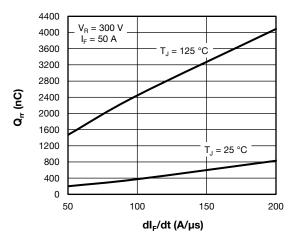


Fig. 3 - Typical Reverse Recovery Charge vs dl_F/dt (Per Diode)

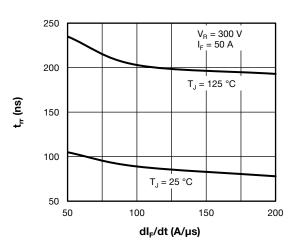


Fig. 4 - Typical Reverse Recovery Time vs dl_F/dt (Per Diode)

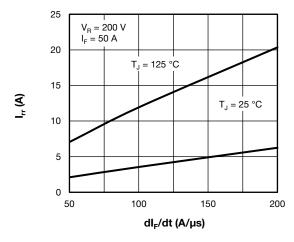


Fig. 5 - Typical Reverse Recovery Current vs dI_F/dt (Per Diode)

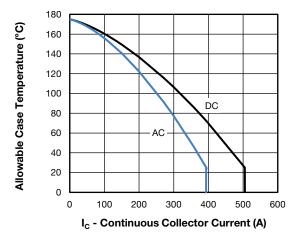


Fig. 6 - Maximum Continuous Forward Current vs. Case Temperature



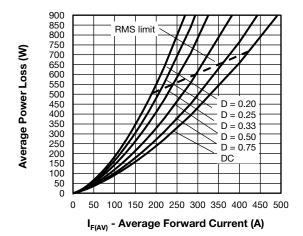


Fig. 7 - Average Power Loss vs. Average Forward Current (Forward Power Loss Characteristics)

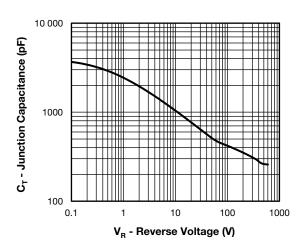


Fig. 8 - Typical Junction Capacitance vs. Reverse Voltage

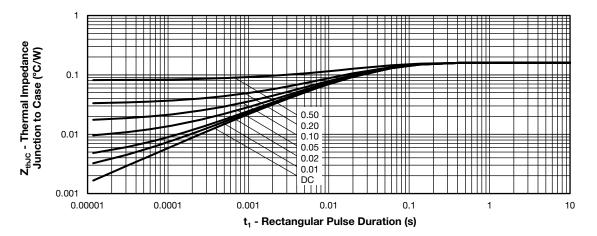
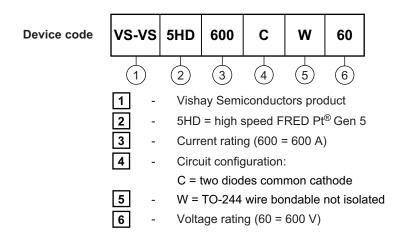


Fig. 9 - Z_{thJC} Maximum Thermal Impedance Junction to Case vs. t₁ Rectangular Pulse Duration

ORDERING INFORMATION TABLE

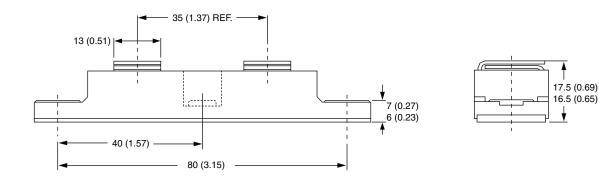


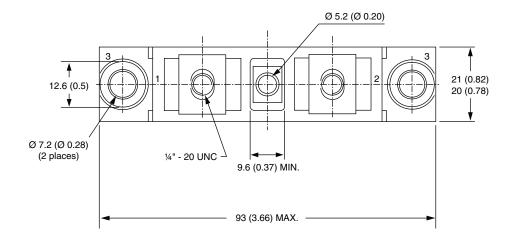


CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two diodes common cathode	С	Lug Lug terminal terminal anode 1 anode 2 Base common cathode			

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95021

DIMENSIONS in millimeters (inches)







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