

AAP Gen 7 (TO-240AA) Power Modules Schottky Rectifier, 200 A



PRIMARY CHARACTERISTICS			
I _{F(AV)}	200 A		
V_{R}	150 V		
Package	AAP Gen 7 (TO-240AA)		
Circuit configuration	Two diodes common cathode		

MECHANICAL DESCRIPTION

The AAP Gen 7, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- · High frequency operation
- · Low thermal resistance
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- · High surge capability
- Easy mounting on heatsink

ELECTRICAL DESCRIPTION / APPLICATIONS

The VS-VSKCS209.. Schottky rectifier common cathode has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	200	A		
V _{RRM}		150	V		
I _{FSM}	t _p = 5 μs sine	11 300	А		
V _F	100 A _{pk} , T _J = 125 °C	0.85	V		
TJ	Range	-55 to +175	°C		

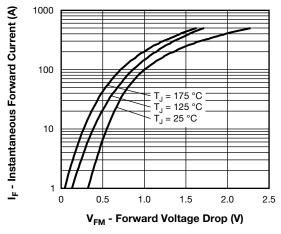
VOLTAGE RATINGS				
PARAMETER	ER SYMBOL		UNITS	
Maximum DC reverse voltage	V_{R}	150	V	
Maximum working peak reverse voltage	V_{RWM}	130	V	

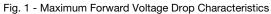


ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per module		50 % duty cycle at T _C = 113 °C, rectangular waveform		200	
forward current	per leg	I _{F(AV)}			100	
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	11 300	Α	
		10 ms sine or 6 ms rect. pulse		1600		
Non-repetitive avalanche energ	Jy	E _{AS}	T _J = 25 °C, I _{AS} = 1.8 A, L = 10 mH		15	mJ
Repetitive avalanche current		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM}	100 A	T _J = 25 °C	1.01	. V
		200 A		1.35	
		100 A	- T _J = 125 °C	0.85	
		200 A		1.13	
Maximum vayayaa laakaga ayyyaat	I _{RM}	T _J = 25 °C	V _R = Rated V _R	6	mA
Maximum reverse leakage current		T _J = 125 °C		85	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		3000	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		7.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs
Maximum RMS insulation voltage	V _{INS}	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.52	· °C/W
Typical thermal resistance, case to heatsink per module		R _{thCS}		0.1	C/VV
Approximate weight				75	g
Approximate weight				2.7	oz.
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for	4	Nm
	busbar		the spread of the compound.	3	INIII
Case style			JEDEC®	TO-240AA co	mpatible





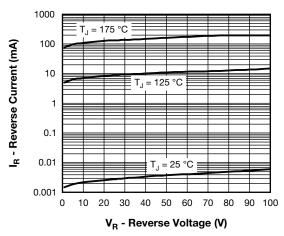


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

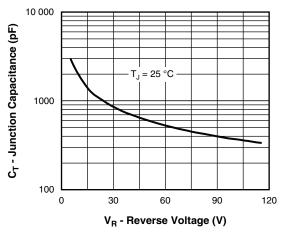


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

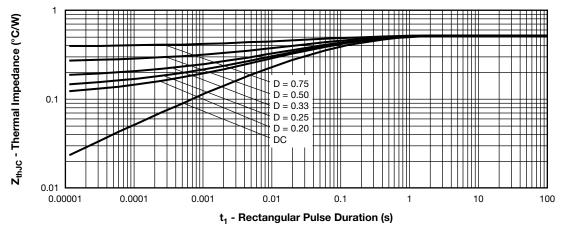


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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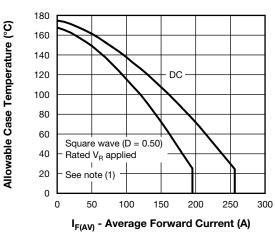
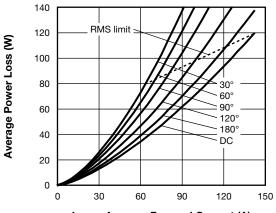
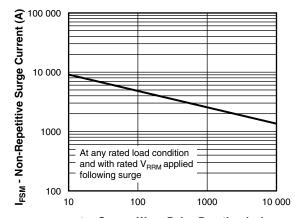


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics



t_p - Square Wave Pulse Duration (μs)
Fig. 7 - Maximum Non-Repetitive Surge Current

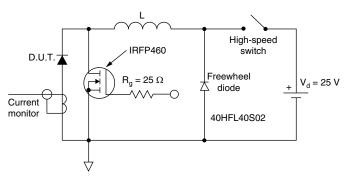


Fig. 8 - Unclamped Inductive Test Circuit

Note

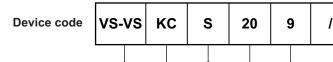
Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R



150

(6)

ORDERING INFORMATION TABLE



(2)

VS-VS = Vishay Semiconductors product

(4)

2 - Circuit configuration:

(3)

KC = ADD-A-PAK - 2 diodes common cathode

(5)

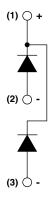
3 - S = Schottky diode

4 - Average current rating (20 = 200 A)

Product silicon identification

6 - Voltage rating (150 = 150 V)

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS		
Dimensions www.vishav.com/doc?95369		



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