

AAP Gen 7 (TO-240AA) Power Modules Schottky Rectifier, 400 A



AAP Gen 7 (TO-240AA)

PRIMARY CHARACTERISTICS			
I _{F(AV)}	400 A		
V_{R}	100 V		
Package	AAP Gen 7 (TO-240AA)		
Circuit configuration	Two diodes common cathode		

MECHANICAL DESCRIPTION

The AAP Gen 7, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation
- · Low thermal resistance
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- High surge capability
- · Easy mounting on heatsink

ELECTRICAL DESCRIPTION / APPLICATIONS

The VS-VSKCS403.. Schottky rectifier common cathode has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	400	A		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	23 000	A		
V _F	200 A _{pk} , T _J = 125 °C	0.83	V		
T _J	Range	-55 to +175	°C		

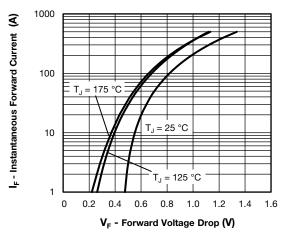
VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-VSKCS403/100	UNITS	
Maximum DC reverse voltage	V_{R}	100	V	
Maximum working peak reverse voltage	V_{RWM}	100	V	

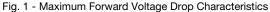


ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum average	per module		50 % duty sugle at T 104 %C restangular ways form		400	
forward current	per leg	I _{F(AV)} 50 % duty cycle at T _C = 104 °C, rectangular waveform		200		
Maximum peak one cycle		I _{FSM} -	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	23 000	A
non-repetitive surge current			10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	2600	
Non-repetitive avalanche energ	у	E _{AS}	T _J = 25 °C, I _{AS} = 5.5 A, L = 1 mH		15	mJ
Repetitive avalanche current		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM}	200 A	T _J = 25 °C	0.99	V
Maximum forward voltage drop		400 A		1.3	
Maximum forward voltage drop		200 A	T _J = 125 °C	0.83	
		400 A		1.09	
Maximum rayaraa laakaga aurrant	I _{RM}	T _J = 25 °C	V _R = Rated V _R	6	mA
Maximum reverse leakage current		T _J = 125 °C		120	IIIA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		5500	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs
Maximum RMS insulation voltage	V _{INS}	50 Hz		3000 (1 min) 3600 (1 s)	٧

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C	
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.32	°C/W	
Typical thermal resistance, case to heatsink per module		R _{thCS}		0.1		
Approximate weight				75	g	
Approximate weight				2.7	oz.	
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 h to allow for the	4	Nm	
busbar		spread of the compound.	3	INIII		
Case style	•		JEDEC®	TO-240AA co	ompatible	





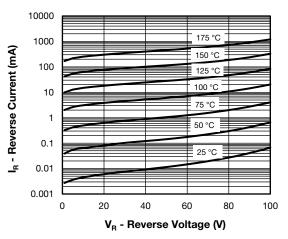


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

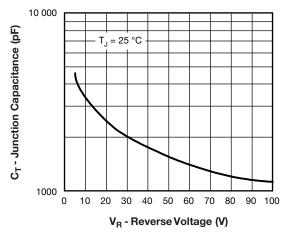


Fig. 3 - Typical Junction Capacitance vs.Reverse Voltage

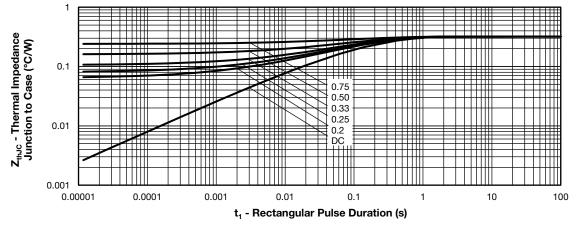


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

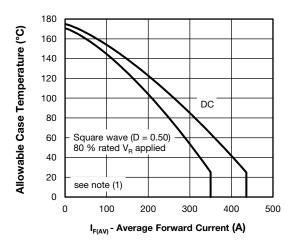


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

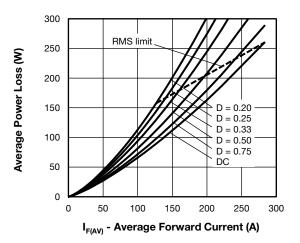


Fig. 6 - Forward Power Loss Characteristics

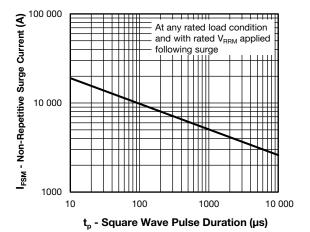


Fig. 7 - Maximum Non-Repetitive Surge Current

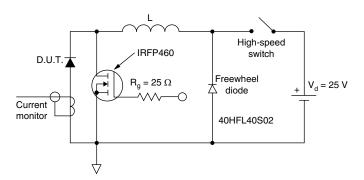


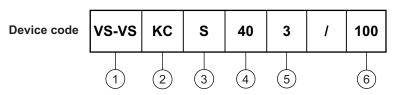
Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R



ORDERING INFORMATION TABLE



- VS-VS = Vishay Semiconductors product

2 - Circuit configuration:

KC = ADD-A-PAK - 2 diodes / common cathode

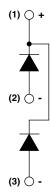
3 - S = Schottky diode

4 - Average rating (x 10)

5 - Product silicon identification

6 - Voltage rating (100 = 100 V)

CIRCUIT CONFIGURATION

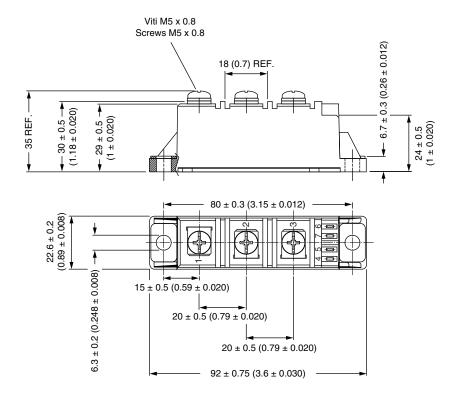


LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95369		



ADD-A-PAK Generation VII - Diode

DIMENSIONS in millimeters (inches)





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