VIS30940
30V N-Channel SGT MOSFET

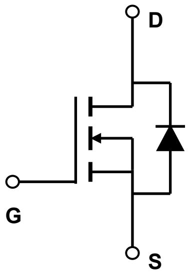
General Description
- SGT MOSFET Technology
- Low $R_{DS(ON)}$ at 4.5V V_{GS}
- Low Gate Charge
- High Current Capability
- RoHS and Halogen-Free Compliant

Applications
- General DC/DC Converters
- VRM Vcore for Notebook and Server
- Load Switch and Battery Power Management
- Motor Drive Bridge Switch

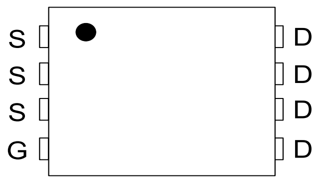
Product Summary

V_{DS}		30V
I_D	(at V_{GS}=10V)	32A
$R_{DS(ON)}$	(at V_{GS}=10V, typ)	4.6mΩ
$R_{DS(ON)}$	(at V_{GS}=4.5V, typ)	5.8mΩ

100% UIS Tested
100% R_g Tested

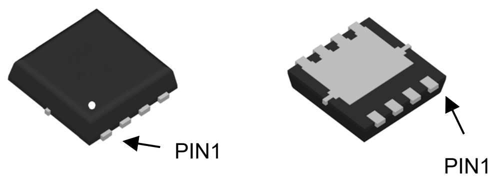


Top View



DFN3.3X3.3

Top View Bottom View



Orderable Part Number	Package Type	Form	Minimum Order Quantity
VIS30940	DFN3.3x3.3	Tape & Reel	5000

Absolute Maximum Ratings T_A=25°C unless otherwise noted

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current (5)	T_C=25°C	I_D	32	A
	T_C=100°C		32	A
Pulsed Drain Current (3)		I_{DM}	150	A
Continuous Drain Current	T_A=25°C	I_{DSM}	20	A
	T_A=100°C		12.5	A
Avalanche Current (3)		I_{AS}	33	A
Avalanche Energy L=0.1mH (3)		E_{AS}	54	mJ
Power Dissipation (2)	T_C=25°C	P_D	26	W
	T_C=100°C		10.4	W
Power Dissipation (1)	T_A=25°C	P_{DSM}	3.1	W
	T_A=100°C		1.25	W
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient (1)	t ≤ 10s	$R_{\theta JA}$	30	40	°C/W
Maximum Junction-to-Ambient (1,4)	Steady-State		60	75	°C/W
Maximum Junction-to-Case	Steady-State	$R_{\theta JC}$	4	4.8	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ	Max	Units
STATIC PARAMETERS							
BV_{DSS}	Drain-Source Breakdown Voltage	I_D=250μA, V_{GS}=0V		30			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=30V, V_{GS}=0V				1	μA
			T_J=55°C			5	
I_{GSS}	Gate-Body leakage current	V_{DS}=0V, V_{GS}=±20V				±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=$V_{GS,}$ I_D=250μA		1.4	1.7	2.3	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V, I_D=20A			4.6	5.5	mΩ
			T_J=125°		6.5		
		V_{GS}=4.5V, I_D=20A			5.8	7.5	
g_{FS}	Forward Transconductance	V_{DS}=5V, I_D=20A			83		S
V_{SD}	Diode Forward Voltage	I_S=1A, V_{GS}=0V			0.72		V
I_S	Maximum Body-Diode Continuous Current					34	A
DYNAMIC PARAMETERS							
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=15V, f=1MHz			1575		pF
C_{oss}	Output Capacitance				450		pF
C_{rss}	Reverse Transfer Capacitance				35		pF
R_g	Gate resistance	f=1MHz			2.5		Ω
SWITCHING PARAMETERS							
Q_g(10V)	Total Gate Charge	V_{GS}=10V, V_{DS}=15V, I_D=20A			23.2		nC
Q_g(4.5V)	Total Gate Charge				10.9		nC
Q_{gs}	Gate Source Charge				6.9		nC
Q_{gd}	Gate Drain Charge				1.5		nC
$t_{D(on)}$	Turn-On Delay Time	V_{GS}=10V, V_{DS}=15V, R_L=0.75Ω, R_{GEN}=3Ω			8		ns
t_r	Turn-On Rise Time				5		ns
$t_{D(off)}$	Turn-Off Delay Time				12		ns
t_f	Turn-Off Fall Time				5		ns
t_{rr}	Body Diode Reverse Recovery Time	I_F=20A, di/dt=200A/μs			22		ns
Q_{rr}	Body Diode Reverse Recovery Charge	I_F=20A, di/dt=200A/μs			20		nC

1) $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The Power dissipation P_{DSM} is based on $R_{\theta JA}$ t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
2) The power dissipation P_D is based on $T_{J(MAX)}$=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
3) Single pulse width limited by junction temperature $T_{J(MAX)}$=150°C.
4) $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JC}$ and case to ambient.
5) The maximum current rating is package limited.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

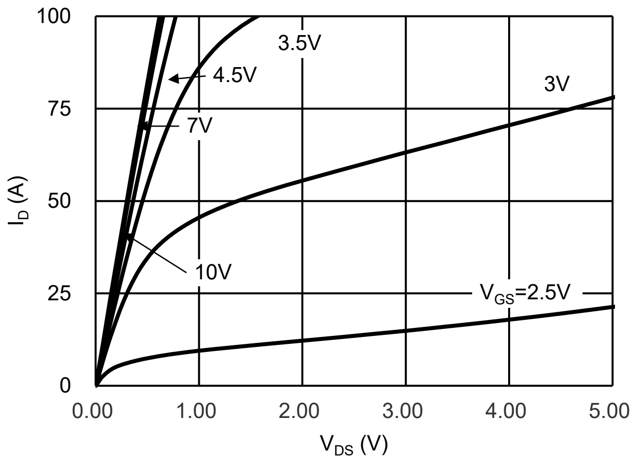


Fig 1. Typical Output Characteristics

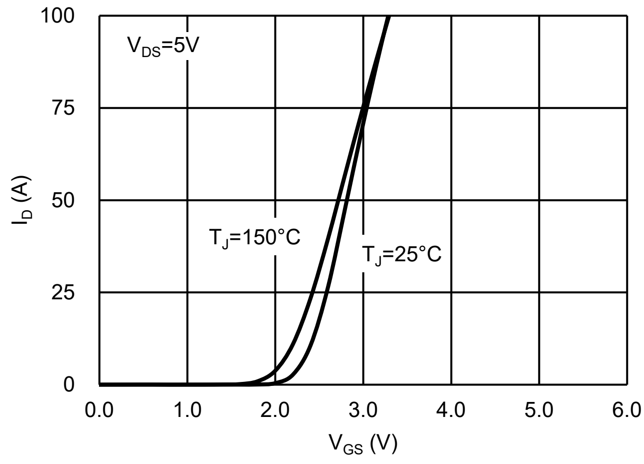


Fig 2. Typical Transfer Characteristics

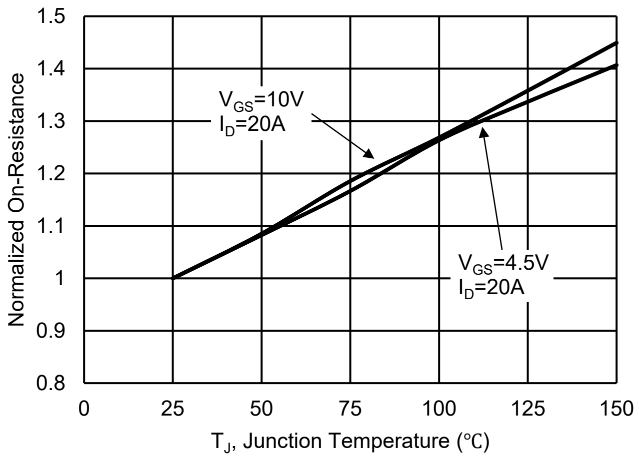


Fig 3. Normalized On-Resistance vs. Temperature

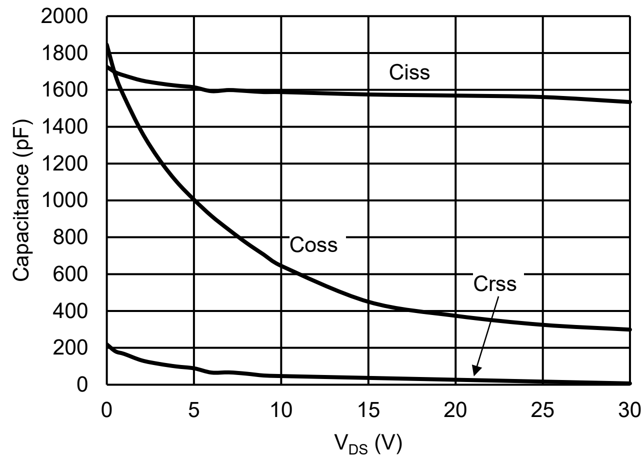


Fig 4. Typical Capacitance vs. V_{DS}

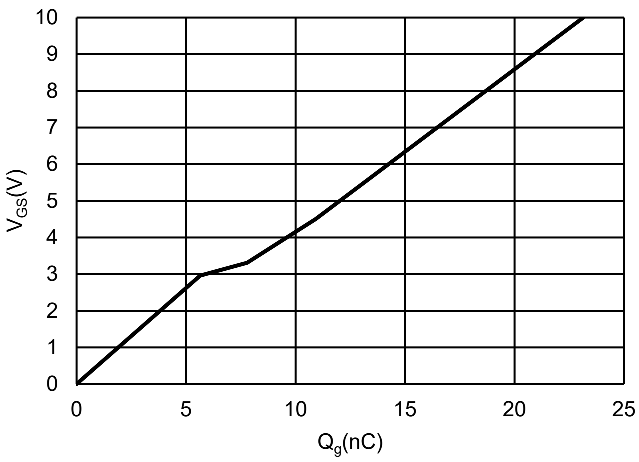


Fig 5. Typical Gate Charge vs. V_{GS}

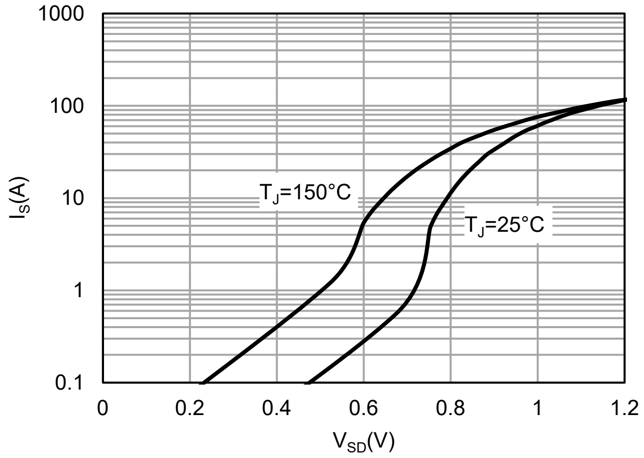


Fig 6. Typical Source-Drain Diode Forward Voltage

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

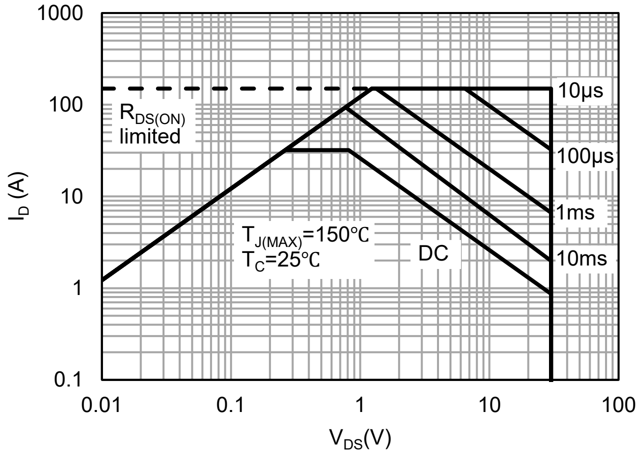


Fig 7. Maximum Safe Operating Area

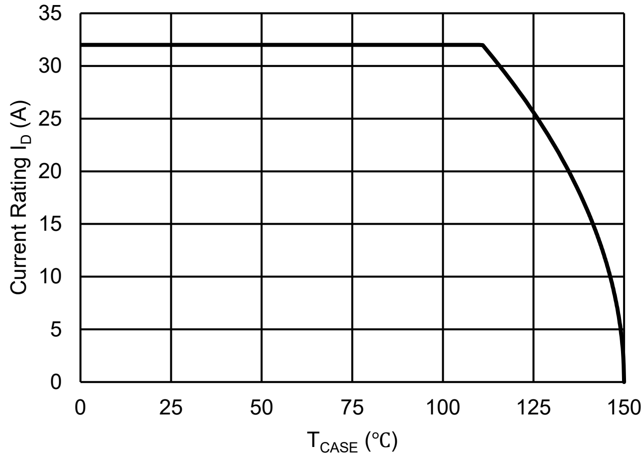


Fig 8. Maximum Drain Current vs. Case Temperature

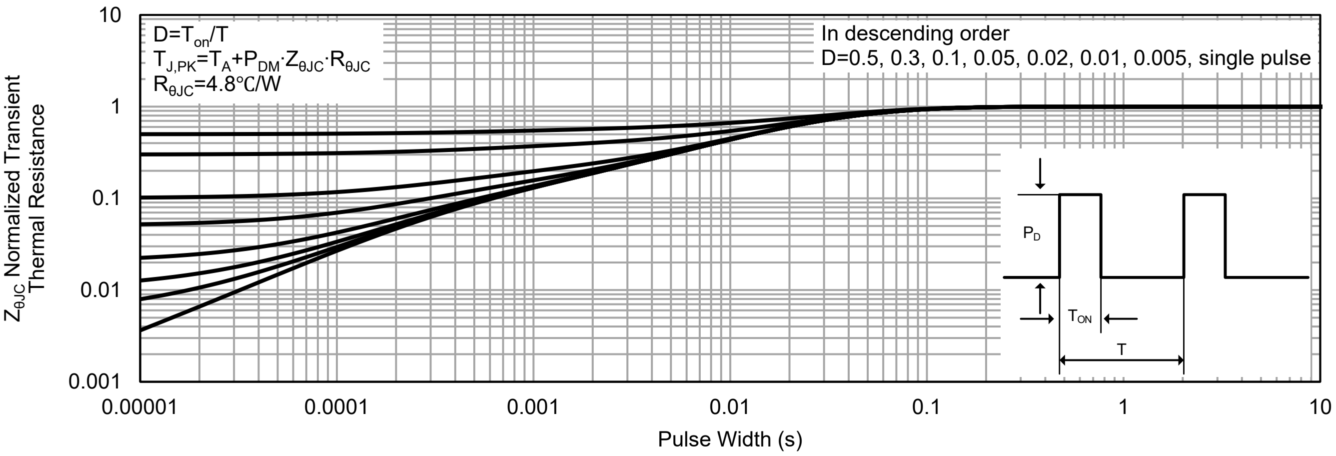


Fig 9. Normalized Maximum Transient Thermal Impedance, Junction-to-Case

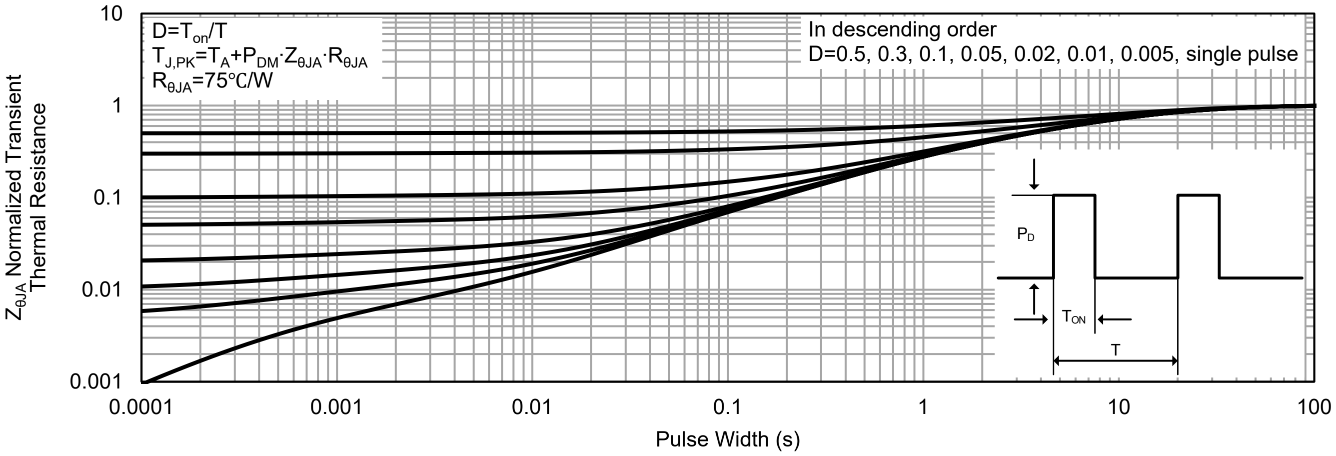


Fig 10. Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

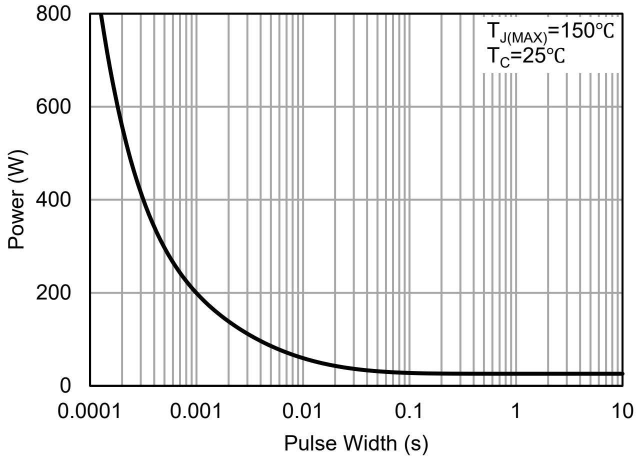


Fig 11. Single Pulse Power Rating Junction-to-Case

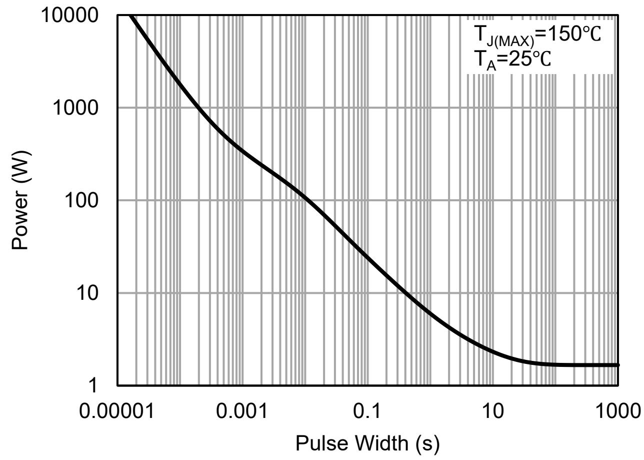


Fig 12. Single Pulse Power Rating Junction-to-Ambient

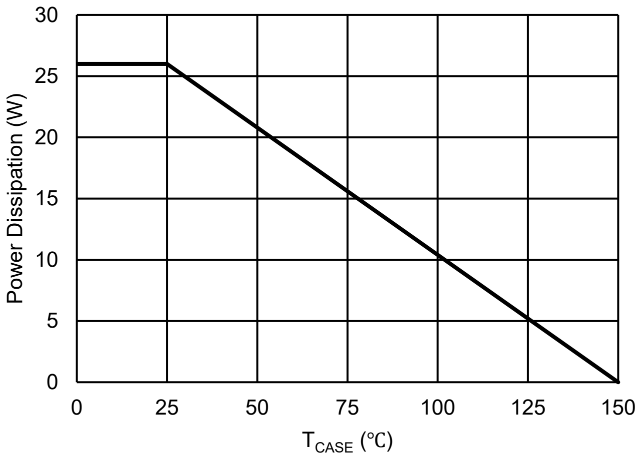


Fig 13. Maximum Power Rating vs. Temperature

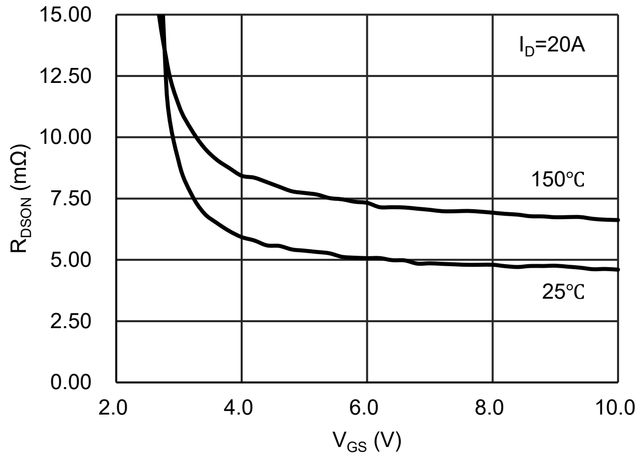


Fig 14. On-Resistance vs. V_{GS}

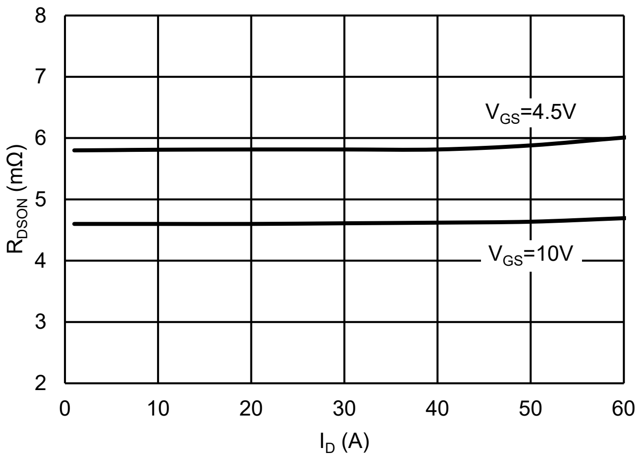


Fig 15. On-Resistance vs. Drain Current

TEST CIRCUIT

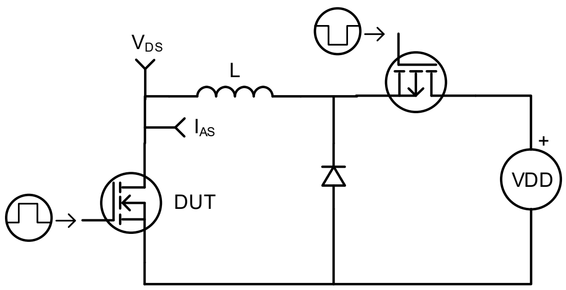


Fig16. Unclamped Inductive Test Circuit

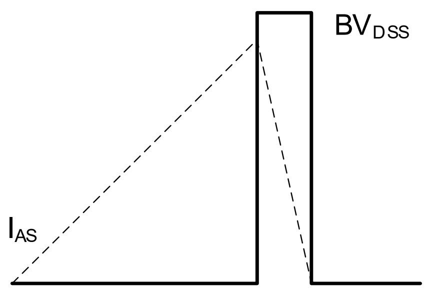


Fig17. Unclamped Inductive Waveform

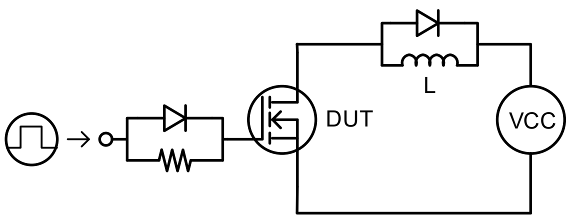


Fig18. Q_g Test Circuit

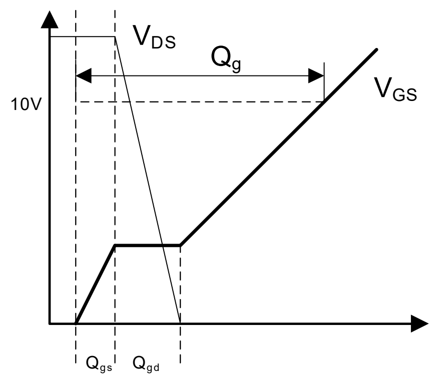


Fig19. Q_g Waveform

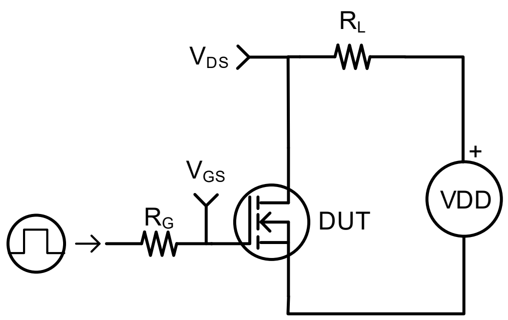


Fig18. Resistive Switching Test Circuit

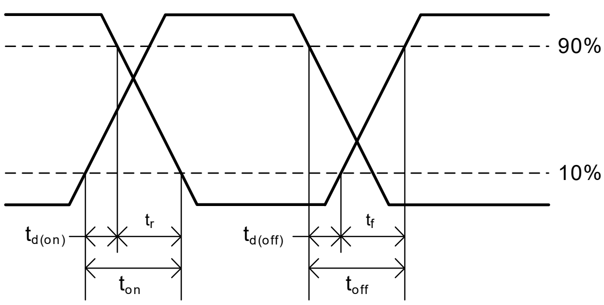


Fig19. Switching Time Waveform

TEST CIRCUIT

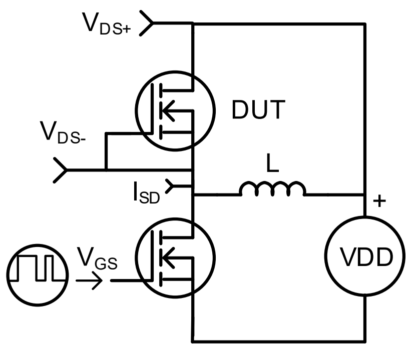


Fig20. Diode Recovery Test Circuit

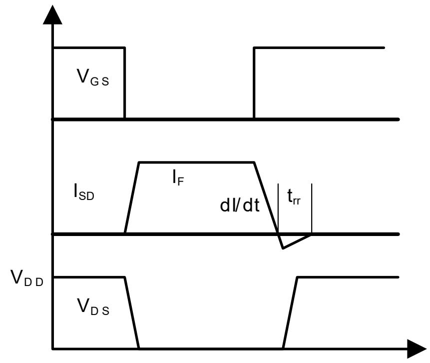
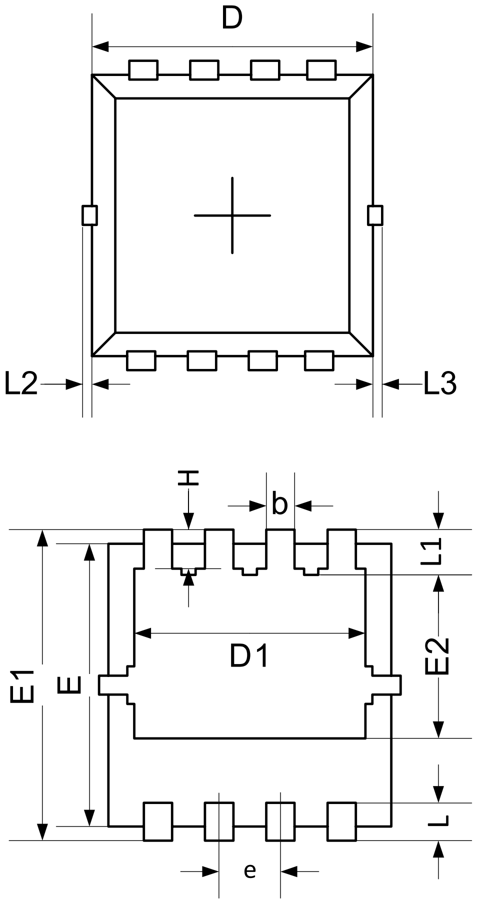
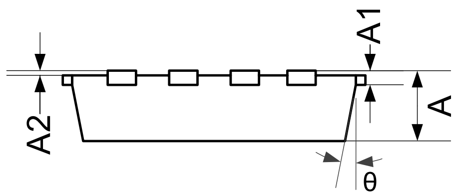


Fig21. Diode Recovery Test Waveform

DFN3.3x3.3 OUTLINE





SYMBOL \ DIM	MILLIMITERS	
	MIN [mm]	MAX [mm]
A	0.650	0.850
A1	0.152 REF	
A2	0~0.05	
D	2.900	3.100
D1	2.300	2.600
E	2.900	3.100
E1	3.150	3.450
E2	1.535	1.935
b	0.200	0.400
e	0.550	0.750
L	0.300	0.500
L1	0.180	0.480
L2	0~0.100	
L3	0~0.100	
H	0.315	0.515
θ	9°	13°